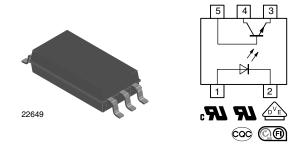


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Vishay Semiconductors

# Optocoupler, Phototransistor Output, SOP-6L5, Half Pitch, Long Mini-Flat Package



#### **LINKS TO ADDITIONAL RESOURCES**







#### **DESCRIPTION**

The TCLT110. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 5-lead SOP-6L package.

#### **APPLICATIONS**

- Switchmode power supplies
- Computer peripheral interface
- Microprocessor system interface

### **FEATURES**

- SMD low profile 5 pin package
- Isolation test voltage 5000 V<sub>RMS</sub>
- CTR flexibility available see order information
- · Special construction
- Extra low coupling capacitance
- · Connected base
- · DC input with transistor output
- Creepage distance > 8 mm
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

# Pb-free



RoHS COMPLIANT

FREE GREEN

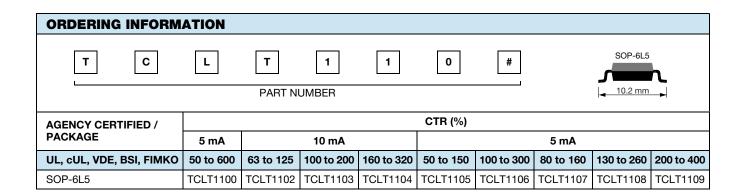
## (5-2008)

## **AGENCY APPROVALS**

- <u>UL</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)
- BSI
- FIMKO
- CQC GB4943.1
- CQC GB8898

## Note

 See the safety standard approval list "Agency Table" for more detailed information





<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		V <sub>R</sub>	6	V				
Forward current		I <sub>F</sub>	60	mA				
Forward surge current	t <sub>P</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	Α				
Power dissipation		P <sub>diss</sub>	100	mW				
Junction temperature		Tj	125	°C				
OUTPUT								
Collector emitter voltage		V <sub>CEO</sub>	80	V				
Emitter collector voltage		V <sub>ECO</sub>	7	V				
Collector current		I <sub>C</sub>	50	mA				
Collector peak current	$t_P/T = 0.5, t_P \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA				
Power dissipation		P <sub>diss</sub>	150	mW				
Junction temperature		Tj	125	°C				
COUPLER								
Isolation test voltage (RMS)		V <sub>ISO</sub>	5000	$V_{RMS}$				
Total power dissipation		P <sub>tot</sub>	250	mW				
Operating ambient temperature range		T <sub>amb</sub>	-55 to +100	°C				
Storage temperature range		T <sub>stg</sub>	-55 to +125	°C				
Soldering temperature (1)		T <sub>sld</sub>	260	°C				

#### Notes

<sup>(1)</sup> Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (www.vishay.com/doc?80054).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT									
Forward voltage	$I_F = \pm 50 \text{ mA}$	V <sub>F</sub>	-	1.25	1.6	V			
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>j</sub>	-	50	-	pF			
OUTPUT	OUTPUT								
Collector emitter voltage	I <sub>C</sub> = 1 mA	$V_{CEO}$	70	-	-	V			
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V			
Collector emitter leakage current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>	-	10	100	nA			
COUPLER									
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>	-	-	0.3	V			
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>	-	110	-	kHz			
Coupling capacitance	f = 1 MHz	C <sub>k</sub>	-	0.3	-	pF			

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<sup>•</sup> Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.



CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCLT1100	CTR	50	1	600	%	
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 10 mA	TCLT1102	CTR	63	-	125	%	
		TCLT1103	CTR	100	ı	200	%	
I <sub>C</sub> /I <sub>F</sub>		TCLT1104	CTR	160	1	320	%	
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 1 mA	TCLT1102	CTR	22	45	-	%	
		TCLT1103	CTR	34	70	-	%	
		TCLT1104	CTR	56	100	-	%	
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 5 mA	TCLT1105	CTR	50	-	150	%	
		TCLT1106	CTR	100	-	300	%	
		TCLT1107	CTR	80	1	160	%	
		TCLT1108	CTR	130	-	260	%	
		TCLT1109	CTR	200	-	400	%	

SAFETY AND INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	2.0	-	-	kV	
Partial discharge test voltage -	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$	V <sub>IOTM</sub>	8	-	-	kV	
lot test (sample test)	(see Fig. 2)	$V_{pd}$	1.68	-	-	kV	
Insulation resistance	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>	-	-	Ω	
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	10 <sup>11</sup>	-	-	Ω	
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>	-	-	Ω	
Forward current		I <sub>si</sub>	130	-	-	mA	
Power dissipation		P <sub>so</sub>	265	-	-	mW	
Rated impulse voltage		V <sub>IOTM</sub>	8	-	-	kV	
Safety temperature		T <sub>si</sub>	150	-	-	°C	
Clearance distance			8.0	-	-	mm	
Creepage distance			8.0	-	-	mm	
Insulation distance (internal)			0.40	-	-	mm	

### Note

According to DIN EN 60747-5-2 (VDE 0884) (see Fig. 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings.
Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

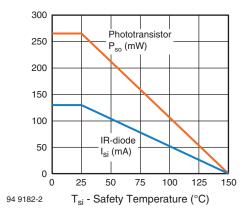


Fig. 1 - Derating Diagram

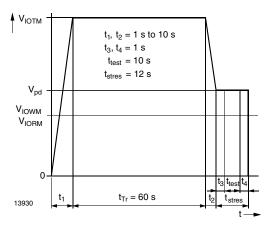


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-2 (VDE 0884); IEC 60747-5-5



<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see Fig. 3)	t <sub>d</sub>	-	3.0	-	μs	
Rise time	$V_S = 5$ V, $I_C = 2$ mA, $R_L = 100$ $\Omega$ , (see Fig. 3)	t <sub>r</sub>	-	3.0	-	μs	
Turn-on time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see Fig. 3)	t <sub>on</sub>	-	6.0	-	μs	
Storage time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see Fig. 3)	t <sub>s</sub>	-	0.3	-	μs	
Fall time	$V_S = 5$ V, $I_C = 2$ mA, $R_L = 100$ $\Omega$ , (see Fig. 3)	t <sub>f</sub>	-	4.7	-	μs	
Turn-off time	$V_S = 5 \text{ V}, \ I_C = 2 \text{ mA}, \ R_L = 100 \ \Omega,$ (see Fig. 3)	t <sub>off</sub>	-	5.0	-	μs	
Turn-on time	$V_S = 5$ V, $I_F = 10$ mA, $R_L = 1$ k $\Omega$ , (see Fig. 4)	t <sub>on</sub>	-	9.0	-	μs	
Turn-off time	$V_S = 5$ V, $I_F = 10$ mA, $R_L = 1$ k $\Omega$ , (see Fig. 4)	t <sub>off</sub>	-	10.0	-	μs	

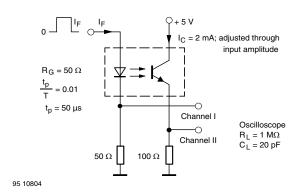


Fig. 3 - Test Circuit, Non-Saturated Operation

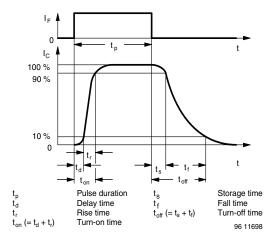


Fig. 5 - Switching Times

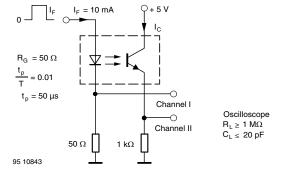
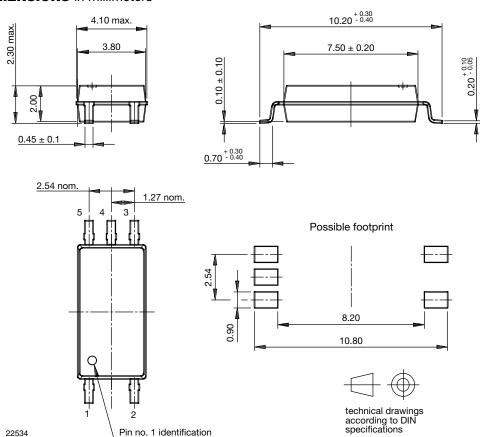
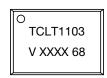


Fig. 4 - Test Circuit, Saturated Operation

## **PACKAGE DIMENSIONS** in millimeters



## **PACKAGE MARKING** (example)



## Note

• XXXX = LMC (lot marking code)

#### TAPE AND REEL DIMENSIONS in millimeters

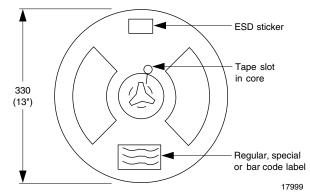


Fig. 6 - Reel Dimensions (3000 units per reel)

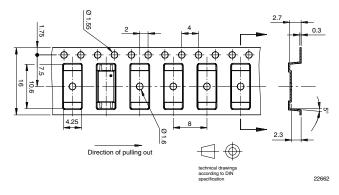


Fig. 7 - Tape Dimensions



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