

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

## TD62064P,TD62064AP,TD62064F,TD62064AF TD62074P,TD62074AP,TD62074F,TD62074AF

### 4CH HIGH-CURRENT DARLINGTON SINK DRIVER

The TD62064P / AP / F / AF and TD62074P / AP / F / AF are high-voltage, high-current darlington drivers comprised of four NPN darlington pairs.

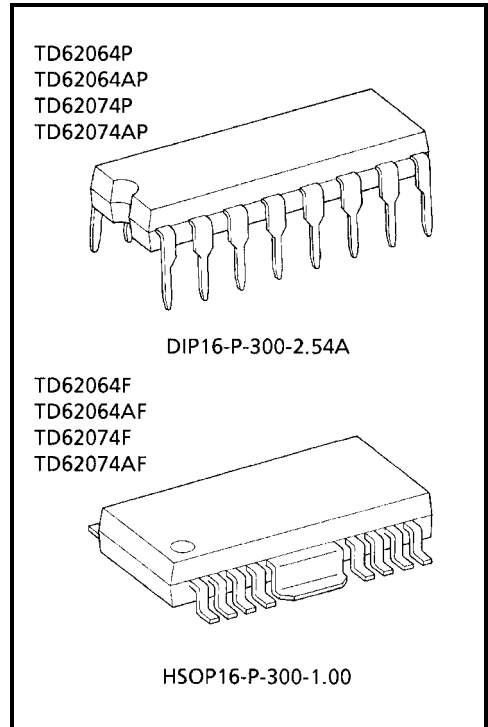
All units feature integral clamp diodes for switching inductive loads and all units of TD62074P / AP / F / AF feature uncommitted collectors and emitters for isolated darlington applications.

For proper operation, the substrate (SUB) must be connected to the most negative voltage.

Applications include relay, hammer, lamp and stepping motor drivers.

### FEATURES

- Output current (single output) 1.5 A (Max)
- High sustaining voltage output  
35 V (Min) (TD62064P / F, 074P / F)  
50 V (Min) (TD62064AP / AF, 074AP / AF)
- Output clamp diodes : TD62064P / AP / F / AF
- Isolated darlington array : TD62074P / AP / F / AF
- Input compatible with TTL and 5 V CMOS
- GND and SUB terminal = heat sink
- Package type-P, AP: DIP-16 pin
- Package type-F, AF: HSOP-16 pin



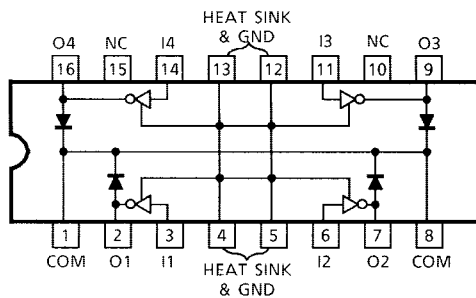
Weight

DIP16-P-300-2.54A : 1.11 g (Typ.)

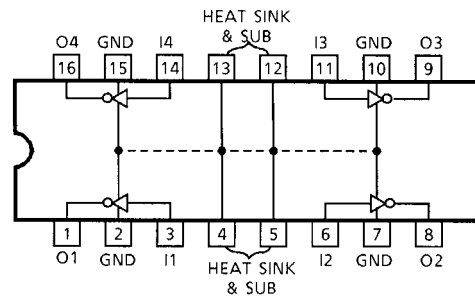
HSOP16-P-300-1.00 : 0.50 g (Typ.)

### PIN CONNECTION (TOP VIEW)

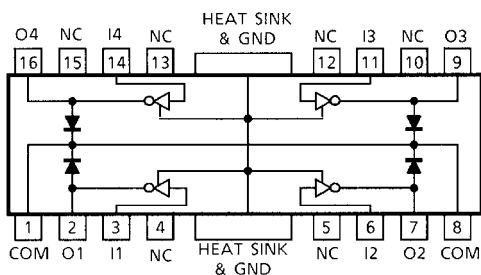
#### TD62064P / AP



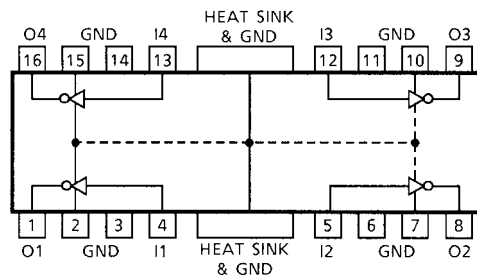
#### TD62074P / AP



#### TD62064F / AF

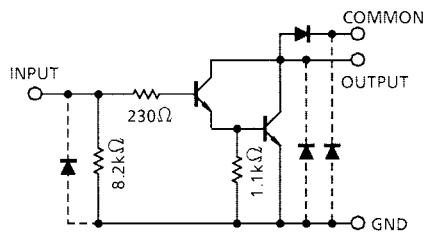


#### TD62074F / AF

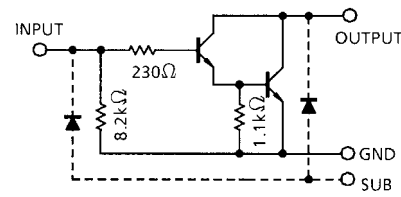


## SCHEMATICS (EACH DRIVER)

### TD62064P / AP / F / AF



### TD62074P / AP / F / AF



Note: The input and output parasitic diodes cannot be used as clamp diodes.

## PRECAUTIONS for USING

- (1) This IC does not include built-in protection circuits for excess current or overvoltage. If this IC is subjected to excess current or overvoltage, it may be destroyed. Hence, the utmost care must be taken when systems which incorporate this IC are designed. Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.
- (2) If a TD62064P/AP/F/AF is being used to drive an inductive load (such as a motor, solenoid or relay), Toshiba recommends that the diodes (pins 1 and 8) be connected to the secondary power supply pin so as to absorb the counter electromotive force generated by the load. Please adhere to the device's maximum ratings. Toshiba recommends that zener diodes be connected between the diodes (pins 1 and 8) and the secondary power supply pin (as the anode) so as to enable rapid absorption of the counter electromotive force. Again, please adhere to the device's maximum ratings.

If a TD62074P/AP/F/AF is being used to drive an inductive load (such as a motor, solenoid or relay), Toshiba recommends that a diode be connected between the output pin (as the anode) and the secondary power supply pin. Please adhere to the device's maximum ratings.

## MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Output Sustaining Voltage	P, F	V <sub>CE (SUS)</sub>	-0.5~35	V
	AP, AF		-0.5~50	
Output Current		I <sub>OUT</sub>	1.5	A / ch
Input Current		I <sub>IN</sub>	50	mA
Input Voltage		V <sub>IN</sub>	-0.5~17	V
Clamp Diode Reverse Voltage	P, F	V <sub>R (Note 1)</sub>	35	V
	AP, AF		50	
Clamp Diode Forward Current		I <sub>F (Note 1)</sub>	1.5	A / ch
Isolated Voltage	P, F	V <sub>SUB (Note 2)</sub>	35	V
	AP, AF		50	
Power Dissipation	P, AP	P <sub>D</sub>	1.47 / 2.7 (Note 3)	W
	F, AF		0.9 / 1.4 (Note 4)	
Operating Temperature		T <sub>opr</sub>	-40~85	°C
Storage Temperature		T <sub>stg</sub>	-55~150	°C

Note 1: TD62064P / AP / F / AF

Note 2: TD62074P / AP / F / AF

Note 3: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 50%)

Note 4: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

## RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Sustaining Voltage	P, F	V <sub>CE (SUS)</sub>		0	—	35	V	
	AP, AF			0	—	50		
Output Current	P, AP (Note 1) F, AF (Note 2)	I <sub>OUT</sub>	DC1 Circuit, Ta = 25°C	0	—	1250	mA / ch	
			T <sub>pw</sub> = 25 ms 4 Circuits T <sub>j</sub> = 120°C Ta = 85°C	Duty = 10 %	0	—		1250
				Duty = 50 %	0	—		390
				Duty = 10 %	0	—		907
Duty = 50 %	0	—	172					
Input Voltage		V <sub>IN</sub>		0	—	8	V	
	(Output On)	V <sub>IN (ON)</sub>	I <sub>OUT</sub> = 1.25 A	2.5	—	8	V	
	(Output Off)	V <sub>IN (OFF)</sub>		0	—	0.4	V	
Input Current		I <sub>IN</sub>		0	—	20	mA	
Clamp Diode Reverse Voltage	P, F	V <sub>R</sub>	TD62064P / AP / F / AF	0	—	35	V	
	AP, AF			0	—	50		
Clamp Diode Forward Current		I <sub>F</sub>		—	—	1.25	A	
Isolation Voltage	P, F	V <sub>SUB</sub>	TD62074P / AP / F / AF	—	—	35	V	
	AP, AF			—	—	50		
Power Dissipation	P, AP	P <sub>D</sub>	Ta = 85°C (Note 1)	—	—	1.4	W	
	F, AF		Ta = 85°C (Note 2)	—	—	0.7		

Note 1: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 50%)

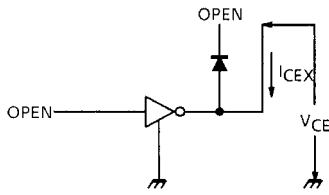
Note 2: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

**ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

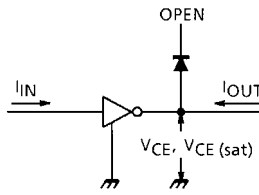
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Leakage Current	AP, AF	$I_{CEX}$	1	$V_{CE} = 50\text{ V}, T_a = 25^\circ\text{C}$	—	—	50	$\mu\text{A}$	
				$V_{CE} = 50\text{ V}, T_a = 85^\circ\text{C}$	—	—	500		
	P, F			$V_{CE} = 35\text{ V}, T_a = 25^\circ\text{C}$	—	—	50		
				$V_{CE} = 35\text{ V}, T_a = 85^\circ\text{C}$	—	—	500		
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$	2	$I_{OUT} = 1.25\text{ A}, I_{IN} = 2\text{ mA}$	—	—	1.6	V	
				$I_{OUT} = 0.75\text{ A}, I_{IN} = 935\text{ }\mu\text{A}$	—	—	1.25		
DC Current Transfer Ratio		$h_{FE}$	2	$V_{CE} = 2\text{ V}$	$I_{OUT} = 1.0\text{ A}$	—	800	—	
					$I_{OUT} = 0.25\text{ A}$	—	1500		
Input Voltage (Output On)		$V_{IN(ON)}$	3	$I_{OUT} = 1.25\text{ A}, I_{IN} = 2\text{ mA}$	—	—	2.4	V	
Clamp Diode Leakage Current	AP, AF	$I_R$	4	$V_R = 50\text{ V}, T_a = 25^\circ\text{C}$	—	—	50	$\mu\text{A}$	
				$V_R = 50\text{ V}, T_a = 85^\circ\text{C}$	—	—	100		
	F			$V_R = 35\text{ V}, T_a = 25^\circ\text{C}$	—	—	50		
				$V_R = 35\text{ V}, T_a = 85^\circ\text{C}$	—	—	100		
Clamp Diode Forward Voltage		$V_F$	5	$I_F = 1.25\text{ A}$	—	—	2	V	
Input Capacitance		$C_{IN}$	6	$V_{IN} = 0\text{ V}, f = 1\text{ MHz}$	—	15	—	pF	
Turn-On Delay	P, F	$t_{ON}$	7	$C_L = 15\text{ pF}$	$V_{OUT} = 35\text{ V}, R_L = 29\text{ }\Omega$	—	0.1	$\mu\text{s}$	
	AP, AF				$V_{OUT} = 50\text{ V}, R_L = 42\text{ }\Omega$				
Turn-Off Delay	P, F	$t_{OFF}$			$V_{OUT} = 35\text{ V}, R_L = 29\text{ }\Omega$	—	1.0		—
	AP, AF				$V_{OUT} = 50\text{ V}, R_L = 42\text{ }\Omega$				

## TEST CIRCUIT

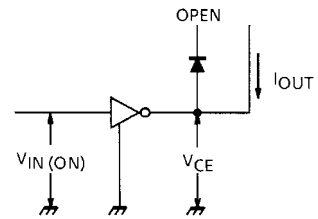
### 1. $I_{CEX}$



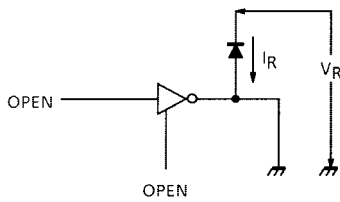
### 2. $V_{CE(sat)}$ , $h_{FE}$



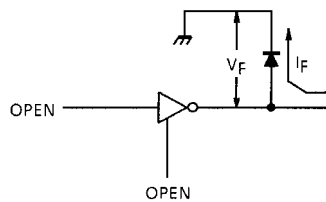
### 3. $V_{IN(ON)}$



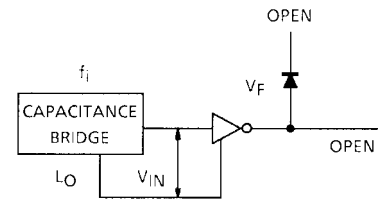
### 4. $I_R$



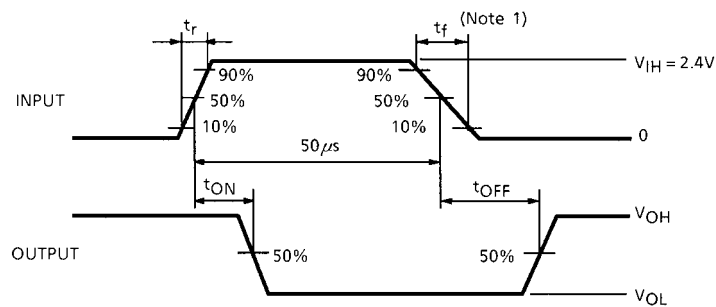
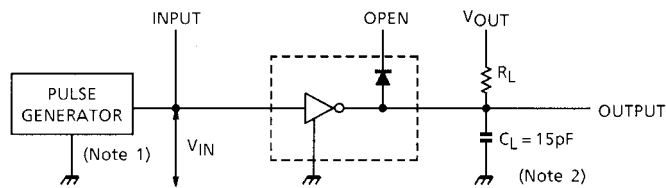
### 5. $V_F$



### 6. $C_{IN}$

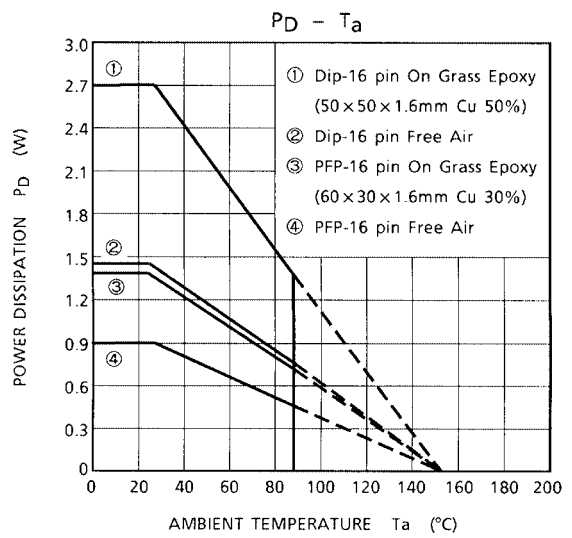
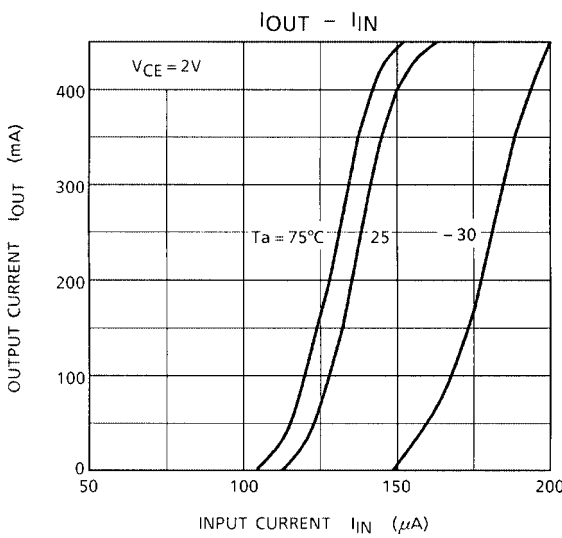
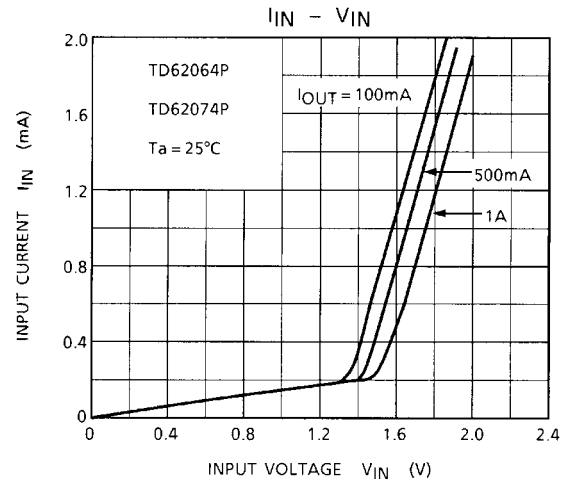
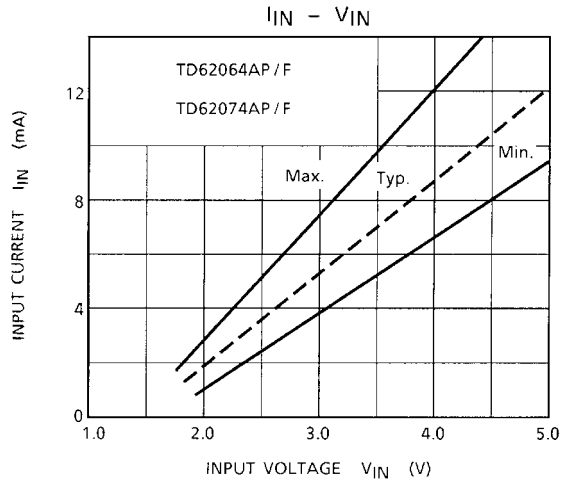
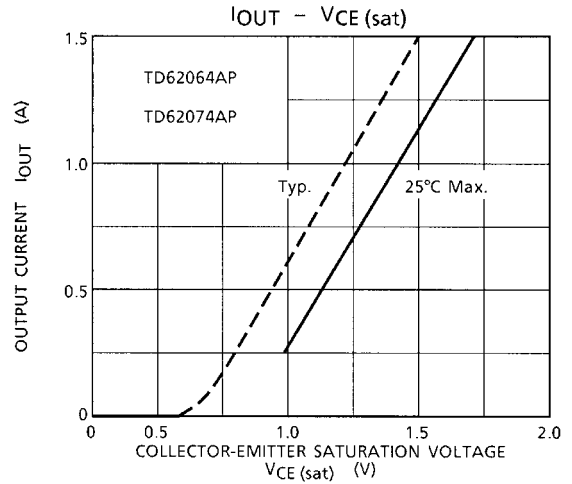
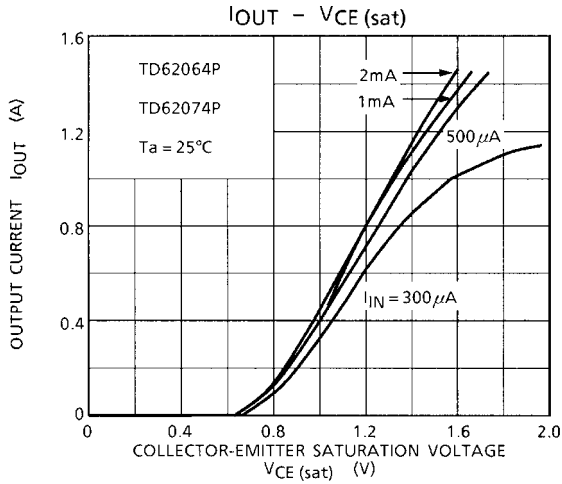


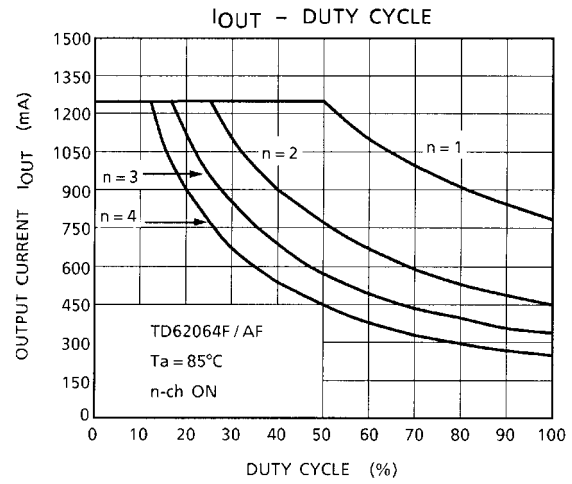
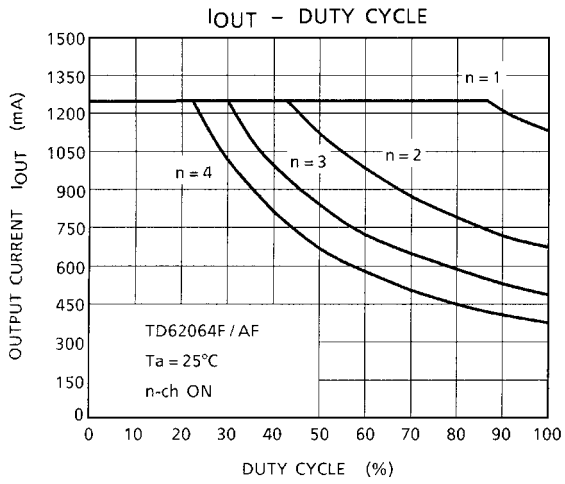
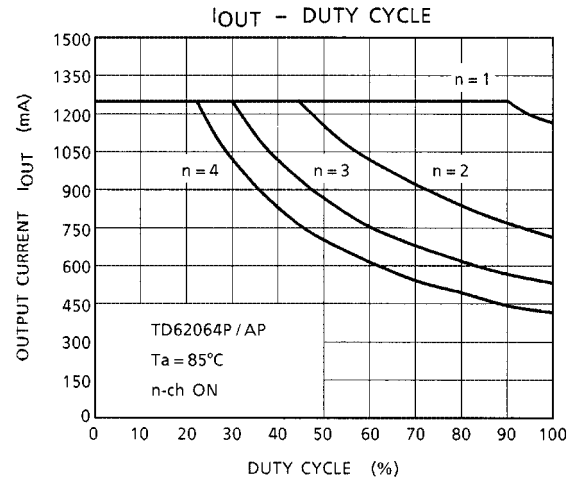
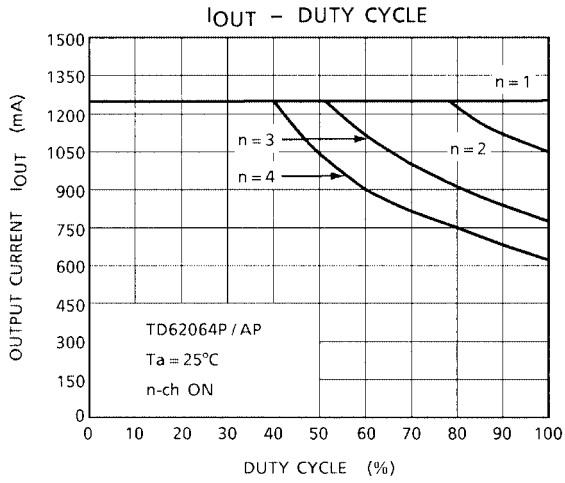
### 7. $t_{ON}$ , $t_{OFF}$



Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10%  
Output Impedance 50  $\Omega$ ,  $t_r \leq 5$ ns,  $t_f \leq 10$ ns

Note 2:  $C_L$  includes probe and jig capacitance

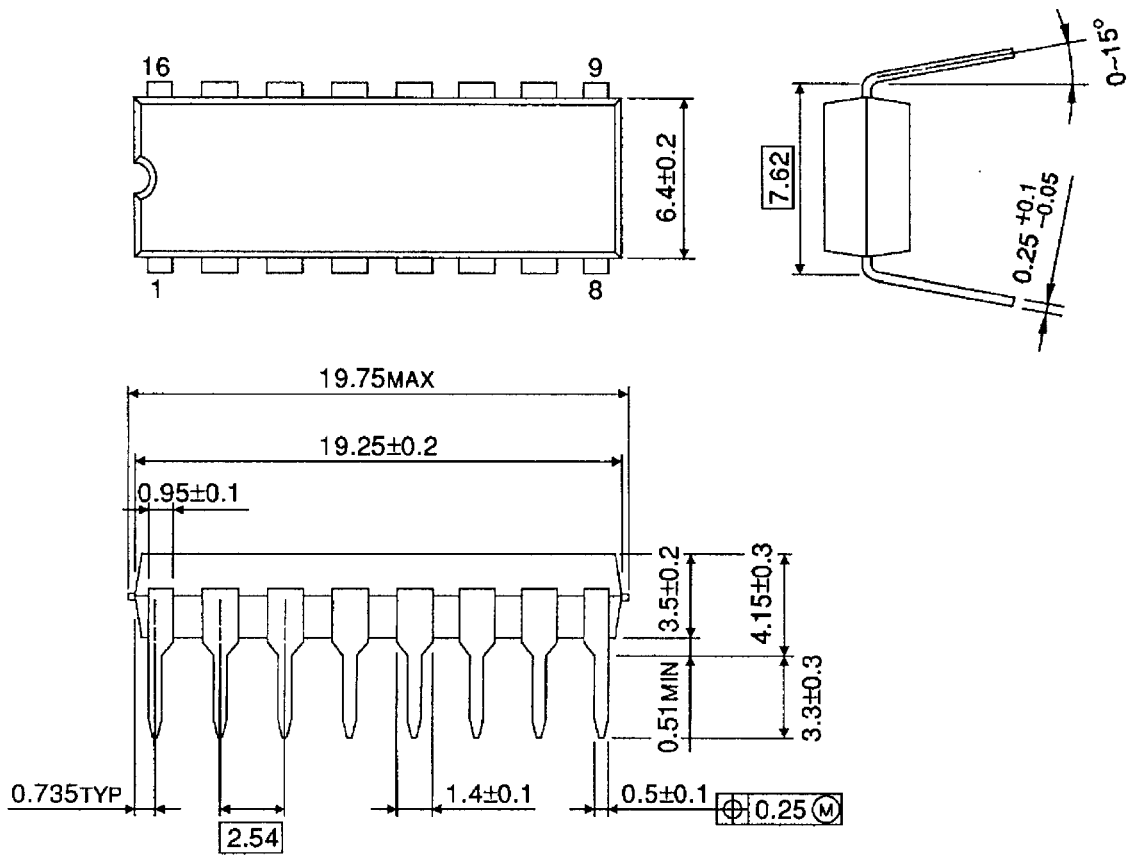




## PACKAGE DIMENSIONS

DIP16-P-300-2.54A

Unit : mm



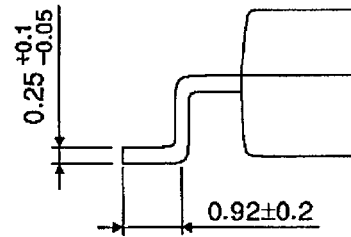
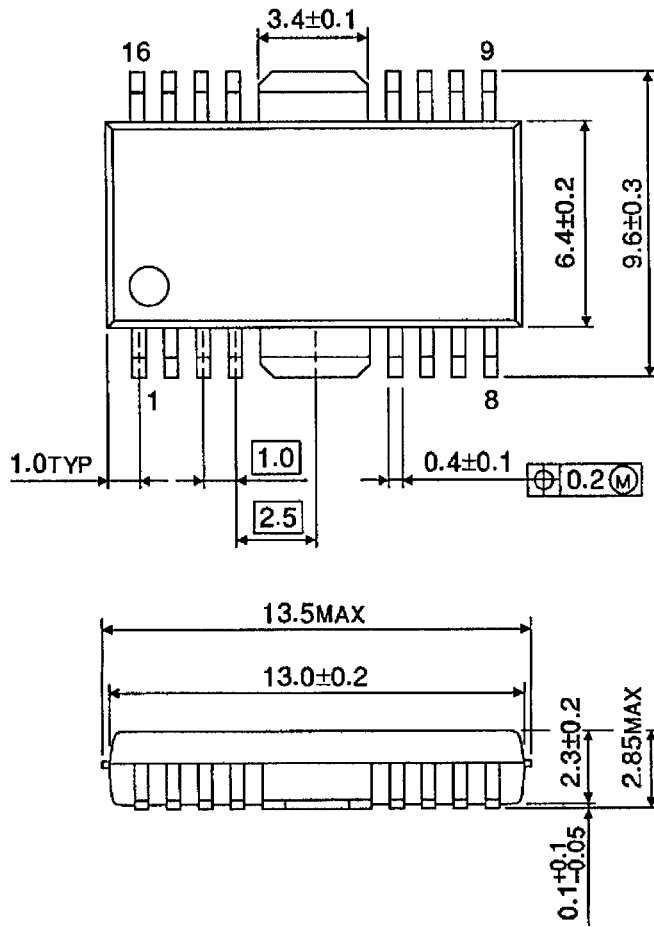
Weight: 1.11 g (Typ.)



## PACKAGE DIMENSIONS

HSOP16-P-300-1.00

Unit : mm



Weight: 0.50 g (Typ.)

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000707EBA

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