TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62304FB,TD62305FB

7CH LOW INPUT ACTIVE DARLINGTON SINK DRIVER

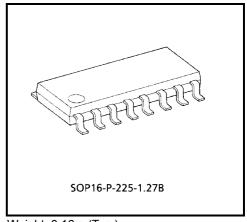
The TD62304FB and TD62305FB are non-inverting transistor arrays, which are comprised of seven NPN darlington buffer-transistor output stages PNP input stages.

These devices can be operated by source input voltage and are suitable for operations with a 5-V general purposed logic IC such as 5-V TTL, 5-V CMOS and 5-V Microprocessor which have sink current output drivers.

Please observe the thermal condition for using.

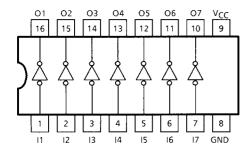
FEATURES

- Output current (single output) : 500 mA (Max.)
 High sustaining voltage : 35 V (Min.)
- Low level active input
- Inputs compatible with 5-V TTL and 5-V CMOS
- Package type-FB: SOP-16 pin



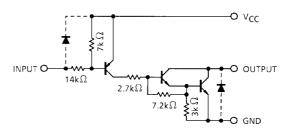
Weight: 0.16 g (Typ.)

PIN CONNECTION (TOP VIEW)

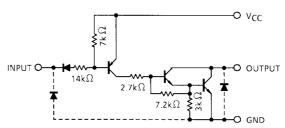


SCHEMATICS (EACH DRIVER)

TD62304FB



TD62305FB



Note: The input and output parasitic diodes cannot be used as clamp diodes.



MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT		
Supply Voltage	V _{CC}	-0.5~7.0	V		
Output Sustaining Voltage	V _{CE} (SUS)	-0.5~35	٧		
Output Current	lout	500	mA / ch		
Input Voltage	V _{IN}	-22~V _{CC} + 0.5	V		
Input Voltage	VIN	-0.5~7 (Note 1)	V		
Input Current	I _{IN}	-10	mA		
Power Dissipation	P _D	0.625 (Note 2)	W		
Operating Temperature	T _{opr}	-40~85	°C		
Storage Temperature	T _{stg}	-55~150	°C		

Note 1: On glass epoxy PCB (30 \times 30 \times 1.6 mm Cu 50%)

Note 2: TD62305FB only

RECOMMENDED OPERATING CONDITIONS (Ta = $-40 \sim 85$ °C)

CHARACTERISTIC		SYMBOL	CONDITION		MIN	TYP.	MAX	UNIT
Supply Voltage		V _{CC}	_		4.5	_	5.5	V
Output Sustaining Voltage		V _{CE} (SUS)	_		0	_	35	V
Output Current		l _{OUT}	DC 1 Circuit		0	1	400	
			T _{pw} ≤ 25 ms 7 circuits	Duty = 10%	0	l	240	mA / ch
			$T_j = 120$ °C Ta = 85°C (Note)	Duty = 50%	0	ı	60	
Input Voltage	TD62304FB	V _{IN}	_		-20	-	V_{CC}	V
	TD62305FB	VIN			0	1	5.5	
Input Voltage (Outout On)	TD62304FB	V _{IN(ON)}	-		-22	ı	V _{CC} - 3.5	· V
	TD62305FB	VIN(ON)	-		-0.5	l	V _{CC} - 3.7	
Input Voltage (Outout Off)	TD62304FB	VINCOLE	_		V _{CC} - 0.4	ı	V _{CC}	V
	TD62305FB	- V _{IN(OFF)}	_	V _{CC} - 0.6	ı	V _{CC}	v	
Power Dissipation		P _D	(Note)		_	_	0.325	W

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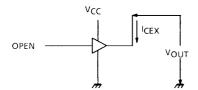
Note: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

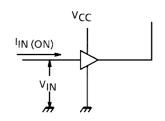
CHARACT	ERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
Output Leakage Current		I _{CEX}	1	V _{CC} = 5.5 V, V _{OUT} = 35 V Ta = 85°C, I _{IN} = 0		-	_	-100	μΑ
Output Saturation Voltage		V _{CE} (sat)	2	V _{CC} = 4.5 V, I _{OUT} = 350 mA	V _{IN} = V _{IN} (ON) (Max.)	_	1.4	2.0	V
					V _{IN} = 0.8 V	_	1.4	2.2	
Input Current	(Output On)	lui (ost)	3	V _{CC} = 5.5 V, V _{IN} = 0.4 V		_	-0.32	-0.45	mA
	(Output On)	lin (on)		V _{CC} = 5.5 V, V _{IN} = -20 V		_	_	-2.6	IIIA
	(Output Off)	I _{IN (OFF)}	4	_		_	_	-40	μA
Input Voltage (Output On)	TD62304FB	Varian	V _{IN(ON)} 5	_		-	_	V _{CC} -2.8	· V
	TD62305FB	VIN(ON)				_	_	V _{CC} -3.7	
Supply Current	(Output On)	I _{CC (ON)}	6	V _{CC} = 5.5 V, V _{IN} = 0 V		_	17	22	mA
	(Output Off)	I _{CC (OFF)}		V _{CC} = V _{IN} = 5.5 V		_	_	100	μΑ
Turn-On Delay		t _{ON}	7	V _{CC} = 5 V, C _L = 15 pF V _{OUT} = 35 V, R _L = 87.5 Ω		_	0.1	_	- µs
Turn-Off Delay		t _{OFF}] ′			_	3	_	

TEST CIRCUIT

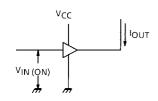
1. ICEX



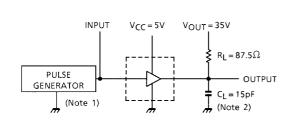
3. I_{IN} (ON)



5. V_{IN (ON)}



7. ton, toff

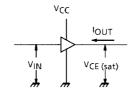


Note 1: Pulse width 50 µs, duty cycle 10%

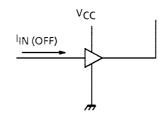
Output impedance 50 Ω , $t_r \le 10$ ns, $t_f \le 5$ ns

Note 2: C_L includes probe and jig capacitance

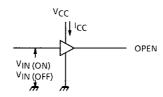
2. VCE (sat)

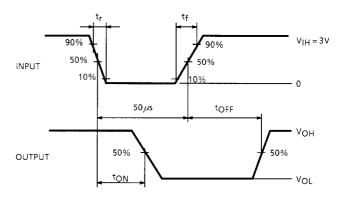


4. I_{IN} (OFF)



6. Icc



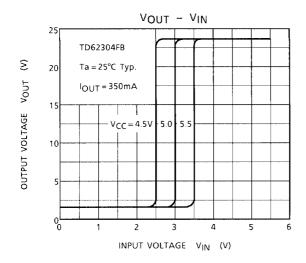


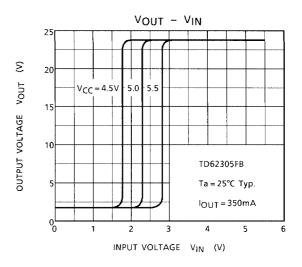
PRECAUTIONS for USING

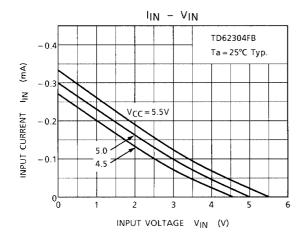
This IC does not include built-in protection circuits for excess current or overvoltage.

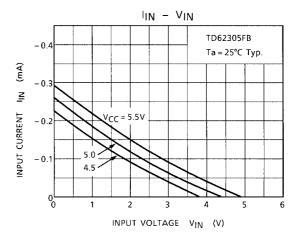
If this IC is subjected to excess current or overvoltage, it may be destroyed.

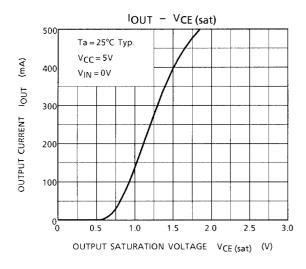
Hence, the utmost care must be taken when systems which incorporate this IC are designed. Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

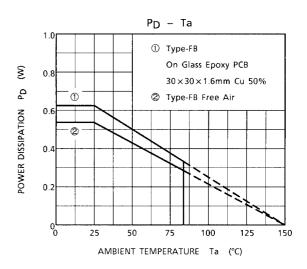








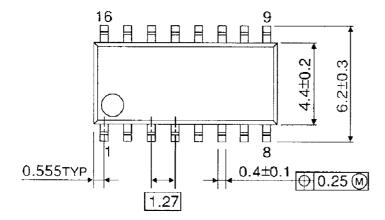


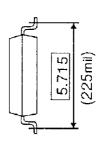


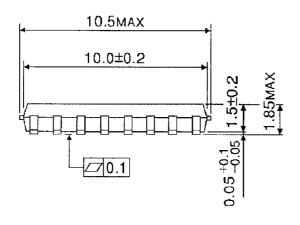
Unit: mm

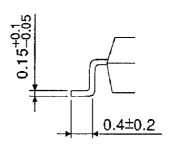
PACKAGE DIMENSIONS

SOP16-P-225-1.27B









Weight: 0.16 g (Typ.)

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RESTRICTIONS ON PRODUCT USE

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