

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

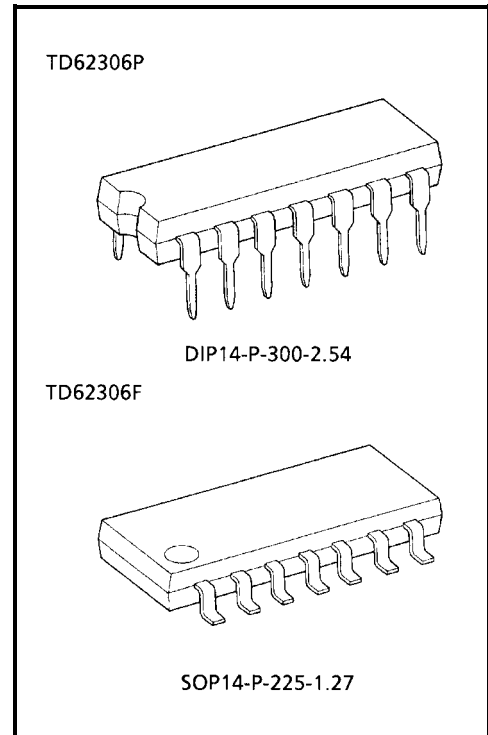
# TD62306P, TD62306F

## 6CH LOW SATURATION SINK DRIVER

The TD62306P, TD62306F are comprised of six NPN low saturation drivers. All units feature integral clamp diodes for switching inductive loads and protective diodes against a negative input voltage. Applications include relay, hammer, lamp and LED driver.

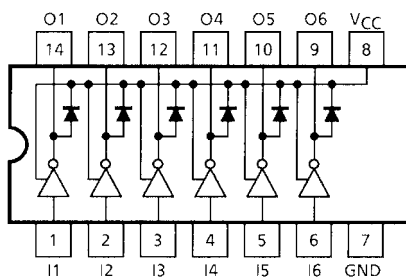
### FEATURES

- Low saturation output voltage :  $V_{CE(sat)} = 0.6\text{ V (Max.)}$   
@ $I_{OUT} = 120\text{ mA}$
- Output rating (single output) 20 V (Min.) / 150 mA (Max.)
- Inputs compatible with 5~15 V PMOS, CMOS
- Input protective diodes against a negative input voltage
- Package type-P : DIP-14 pin
- Package type-F : SOP-14 pin

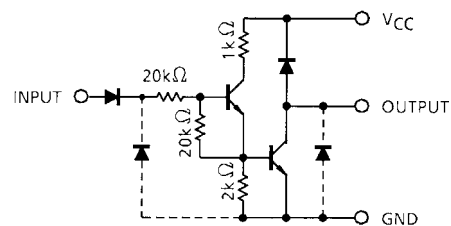


Weight  
 DIP14-P-300-2.54 : 1.11 g (Typ.)  
 SOP14-P-225-1.27 : 0.16 g (Typ.)

### PIN CONNECTION (TOP VIEW)



### SCHEMATICS (EACH DRIVER)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

## MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>CC</sub>	-0.5~20	V
Output Sustaining Voltage	V <sub>CE(SUS)</sub>	-0.5~V <sub>CC</sub> + 0.5	V
Output Current	I <sub>OUT</sub>	150	mA / ch
Input Voltage	V <sub>IN</sub>	-37~20	V
Input Current	I <sub>IN</sub>	1.5	mA
Clamp Diode Reverse Voltage	V <sub>R</sub>	20	V
Clamp Diode Forward Current	I <sub>F</sub>	120	mA
Power Dissipation	P	P <sub>D</sub>	W
	F		
Operating Temperature	P	T <sub>opr</sub>	°C
	F		
Storage Temperature	T <sub>stg</sub>	-30~75	°C
		-40~85	°C
		-55~150	°C

Note: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 30%)

## RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C for Type-F and Ta = -30~75°C for Type-P)

CHARACTERISTIC	SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Supply Voltage	V <sub>CC</sub>	—	4.75	—	18	V
Output Current	I <sub>OUT</sub>	DC 1 Circuit	0	—	120	mA / ch
		T <sub>pw</sub> = 25 ms, Duty = 10% 6 Circuits	0	—	100	
Input Voltage	V <sub>IN</sub>	—	-35	—	V <sub>CC</sub>	V
Clamp Diode Reverse Voltage	V <sub>R</sub>	—	—	—	18	V
Clamp Diode Forward Current	I <sub>F</sub>	—	—	—	120	mA
Power Dissipation	P	P <sub>D</sub>	(Note)	—	—	W
	F					
					0.325	

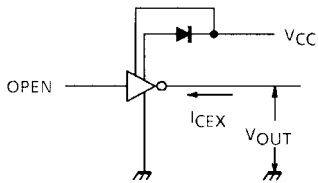
Note: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 30%)

## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

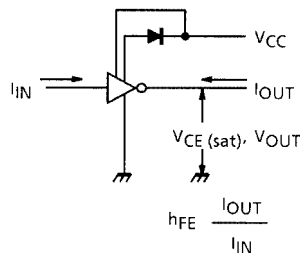
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current	P	I <sub>CEX</sub>	1	V <sub>CC</sub> = 18 V V <sub>OUT</sub> = 18 V	—	—	100	μA
	F			Ta = 75°C Ta = 85°C			100	
Output Saturation Voltage		V <sub>CE (sat)</sub>	2	V <sub>CC</sub> = 5 V, I <sub>IN</sub> = 0.2 mA I <sub>OUT</sub> = 120 mA	—	0.45	0.6	V
DC Forward Current Transfer Ratio		h <sub>FE</sub>	2	V <sub>CC</sub> = 5 V, V <sub>OUT</sub> = 2 V I <sub>OUT</sub> = 120 mA	1000	—	—	—
Input Current	Output On	I <sub>IN (ON)</sub>	3	V <sub>IN</sub> = 5 V, I <sub>OUT</sub> = 120 mA	—	0.16	0.23	mA
	Output Off	I <sub>IN (OFF)</sub>		4	V <sub>IN</sub> = 15 V, I <sub>OUT</sub> = 120 mA	—	0.66	
Clamp Diode Forward Voltage		V <sub>F</sub>	5	I <sub>F</sub> = 120 mA	—	1.25	1.6	V
Supply Current	Output On	I <sub>CC (ON)</sub>	6	V <sub>CC</sub> = V <sub>IN</sub> = 5 V	—	4.0	6.0	mA / Gate
	Output Off	I <sub>CC (OFF)</sub>		6	V <sub>CC</sub> = V <sub>IN</sub> = 15 V	—	14.0	
Turn-On Delay		t <sub>ON</sub>	7	V <sub>CC</sub> = 18 V, R <sub>L</sub> = 150 Ω C <sub>L</sub> = 15 pF	—	0.1	—	μs
Turn-Off Delay		t <sub>OFF</sub>			—	0.8	—	μs

## TEST CIRCUIT

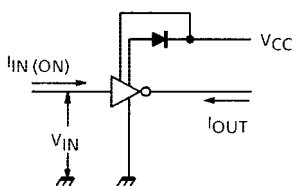
### 1. I<sub>CEX</sub>



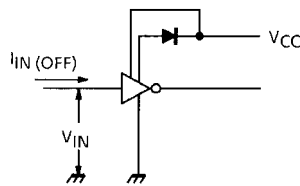
### 2. h<sub>FE</sub>, V<sub>CE (sat)</sub>



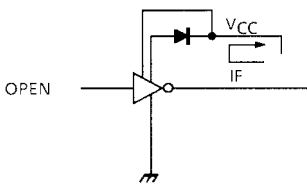
### 3. I<sub>IN (ON)</sub>



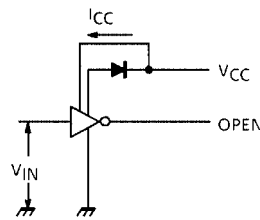
### 4. I<sub>IN (OFF)</sub>



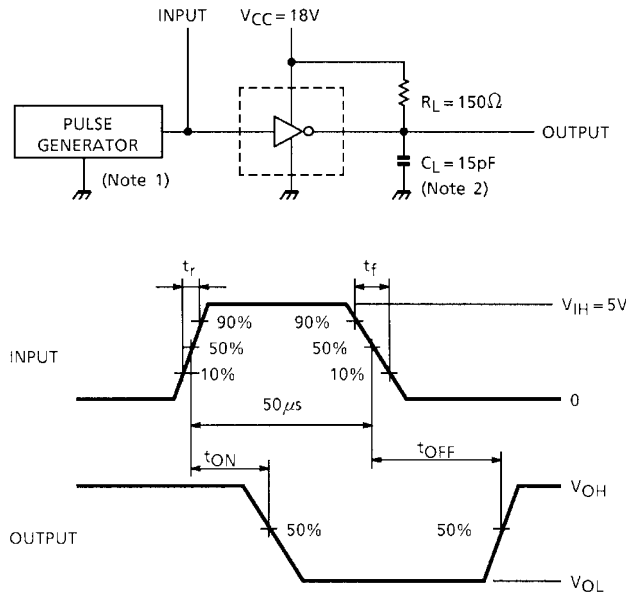
**5.  $V_F$**



**6.  $I_{CC}$**



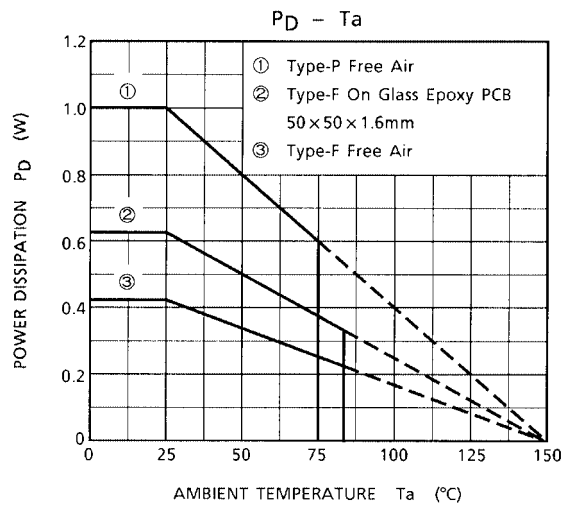
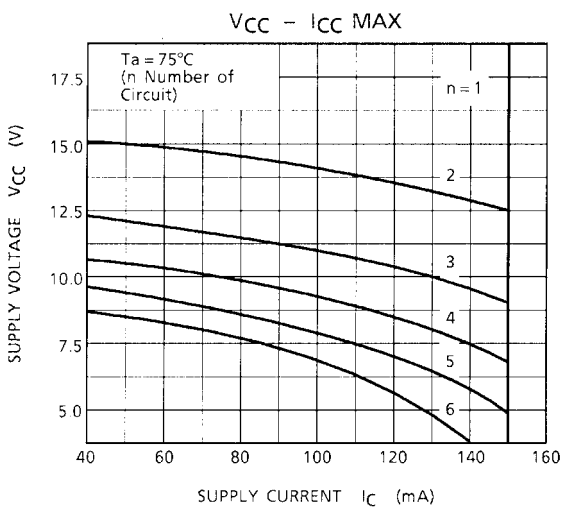
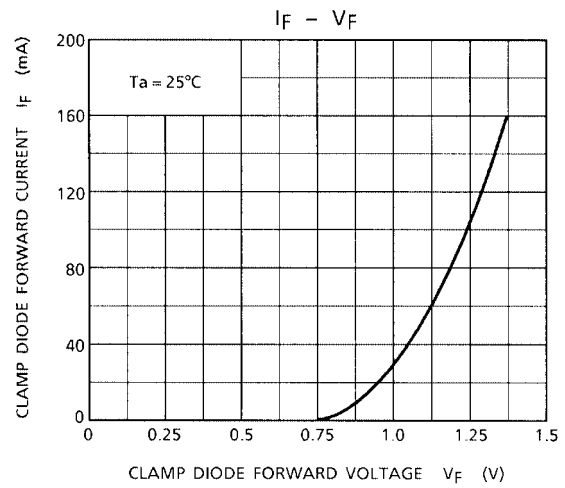
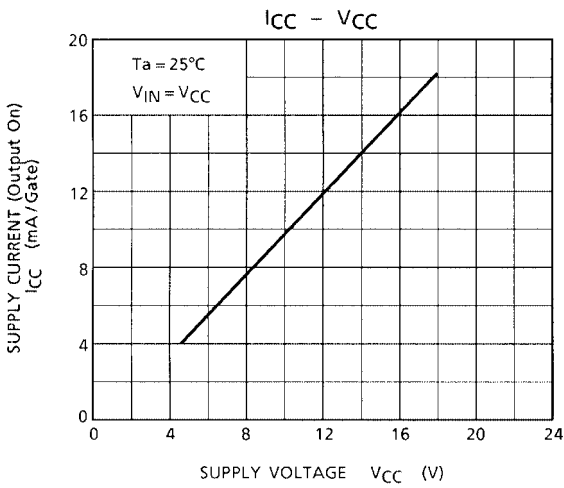
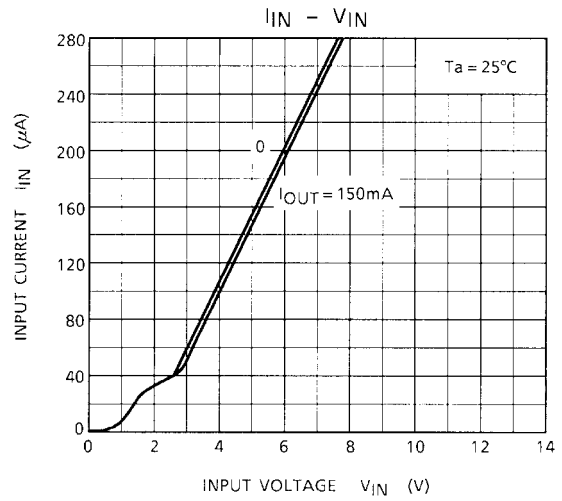
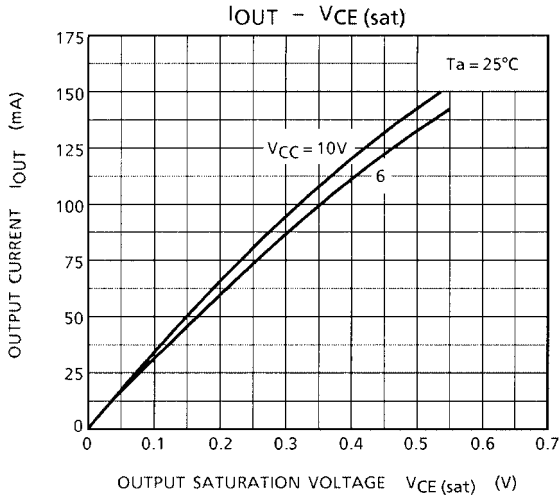
**7.  $t_{ON}$ ,  $t_{OFF}$**



Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10%  
 Output Impedance 50  $\Omega$ ,  $t_r \leq 5$  ns,  $t_f \leq 10$  ns  
 Note 2:  $C_L$  includes probe and jig capacitance

**PRECAUTIONS for USING**

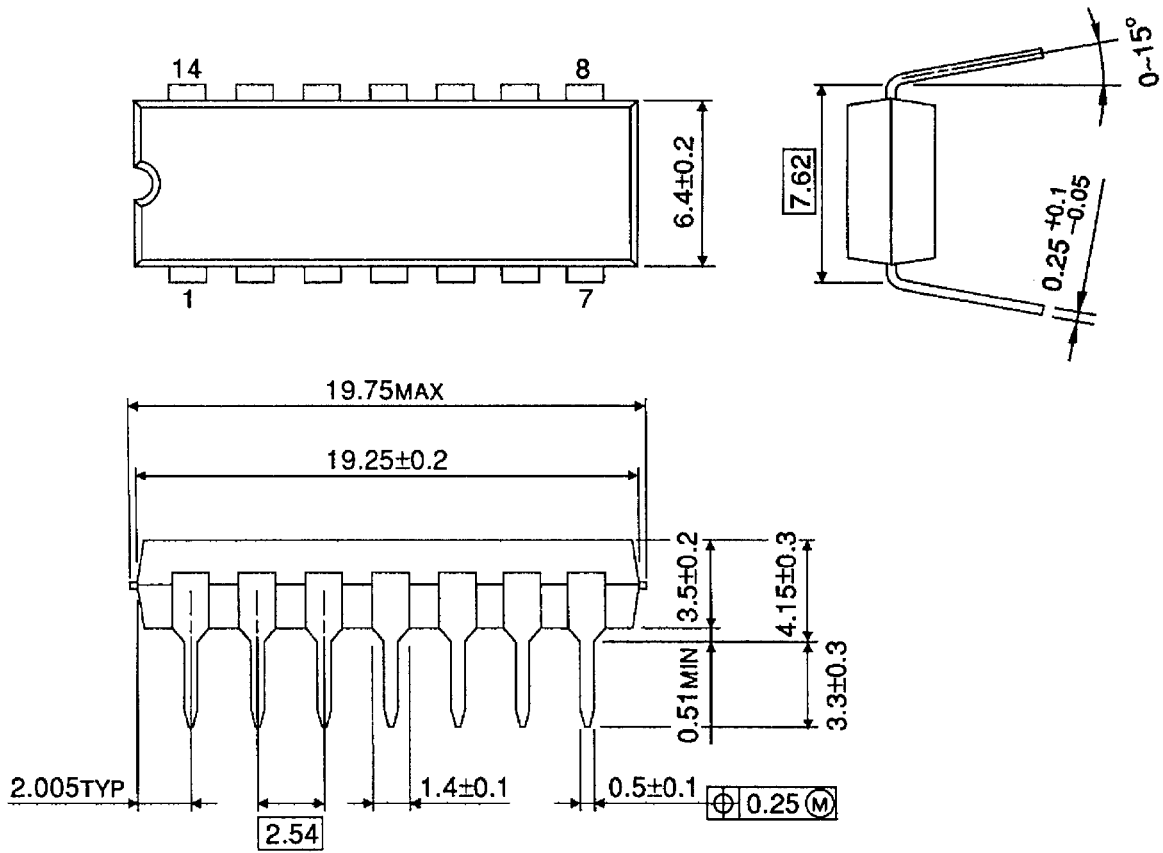
This IC does not include built-in protection circuits for excess current or overvoltage. If this IC is subjected to excess current or overvoltage, it may be destroyed. Hence, the utmost care must be taken when systems which incorporate this IC are designed. Utmost care is necessary in the design of the output line, VCC and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



## PACKAGE DIMENSIONS

DIP14-P-300-2.54A

Unit: mm

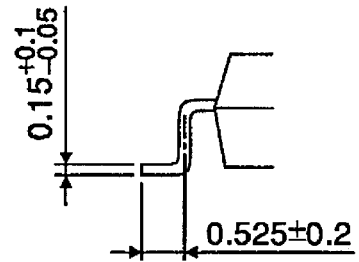
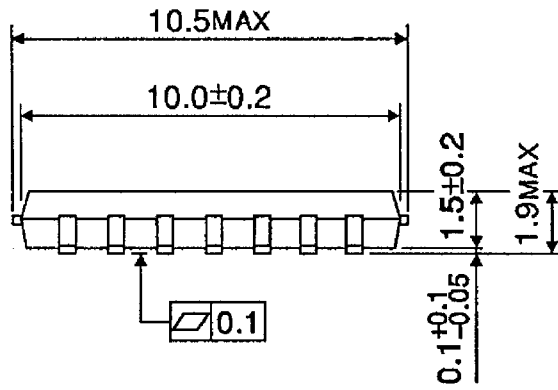
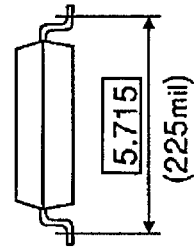
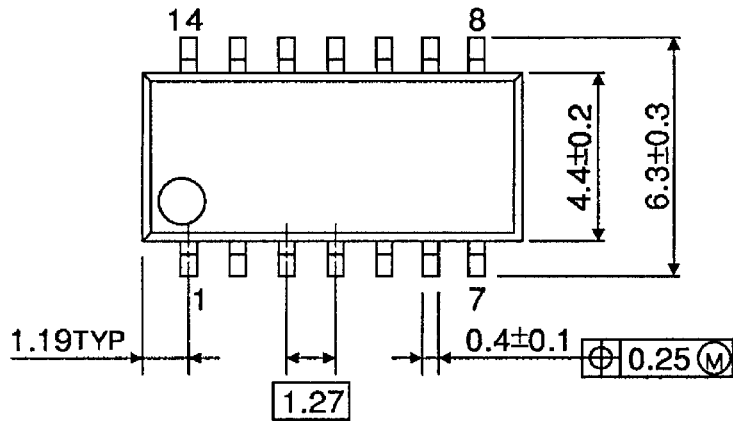


Weight: 1.11 g (Typ.)

## PACKAGE DIMENSIONS

SOP14-P-225-1.27

Unit: mm



Weight: 0.16 g (Typ.)

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000707EBA

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