

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62384AP,TD62384F,TD62384AF TD62385AP,TD62385F,TD62385AF

8CH LOW INPUT ACTIVE DARLINGTON SINK DRIVER

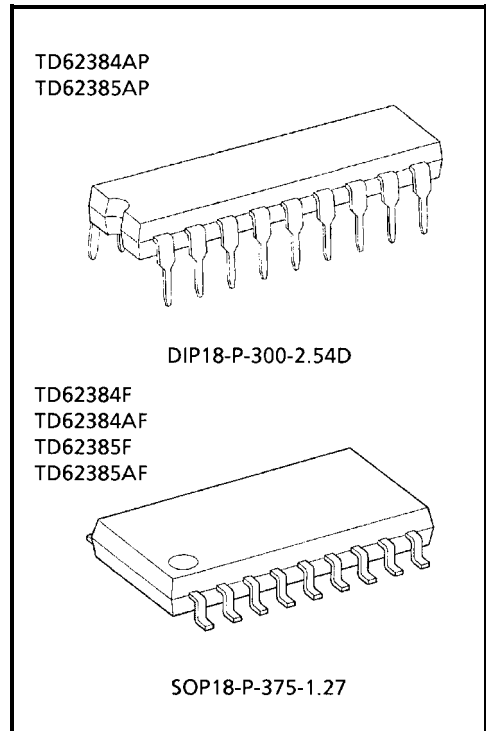
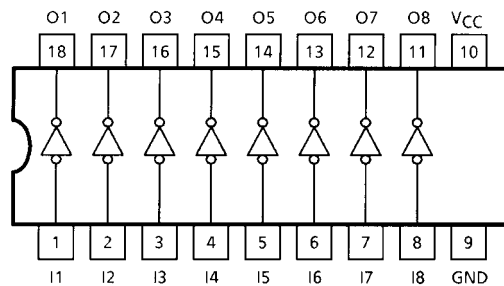
The TD62384AP / F / AF and TD62385AP / F / AF are non-inverting transistor arrays, which are comprised of eight NPN darlington output stages and PNP input stages. These devices can be operated by source input voltage and are suitable for operations with a 5-V general purposed logic IC such as 5-V TTL, 5-V CMOS and 5-V Microprocessor which have sink current output drivers. Please observe the thermal condition for using.

FEATURES

- Output current (single output) 500 mA (Max)
- High sustaining voltage 35 V (TD62384F, 385F)
50 V (TD62384AP / AF, 385AP / AF) (Min)
- Low level active input
- Inputs compatible with 5-V TTL and 5-V CMOS
- Package type-AP : DIP-18 pin
- Package type-F, AF : SOP-18 pin

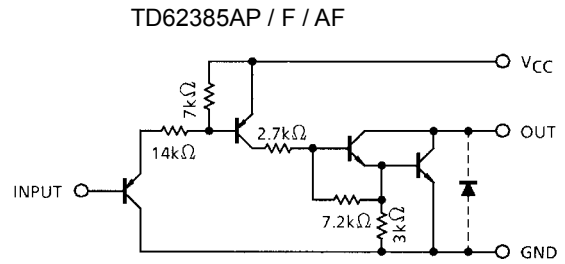
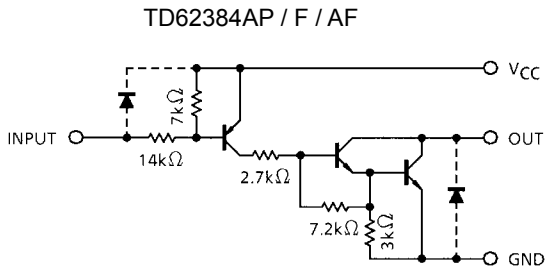
TYPE	V _{IN} (ON)
TD62384AP / F / AF	-20 V~V _{CC} - 2.8 V
TD62385AP / F / AF	0 V~V _{CC} - 3.7 V

PIN CONNECTION (TOP VIEW)



Weight
DIP18-P-300-2.54D : 1.47 g (Typ.)
SOP18-P-375-1.27 : 0.41 g (Typ.)

SCHEMATICS (EACH DRIVER)



Note : The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC}	-0.5~7.0	V
Output Sustaining Voltage	AP / AF	V _{CE (SUS)}	V
	F		
Output Current	I _{OUT}	500	mA / ch
Input Voltage	V _{IN} (Note 1)	-22~V _{CC} + 0.5	V
	V _{IN} (Note 2)		
Input Current	I _{IN}	-10	mA
Power Dissipation	AP	P _D (Note 3)	W
	F / AF		
Operating Temperature	T _{opr}	-40~85	°C
Storage Temperature	T _{stg}	-55~150	°C

Note 1: TD62384AP / AF / F

Note 2: TD62385AP / AF / F

Note 3: Delated above 25°C in the proportion of 11.7 mW / °C (AP-Type), 7.7 mW / °C (F, AF-Type).

RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTERISTIC	SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Supply Voltage	V _{CC}	—	4.5	5.0	5.5	V
Output Sustaining Voltage	AP / AF	V _{CE (SUS)}	—	—	50	V
	F				35	
Output Current	AP	I _{OUT}	T _{pw} = 25 ms, Duty = 50% 8 Circuits, Ta = 85°C, Tj = 120°C	—	115	mA / ch
	F / AF				78	
					T _{pw} = 25 ms, Duty = 10% 1 Circuit	
Input Voltage	TD62384	V _{IN}	—	—	V _{CC}	V
	TD62385				5.5	
Power Dissipation	AP	P _D	—	—	0.52	W
	F / AF				0.35	

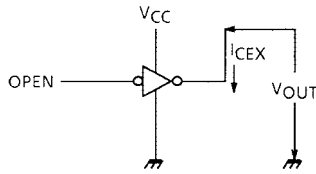
ELECTRICAL CHARACTERISTIC (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current	AP / AF	I _{CEX}	1	V _{CC} = 5.5 V, I _{IN} = 0, Ta = 85°C, V _{OUT} = 50 V	—	—	100	μA
	F			V _{OUT} = 35 V				
Output Saturation Voltage		V _{CE (sat)}	2	V _{CC} = 4.5 V, I _{OUT} = 350 mA, V _{IN} = V _{IN (ON)} MAX.	—	1.4	2.0	V
Input Current	(Output On)	I _{IN (ON)}	3	V _{CC} = 5.5 V, V _{IN} = 0.4 V	—	-0.32	-0.45	mA
				V _{CC} = 5.5 V, V _{IN} = -20 V	—	—	-2.6	
	(Output Off)	I _{IN (OFF)}	4	—	—	—	-4.0	μA
Input Voltage	(Output on)	TD62384	5	—	—	—	V _{CC} - 2.8	V
		TD62385		—	—	—	V _{CC} - 3.7	
Supply Current		I _{CC (ON)}	6	V _{CC} = 5.5 V, V _{IN} = 0	—	17	22	mA
		I _{CC (OFF)}		V _{CC} = 5.5 V, V _{IN} = V _{CC}	—	—	100	
Turn-On Delay		t _{ON}	7	V _{CC} = 5 V, V _{OUT} = 50 V, R _L = 163 Ω, C _L = 15 pF (Note 1)	—	0.1	—	μs
Turn-Off Delay		t _{OFF}			—	3	—	

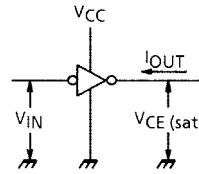
Note 1: F-Type: V_{OUT} = 35 V, R_L = 116 Ω

TEST CIRCUIT

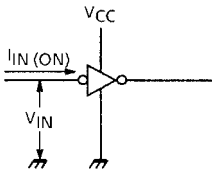
1. I_{CEX}



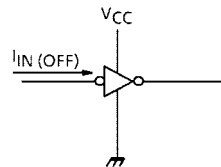
2. $V_{CE(sat)}$



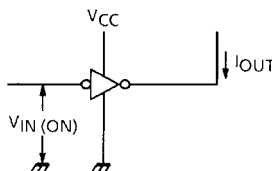
3. $I_{IN(ON)}$



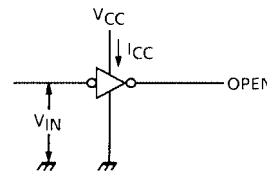
4. $I_{IN(OFF)}$



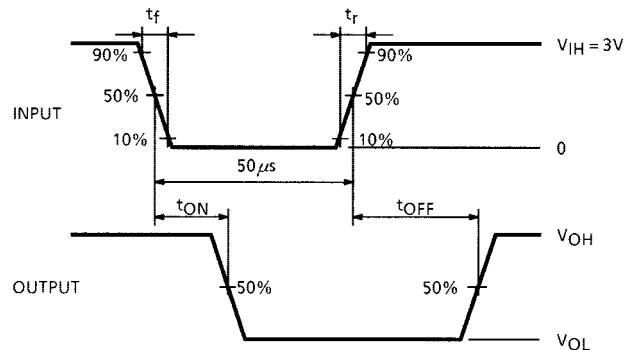
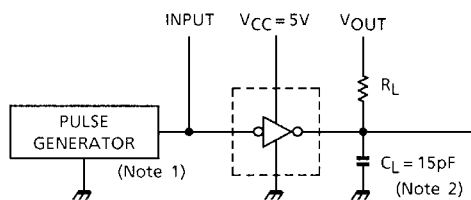
5. $V_{IN(ON)}$



6. I_{CC}



7. t_{ON}, t_{OFF}



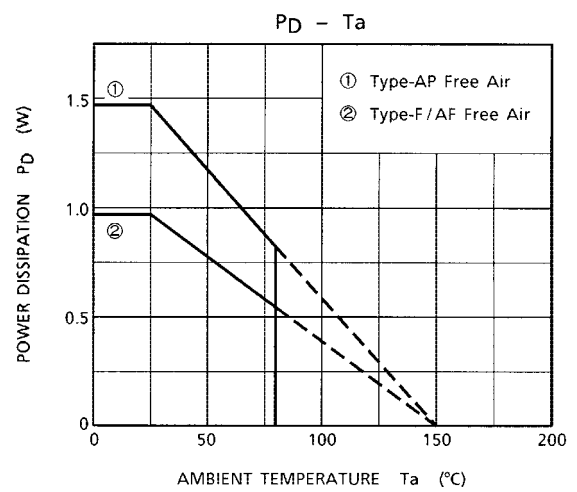
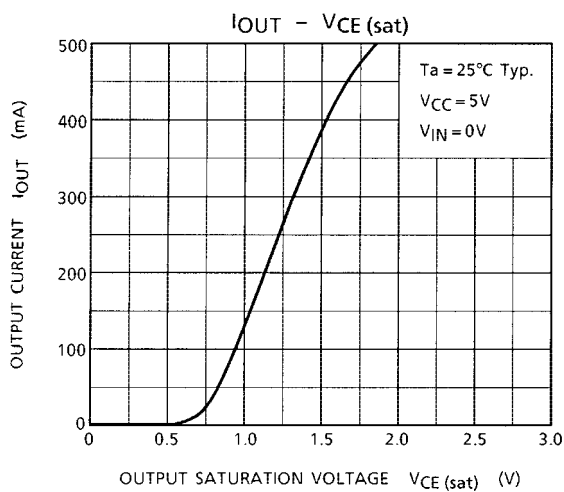
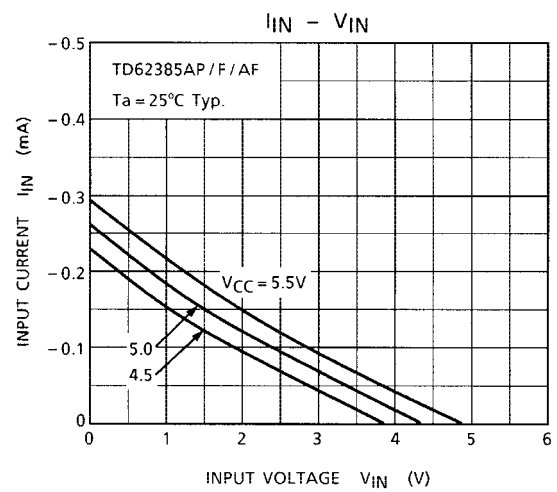
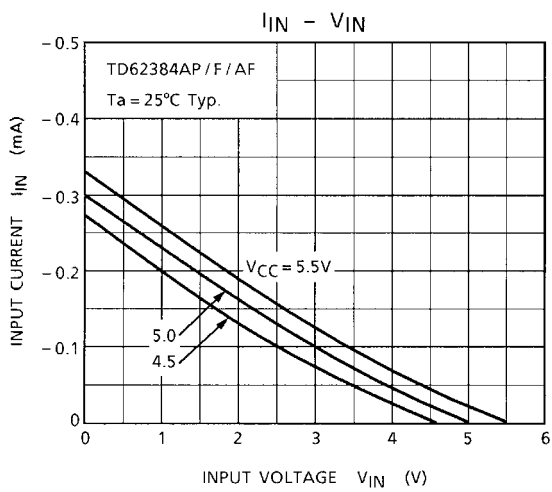
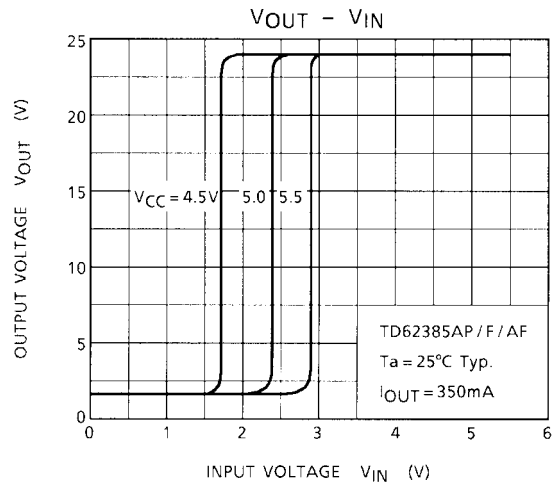
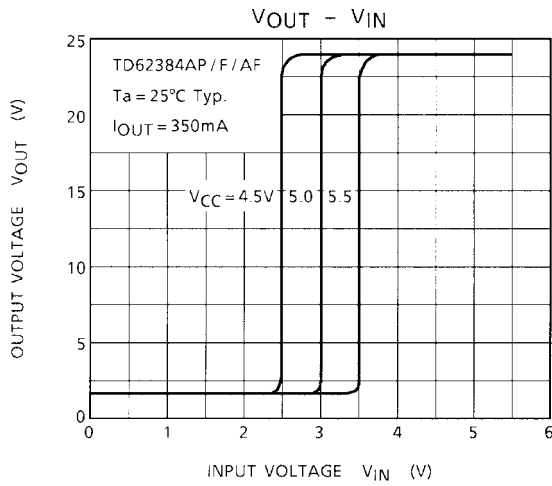
Note 1: Pulse Width 50 μ s, Duty Cycle 10%
 Output Impedance 50 Ω , $t_r \leq 5$ ns, $t_f \leq 10$ ns
 Note 2: C_L includes probe and jig capacitance.

PRECAUTIONS for USING

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

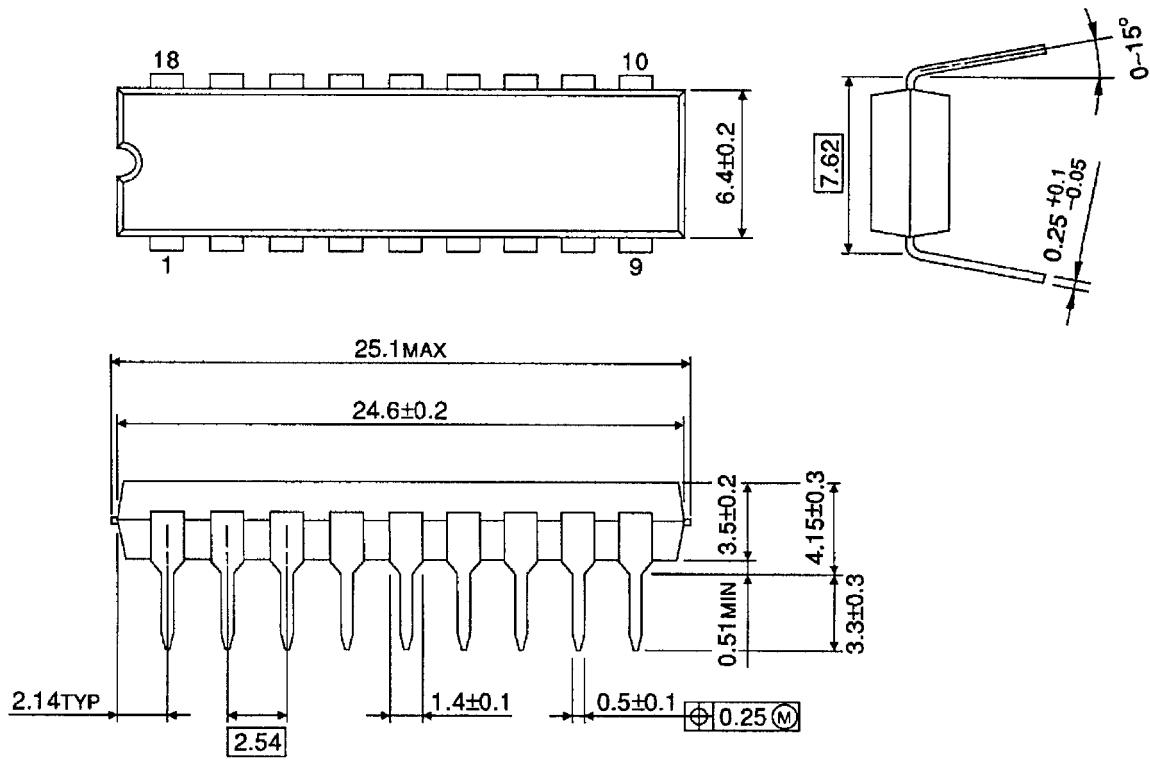
Utmost care is necessary in the design of the output line, VCC and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



PACKAGE DIMENSIONS

DIP18-P-300-2.54D

Unit: mm

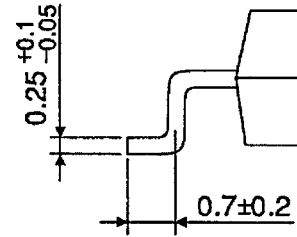
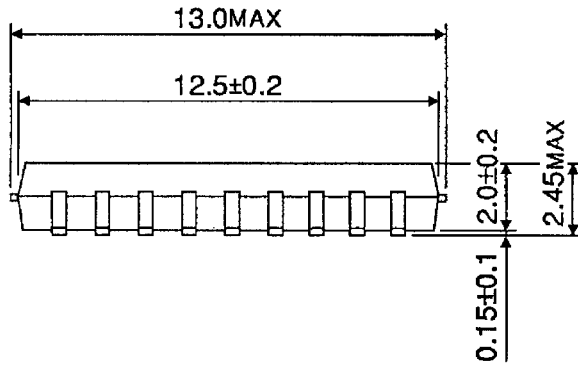
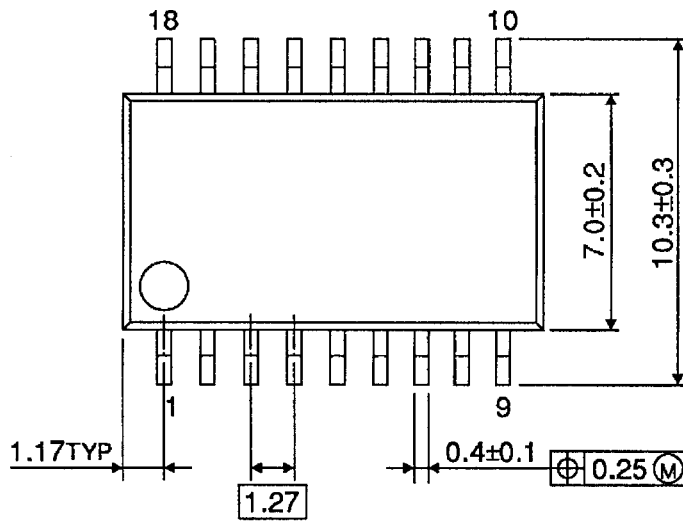


Weight: 1.47 g (Typ.)

PACKAGE DIMENSIONS

SOP18-P-375-1.27

Unit: mm



Weight: 0.41 g (Typ.)

RESTRICTIONS ON PRODUCT USE

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