TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62504P-H

#### 7CH SINGLE DRIVER : COMMON EMITTER

The TD62504P–H is comprised of seven or five NPN Transistor Arrays. Applications include relay, hammer, lamp and display (LED)

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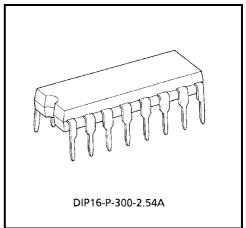
#### FEATURES

- Package Type : DIP16 pin
- RIN = 10.5 kΩ
- High Sustaining Voltage Output : V<sub>CEO</sub> = 35 V (Min)

: VCE (sat) = 0.8 V

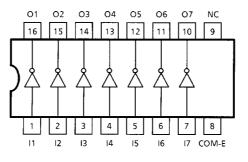
@Iout = 150 mA

- Output Current (Single Output) : 200 mA (Max)
- Low Saturation Voltage
- Inputs Compatible with Various Types of Logic.
- Wide operating temperature range: -40~105°C

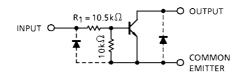


#### Weight: 0.11 g (Typ.)

#### **PIN CONNECTION (TOP VIEW)**



#### SCHEMATICS (EACH DRIVER)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

## MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Emitter Voltage	V <sub>CEO</sub>	35	V
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector Current	Ι <sub>C</sub>	200	mA / ch
Input Voltage	V <sub>IN</sub>	-0.5~30	V
Power Dissipation	PD	1.0	W
Operating Temperature	T <sub>opr</sub>	-40~105	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

# **RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)**

CHARACTERISTIC	SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Collector-Emitter Voltage	V <sub>CEO</sub>	—	0	_	35	V
Collector-Base Voltage	V <sub>CBO</sub>	—	0	_	50	V
Collector Current	Ι <sub>C</sub>	_	0	_	150	mA / ch
Input Voltage	V <sub>IN</sub>	_	0	_	25	v
	V <sub>IN (ON)</sub>	I <sub>IN</sub> = 1 mA	15.0	_	25	v
Power Dissipation	PD	Ta = 85°C	_	—	0.52	W

# ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current	ICEX	1	V <sub>CE</sub> = 35 V, V <sub>IN</sub> = 0 V		—	10	μA
Collector-Emitter Saturation Voltage	uration Voltage V <sub>CE (sat)</sub>	2	I <sub>IN</sub> = 1 mA, I <sub>C</sub> = 10 mA	_	—	0.2	v
			I <sub>IN</sub> = 3 mA, I <sub>C</sub> = 15 0 mA	_	—	0.8	
DC Current Transfer Ratio	h <sub>FE</sub>	2	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 10 mA	50	—	_	—
Input Voltage	V <sub>IN (ON)</sub>	3	I <sub>IN</sub> = 1 mA, I <sub>C</sub> = 10 mA	7.5	11.5	15.0	V
Turn-On Delay	t <sub>ON</sub>	4	V <sub>CEO</sub> = 35 V, R <sub>L</sub> = 220 Ω C <sub>L</sub> = 15 pF		50	-	ns
Turn-Off Delay	t <sub>OFF</sub>		C <sub>L</sub> = 15 pF	_	200	-	115

### **RECOMMENDED OPERATING CONDITIONS (Ta = -40~105°C)**

CHARACTERISTIC	SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Collector-Emitter Voltage	V <sub>CEO</sub>	—		_	35	V
Collector-Base Voltage	V <sub>CBO</sub>	_		_	50	V
Collector Current	ΙC	DC 1 circuits	0	_	150	mA / ch
		7 circuits	0	_	100	
	V <sub>IN</sub>	—	0	_	25	
Input Voltage	V <sub>IN (ON)</sub>	I <sub>IN</sub> = 1 mA	15.0	_	25	V
	V <sub>IN (OFF)</sub>	—	0	_	0.50	
Power Dissipation	PD	Ta = 105°C	_	_	0.36	W

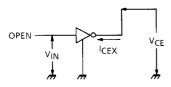
## ELECTRICAL CHARACTERISTICS (Ta = 105°C)

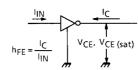
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Leakage Current	ICEX	1	V <sub>CE</sub> = 35 V, V <sub>IN</sub> = 0 V	-	_	300	μA	
Collector-Emitter Saturation Voltage	V <sub>CE (sat)</sub> 2	2	I <sub>IN</sub> = 1 mA, I <sub>C</sub> = 10 mA	_	—	0.3	v	
			I <sub>IN</sub> = 3 mA, I <sub>C</sub> = 15 0 mA	-	_	0.9		
DC Current Transfer Ratio	h <sub>FE</sub>	2	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 10 mA	50	—	—	—	
Input Voltage	V <sub>IN (ON)</sub>	3	I <sub>IN</sub> = 1 mA, I <sub>C</sub> = 10 mA	6.5	11.5	16.0	V	
Turn-On Delay	t <sub>ON</sub>	4	- 4	V <sub>CEO</sub> = 35 V, R <sub>L</sub> = 220 Ω C <sub>L</sub> = 15 pF	_	100	_	ns
Turn-Off Delay	tOFF			C <sub>L</sub> = 15 pF	_	500	_	115

2. hFE, VCE (sat)

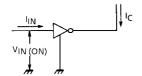
#### **TEST CIRCUIT**

#### 1. ICEX

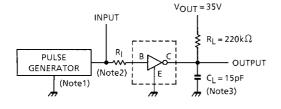


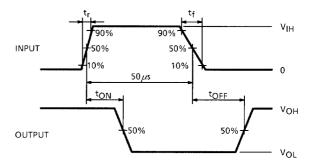


#### 3. VIN (ON)



#### 4. ton, toff





# Note 1: Pulse Width 50 µs, Duty Cycle 10%

Output Impedance 50  $\Omega$ , t<sub>r</sub> ≤ 5 ns, t<sub>f</sub> ≤ 10 ns

Note 2: See below

Input Condition

TYPE NUMBER	RI	VIH
TD62504P-H	0 Ω	10 V

Note 3: CL includes probe and jig capacitance

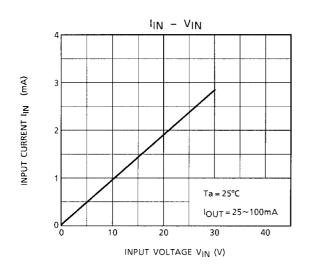
#### **PRECAUTIONS for USING**

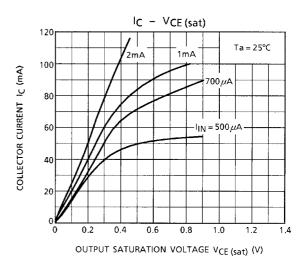
This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

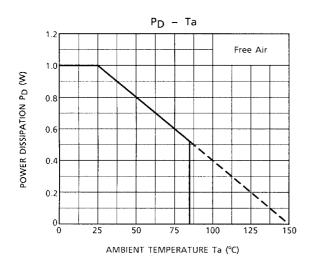
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

# TOSHIBA





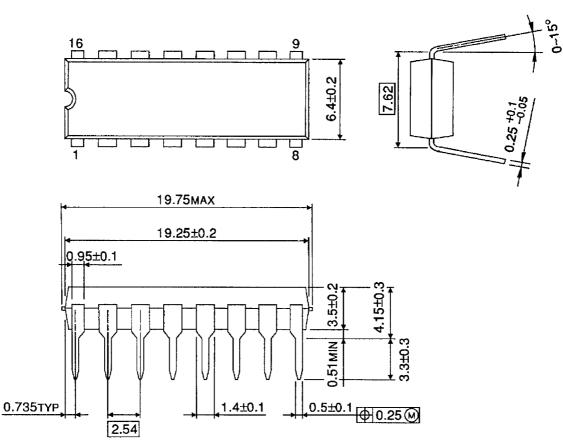


# **TOSHIBA**

# PACKAGE DIMENSIONS

DIP16-P-300-2.54A

Unit: mm



Weight: 1.11 g (Typ.)

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