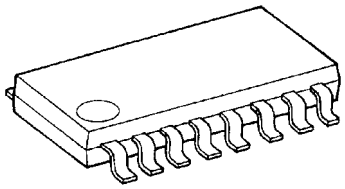
TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

TD62M2701F

LOW SATURATION VOLTAGE H−BRIDGE DRIVER

TD62M2701F is multi−chip H−bridge driver IC incorporates 4 low saturation discrete transistors which equipped bias−resistor and fly−wheel diode. This IC is suitable for forward−reverse control on a battery use motor drive applications.



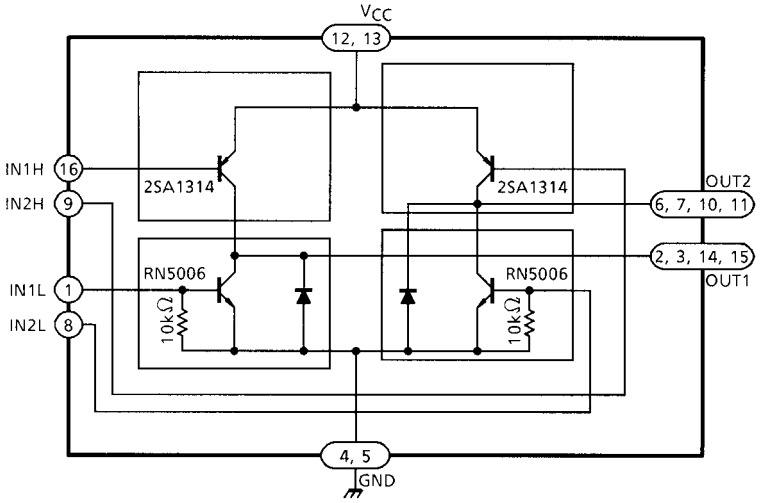
SSOP16-P-225-1.00A

Weight: 0.14 g (Typ.)

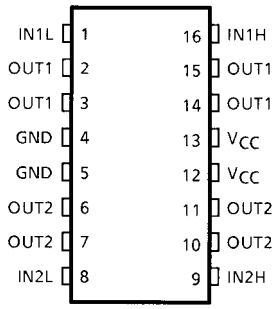
FEATURES

- Suitable for high efficiency motor drive circuit
- Built−in fly−wheel diode (lower side)
- Built−in bias resistor (lower side) : R = 10 kΩ (Typ.)
- SSOP 16 (1 mm pitch) package sealed
- Low saturation voltage
  : $V_{CE\ (sat)}$ (upper + lower)= 0.23 V (Typ.) : I_O = 1 A
  = 0.45 V (Typ.) : I_O = 2 A

BLOCK DIAGRAM



PIN CONNECTION (TOP VIEW)



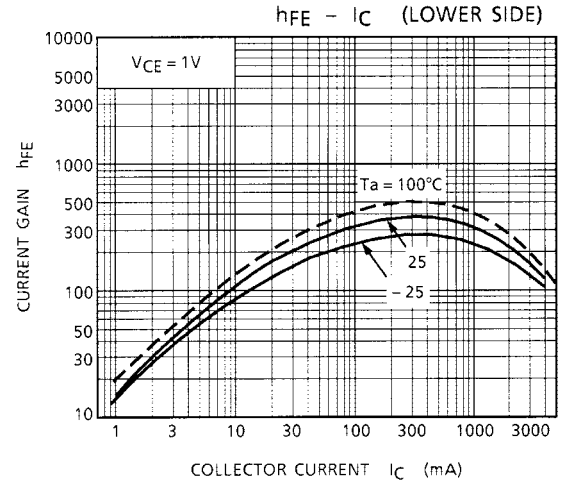
MAXIMUM RATINGS (Ta = 25°C)

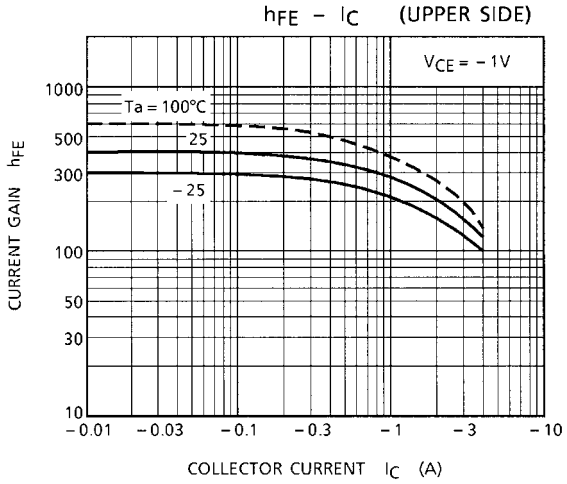
CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{CC}	10	V
Collector−Base Voltage	V_{CBO}	10	V
Collector−Emitter Voltage	V_{CER}	10	V
Emitter−Base Voltage	V_{EBO}	6	V
Output Current	I_{OUT}	2	A
	$I_{O\ (PEAK)}$	4 (Note 1)	
Base Current	I_B	±0.4	A
	$I_{B\ (PEAK)}$	±0.8 (Note 1)	
Diode Forward Current	I_F	2 (Note 2)	A
Power Dissipation	P_D	490	mW
Junction Temperature	T_j	150	°C
Operating Temperature	T_{opr}	−40~85	°C
Storage Temperature	T_{stg}	−55~150	°C

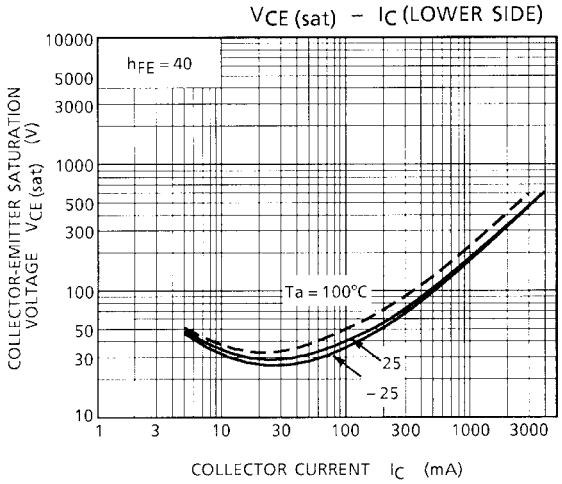
Note 1: T = 10 ms Max. and maximum duty is less than 30%
Note 2: T = 10 ms single pulse

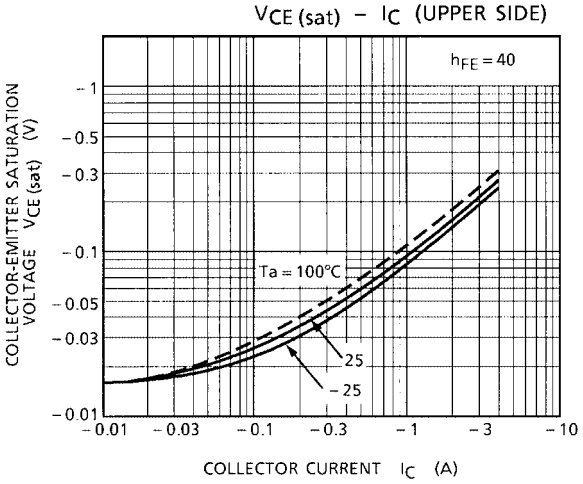
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

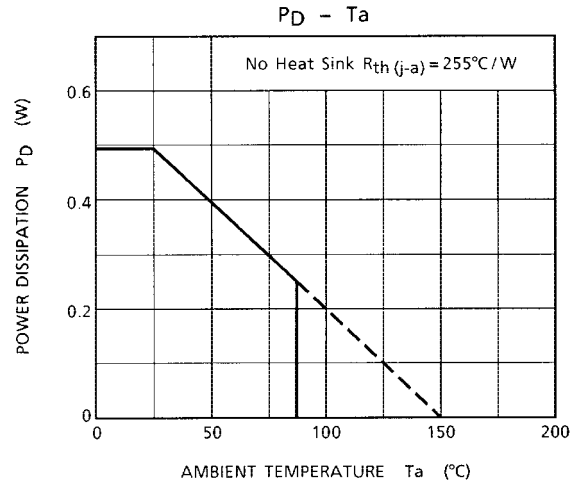
CHARACTERISTIC		SYMBOL	TEST CIR−CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Gain	Upper Side	$h_{FE\ (1)}$	—	V_{CE} = −1 V, I_C = −0.5 A	200	—	700	—
	Lower Side	$h_{FE\ (1)}$	—	V_{CE} = 1 V, I_C = 0.5 A	160	—	700	
		$h_{FE\ (2)}$	—	V_{CE} = 1 V, I_C = 2.0 A	60	130	—	
Output Saturation Voltage	Upper Side	$V_{CE\ (sat)}$	—	I_C = −1A, I_B = −25 mA	—	−0.10	−0.22	V
				I_C = −2A, I_B = −50 mA	—	−0.20	−0.45	
	Lower Side			I_C = 1A, I_B = 25 mA	—	0.13	0.22	
				I_C = 2A, I_B = 50 mA	—	0.25	0.45	
	Summing Total			I_C = 0.5A, I_B = 12.5 mA	—	—	0.20	
				I_C = 1A, I_B = 25 mA	—	0.23	0.42	
				I_C = 2A, I_B = 50 mA	—	0.45	0.85	
Transition Frequency		f_T	—	V_{CE} = 2 V, I_C = 0.5 A	—	150	—	MHz
Output Leakage Current	Upper Side	I_{OL}	—	V_{CC} = −10 V	—	0	−5	μA
	Lower Side			V_{CC} = 10 V	—	0	5	
Diode Forward Voltage (Lower Side)		V_F	—	I_F = 300 mA	—	0.89	1.2	V
				I_F = 450 mA 10 ms	—	1.60	—	
Base−Emitter Resistance		R_{BE}	—	—	7	10	13	kΩ
Base−Emitter Forward Voltage	Upper Side	$V_{BE\ (PNP)}$	—	V_{CE} = −1 V, I_C = −2 A	—	−0.84	−1.5	V
	Lower Side	$V_{BE\ (NPN)}$	—	V_{CE} = 1 V, I_C = 2 A	—	0.84	1.5	

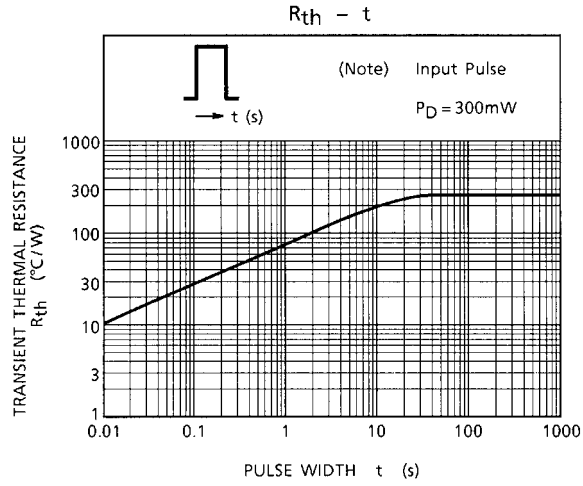










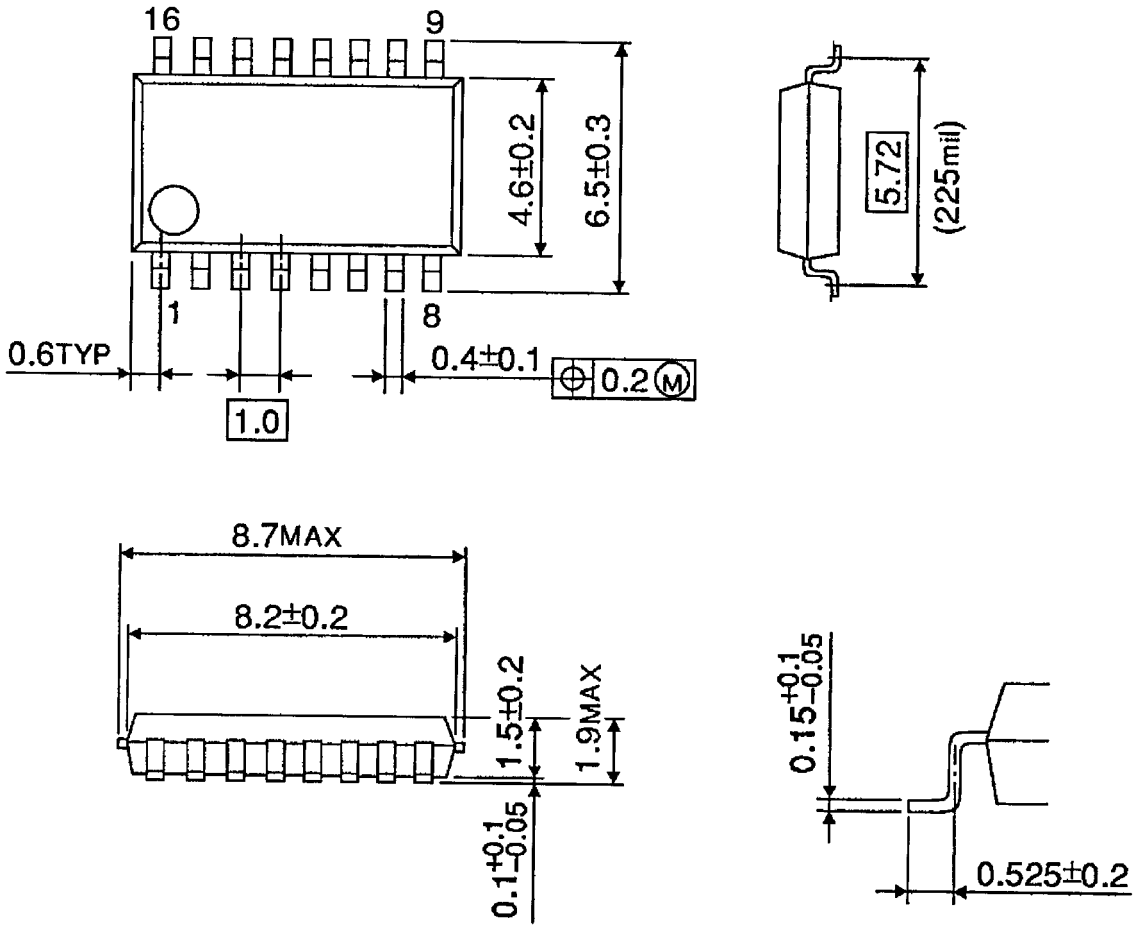


PRECAUTIONS FOR USING

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.
Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short−circuit between outputs, air contamination fault, or fault by improper grounding.

PACKAGE DIMENSIONS

SSOP16−P−225−1.00A Unit: mm



Weight: 0.14 g (Typ.)

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