



# LINEAR INTEGRATED CIRCUIT

## Hi-Fi DUAL PREAMPLIFIER

The TDA2310 is a dual high quality **class A** preamplifier intended for extremely low distortion application in Hi-Fi systems.

The TDA2310 is a monolithic integrated circuit in a 14-lead dual-in-line plastic package and its main features are:

- Very high dynamic range
- Very low distortion
- High open loop bandwidth
- Very low noise
- No pop-noise
- High slew-rate:  $14V/\mu s$  ( $G_v = 30$  dB) -  $50V/\mu s$  ( $G_v = 50$  dB)
- Large output voltage swing
- Single or split supply operation
- Output short circuit protection

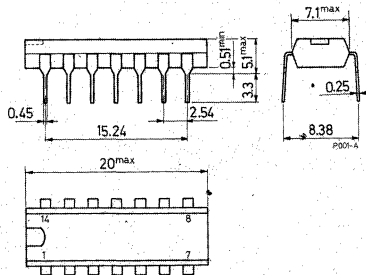
## ABSOLUTE MAXIMUM RATINGS

$V_s$	DC supply voltage	$\pm 22$	V
$V_s$	Operating supply voltage	$\pm 20$	V
$V_{cm}$	Common mode input voltage	$\pm 15$	V
$V_i$	Differential input voltage	$\pm 5$	V
$P_{tot}$	Total power dissipation at $T_{amb} < 60^\circ C$	500	mW
$T_j, T_{stg}$	Junction and storage temperature	-40 to 150	$^\circ C$

ORDERING NUMBER: TDA2310

## MECHANICAL DATA

Dimensions in mm

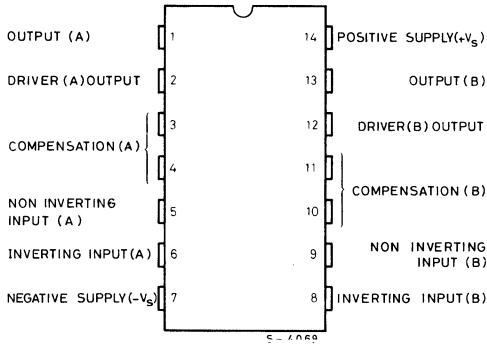




TDA2310

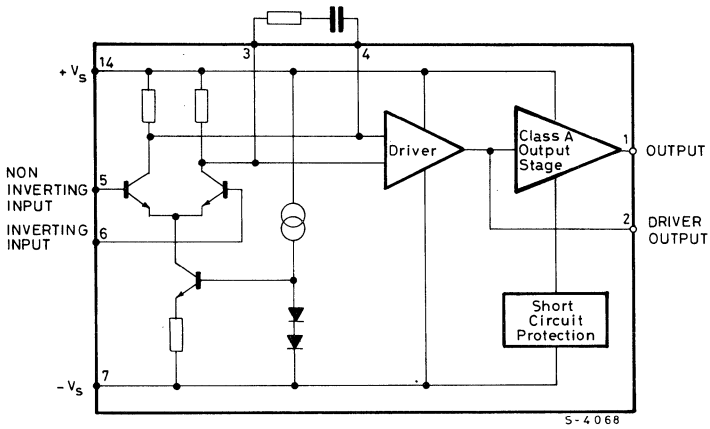
### CONNECTION DIAGRAM

(top view)



### BLOCK DIAGRAM

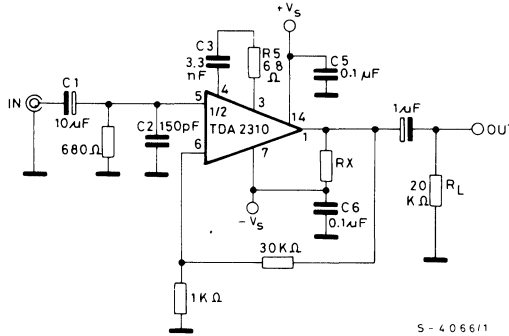
(one section)



**THERMAL DATA**
 $R_{thj-amb}$  Thermal resistance junction-ambient

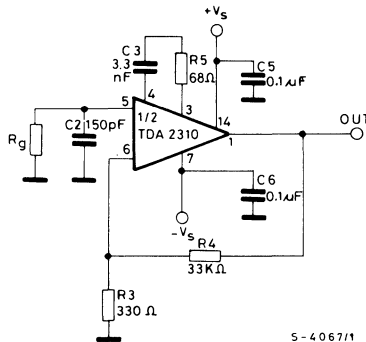
max. 180 °C/W

Fig. 1 - Gain and distortion test



S - 4 0 6 6 / 1

Fig. 2 - Noise test



S - 4 0 6 7 / 1

**ELECTRICAL CHARACTERISTICS** (Refer to the Test circuit of fig. 1,  $T_{amb} = 25^{\circ}\text{C}$ ,  $V_s = \pm 15\text{V}$ ,  $G_v = 30\text{dB}$ ,  $R_L = 20\text{K}\Omega$  unless otherwise specified)

Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_s$ Supply voltage		$\pm 5$		$\pm 20$	V
$I_s$ Supply current			10	15	mA
$I_b$ Input bias current			0.2	1	$\mu\text{A}$
$I_{os}$ Input offset current			50	300	nA
$V_{os}$ Input offset voltage			1	3	mV



TDA2310

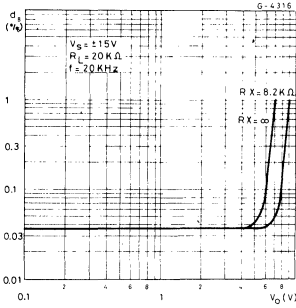
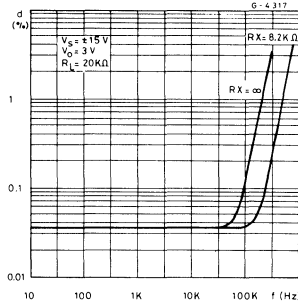
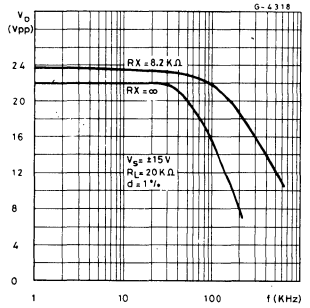
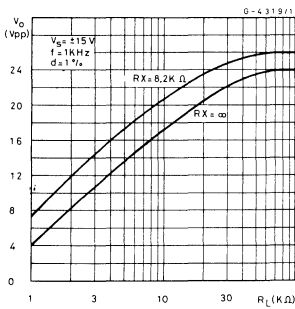
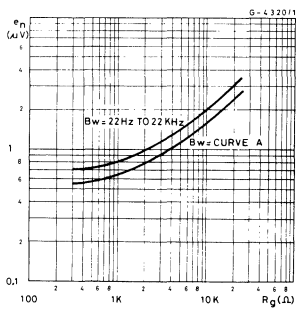
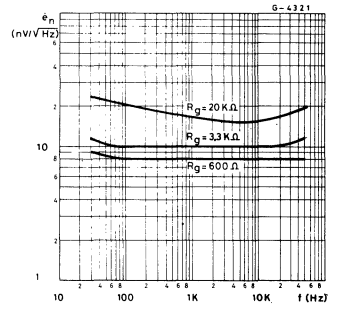
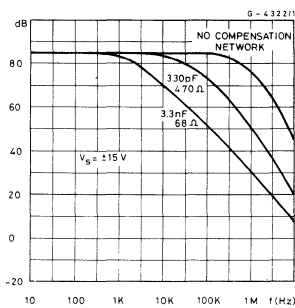
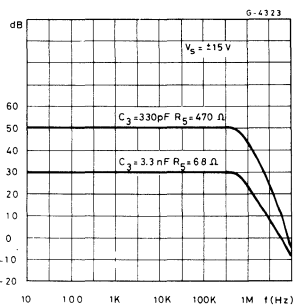
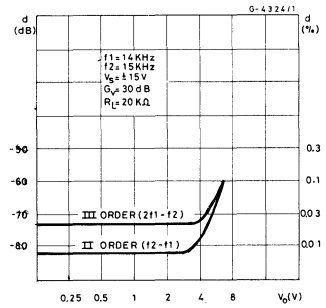
**ELECTRICAL CHARACTERISTICS** (continued)

Parameter		Test conditions		Min.	Typ.	Max.	Unit
G <sub>v</sub>	Voltage gain (open loop)	f = 1KHz	No compensation		85		dB
		f = 20KHz			85		dB
ΔG <sub>v</sub>	Voltage gain spread (closed loop)	f = 1KHz			±0.2		dB
		f = 100KHz			±0.5		dB
R <sub>i</sub>	Input resistance	f = 1KHz			5		MΩ
R <sub>o</sub>	Output resistance				10		Ω
V <sub>pp</sub>	Output voltage swing (peak to peak)	d = 1%	f = 1KHz		24		V
			f = 100KHz		22		V
V <sub>o</sub>	Output voltage (rms)	R <sub>x</sub> = 8.2KΩ	f = 1KHz	6	8		V
			f = 20KHz	6	8		V
RW	Power bandwidth	V <sub>o</sub> = 20 V <sub>pp</sub> , R <sub>x</sub> = 8.2 KΩ			160		KHz
SR	Slew rate	G <sub>v</sub> = 30dB			14		V/μs
		G <sub>v</sub> = 50dB (C <sub>3</sub> = 330pF, R <sub>5</sub> = 470Ω)			50		
d	Total harmonic distortion	V <sub>o</sub> = 3V	f = 1KHz		0.035		%
			f = 20 KHz		0.035		%
d <sub>2</sub>	Second order CCIF intermodulation distortion	V <sub>o1</sub> = 1V V <sub>o2</sub> = 1V	f <sub>2</sub> - f <sub>1</sub> = 1 KHz		0.01	0.1	%
d <sub>3</sub>	Third order CCIF intermodulation distortion	f <sub>1</sub> = 14KHz f <sub>2</sub> = 15KHz	2f <sub>1</sub> - f <sub>2</sub> = 13 KHz		0.03	0.1	%
e <sub>N</sub>	* Total input noise	R <sub>g</sub> = 600Ω R <sub>g</sub> = 3.3KΩ (°)			0.6 1.0	0.8	μV
		R <sub>g</sub> = 600Ω R <sub>g</sub> = 3.3KΩ (°°)			0.75 1.2		μV
S/N	* Signal to noise ratio	V <sub>o</sub> = 500mV	R <sub>g</sub> = 3.3K R <sub>g</sub> = 600 (°) R <sub>g</sub> = 0		74 78 80		dB
			R <sub>g</sub> = 3.3K R <sub>g</sub> = 600 (°°) R <sub>g</sub> = 0		72 76 78		dB
C <sub>s</sub>	Channel separation	f = 20KHz R <sub>g</sub> = 600Ω			100		dB
CMR	Common mode rejection	R <sub>g</sub> = 600Ω			95		dB
SVR	Supply voltage rejection	R <sub>g</sub> = 600Ω			85		dB
I <sub>sh</sub>	Output short circuit current				15		mA

(\*) Test circuit of fig. 2 (G<sub>v</sub> = 40 dB)

(°) BW = curve A

(°°) BW = 22Hz to 22KHz

**Fig. 3 - Harmonic distortion vs. output level.**

**Fig. 4 - Harmonic distortion vs. frequency.**

**Fig. 5 - Output voltage swing vs. frequency.**

**Fig. 6 - Output voltage swing vs. load resistance.**

**Fig. 7 - Total input noise vs. source resistance.**

**Fig. 8 - Noise density vs. frequency.**

**Fig. 9 - Open loop frequency response.**

**Fig. 10 - Closed loop gain vs. frequency.**

**Fig. 11 - Two tone CCIF intermod. distortion.**


## APPLICATION INFORMATION

Fig. 12 - Very low dynamic distortion stereo RIAA preamplifier.

$V_s = \pm 15V$

RIAA frequency response (20Hz to 20KHz) =  $\pm 0.5$  dB

Harmonic distortion = 0.02% ( $f = 20KHz$ )

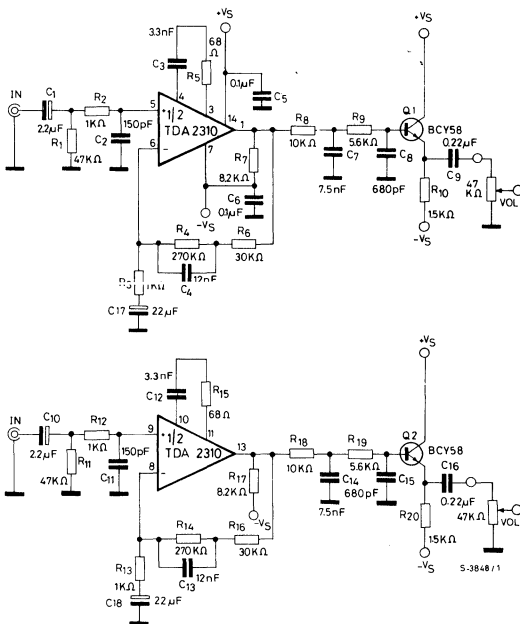


Fig. 13 - RIAA preamplifier response.

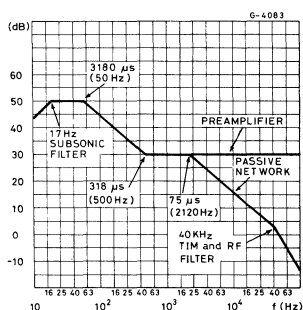


Fig. 14 - Two tone intermodulation distortion vs. input level.

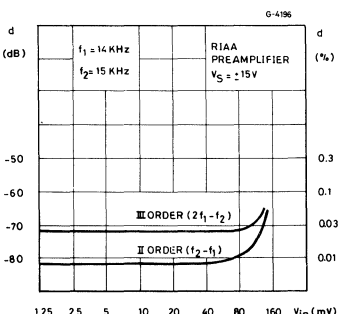
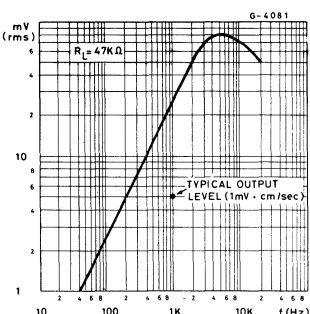
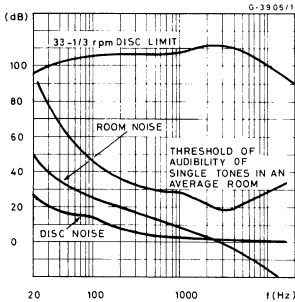


Fig. 15 - Maximum output level of high quality magnetic cartridge vs. frequency.

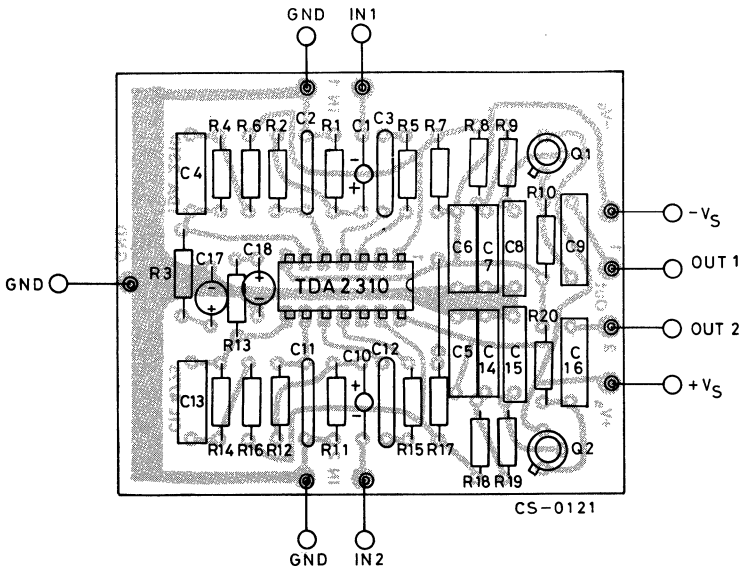


**APPLICATION INFORMATION (continued)**
**Fig. 16 - Dynamic range of disc music.**


As shown in fig. 15 the maximum expected output level of an high quality magnetic cartridge playing modern discs is lower than 80mV rms.

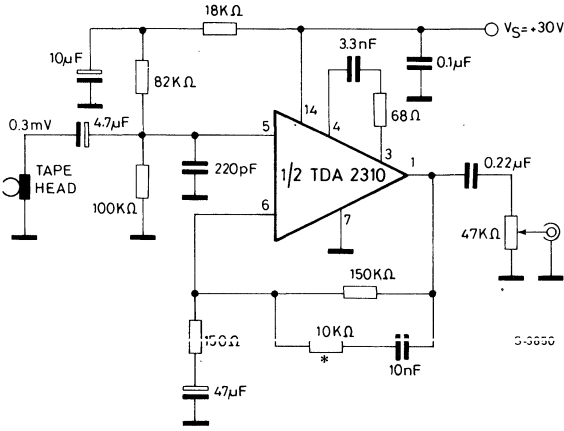
The dynamic range needed is about 70dB (fig. 16).

The TDA2310 is perfectly suited to RIAA preamplifier applications due to the ~100 dB dynamic range (150mV input 0.1% distortion to 1  $\mu$ V noise).

**Fig. 17 - PC board and components layout of RIAA preamplifier (1:1 scale)**


## APPLICATION INFORMATION (continued)

Fig. 18 - Hi-Fi tape preamplifier (EQ. = 70 $\mu$ s).



\* 18K $\Omega$  for EQ = 120 $\mu$ s.

Fig. 19 - Frequency response of graphic equalizer of fig.20

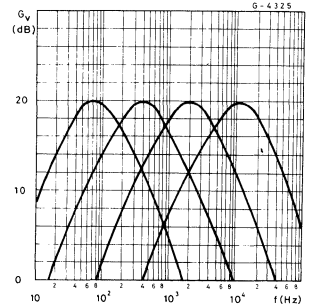
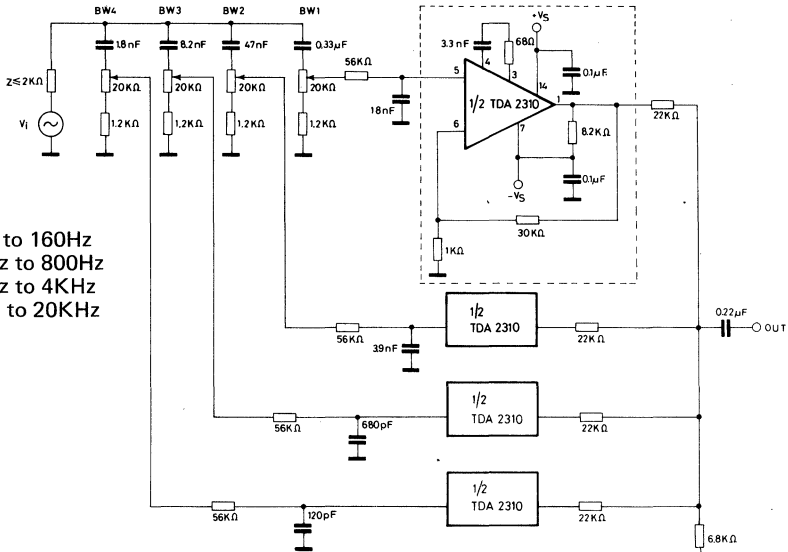


Fig. 20 - Four band graphic equalizer



BW1 = 30Hz to 160Hz  
 BW2 = 160Hz to 800Hz  
 BW3 = 800Hz to 4KHz  
 BW4 = 4KHz to 20KHz



**APPLICATION INFORMATION** (continued)

The table shows the suggested compensation networks depending on the slew-rate and gain required in the application.

Slew-Rate (V/ $\mu$ s)	$G_v$ min. (dB)	Compensation Network	Note
50	50	<p>S-4 071</p>	$R = 470\Omega$ $C = 330pF$ High gain Applications
14	30	<p>S-4 071</p>	$R = 68\Omega$ $C = 3.3nF$ RIAA Preamplifier
14	10	<p>S-4 072/1</p>	$R_1 = 56K\Omega$ $R_2 = 180K\Omega$ $R_3 = 680\Omega$ $C_1 = 10nF$ Inverting Configuration
	0		$R = 68\Omega$ $C = 3.3nF$ $R_1 = R_2 = 56K\Omega$ $R_3 = 680\Omega$ $C_1 = 10nF$
5	20	<p>S-4 071</p>	$R = 33\Omega$ $C = 10nF$ Low Slew-Rate Applications
2	6	<p>S-4 071</p>	$R = 10\Omega$ $C = 47nF$