

P-Channel Enhancement Mode MOSFET

TDM3405

DESCRIPTION

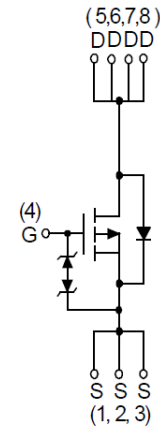
The TDM3405 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

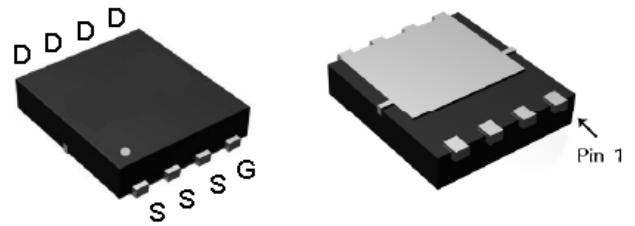
- -40V/-55A
- $R_{DS(ON)} < 15m\Omega$ @ $V_{GS} = -4.5V$
 $R_{DS(ON)} < 9.4m\Omega$ @ $V_{GS} = -10V$
 $R_{DS(ON)} < 8m\Omega$ @ $V_{GS} = -20V$
- Reliable and Rugged
- HBM ESD capability level of 8KV typical
- Lead free product is available
- DFN5X6 Package

Application

- PWM applications
- Load switch
- Power management



P-Channel MOSFET



DFN5x6-8

ABSOLUTE MAXIMUM RATINGS($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current($V_{GS} = -10V$) note1	$I_D (T_A = 25^\circ\text{C})$	-17	A
	$I_D (T_A = 70^\circ\text{C})$	-14	A
300 μs Pulsed Drain Current Tested note1	$I_{DP}(T_A = 25^\circ\text{C})$	-69	A
Continuous Drain Current ($V_{GS} = -10V$) note2	$I_D (T_C = 25^\circ\text{C})$	-55	A
	$I_D (T_C = 100^\circ\text{C})$	-35	A
300 μs Pulsed Drain Current Tested note2	$I_{DP}(T_C = 25^\circ\text{C})$	-222	A
Diode Continuous Forward Current note2	I_S	-27	A
Maximum Power Dissipation note1	$P_D(T_A = 25^\circ\text{C})$	5	W
	$P_D(T_A = 70^\circ\text{C})$	3.2	W
Maximum Power Dissipation note2	$P_D(T_C = 25^\circ\text{C})$	52	W
	$P_D(T_C = 100^\circ\text{C})$	20	W

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Maximum Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to 150	°C
Thermal Resistance-Junction to Ambient note1	$R_{\theta JA}$	60	°C/W
Thermal Resistance-Junction to Case note2	$R_{\theta JC}$	2.4	°C/W

NOTES:

1. Surface Mounted on 1in² pad area, $t \leq 10\text{sec}$. $R_{\theta JA}$ steady state $t = 999\text{s}$.
2. The power dissipation P_D is based on $T_{J(MAX)} = 150^\circ\text{C}$, and it is useful for reducing junction-to-case thermal resistance ($R_{\theta JC}$) when additional heat sink is used.

ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

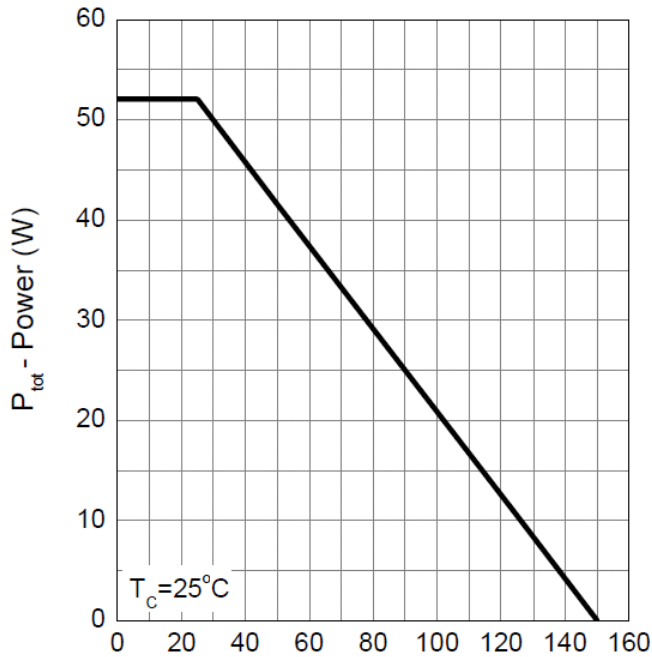
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
STATIC CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-32, V _{GS} =0V	-	-	-1	μ A
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	± 10	μ A
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-1.5	-2	-2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V,I _{DS} =-15A	-	11	15	mΩ
		V _{GS} =-10V, I _{DS} =-25A	-	7.5	9.4	mΩ
		V _{GS} =-20V, I _{DS} =-25A	-	6.6	8	mΩ
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C _{iss}	V _{DS} =-20V,V _{GS} =0V, F=1.0MHz	-	2780	3610	PF
Output Capacitance	C _{oss}		-	425	-	PF
Reverse Transfer Capacitance	C _{rss}		-	330	-	PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-20V, R _L =20Ω, V _{GEN} =-10V, R _G =6Ω I _{DS} =-1A	-	17	30	nS
Turn-on Rise Time	t _r		-	14	25	nS
Turn-Off Delay Time	t _{d(off)}		-	59	106	nS
Turn-Off Fall Time	t _f		-	22	40	nS
Total Gate Charge	Q _g	V _{DS} =-20V,I _{DS} =-25A,V _{GS} =-10V	-	59	83	nC
Gate-Source Charge	Q _{gs}		-	8	-	nC
Gate-Drain Charge	Q _{gd}		-	16	-	nC
Body Diode Reverse Recovery Time	T _{rr}	I _{DS} =-25A, dI/dt=100A/μs	-	23	-	nS
Body Diode Reverse Recovery Charge	Q _{rr}		-	10	-	nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _{SD} =-1A	-	-0.7	-1	V

NOTES:

- 3 Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
- 4 Guaranteed by design, not subject to production testing

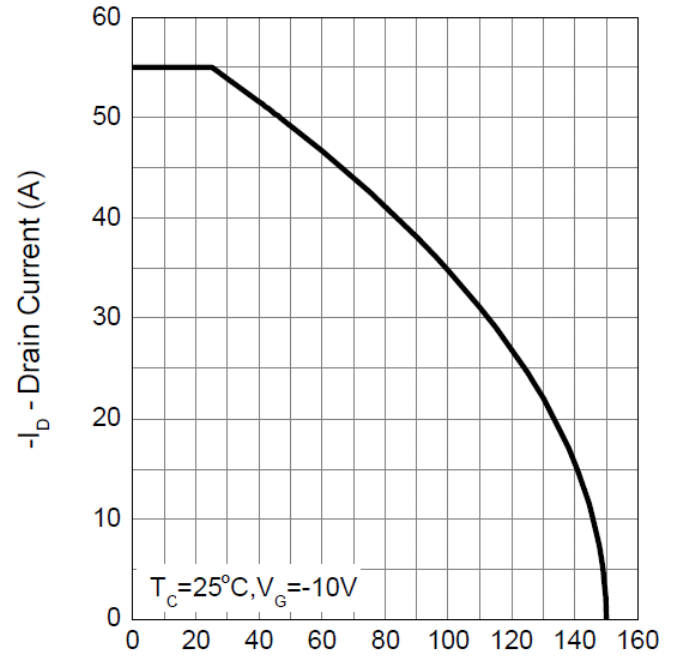
Typical Operating Characteristics

Power Dissipation



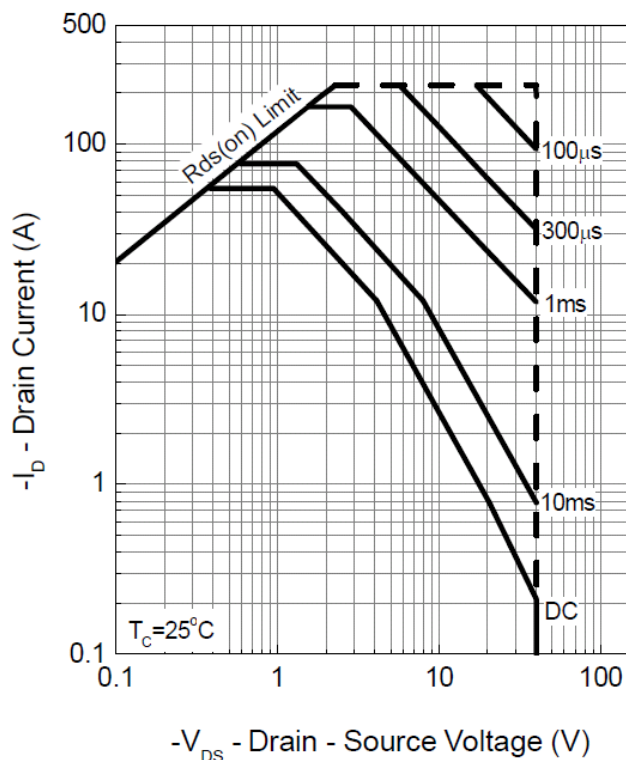
T_j - Junction Temperature ($^{\circ}\text{C}$)

Drain Current



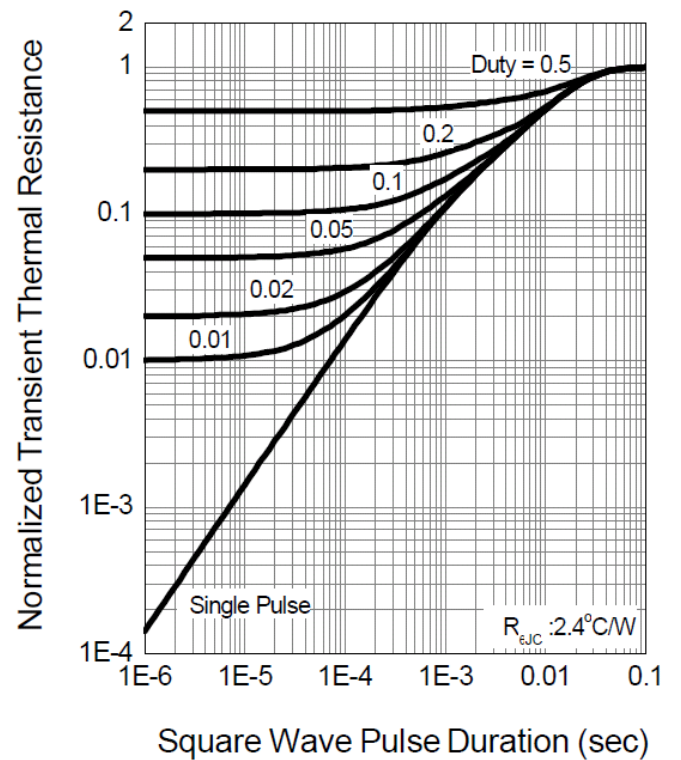
T_j - Junction Temperature ($^{\circ}\text{C}$)

Safe Operation Area



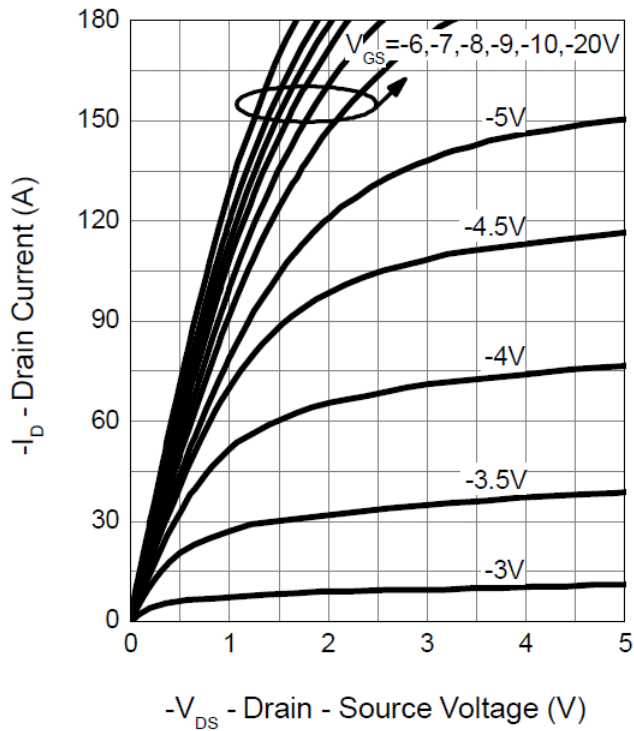
$-V_{DS}$ - Drain - Source Voltage (V)

Thermal Transient Impedance

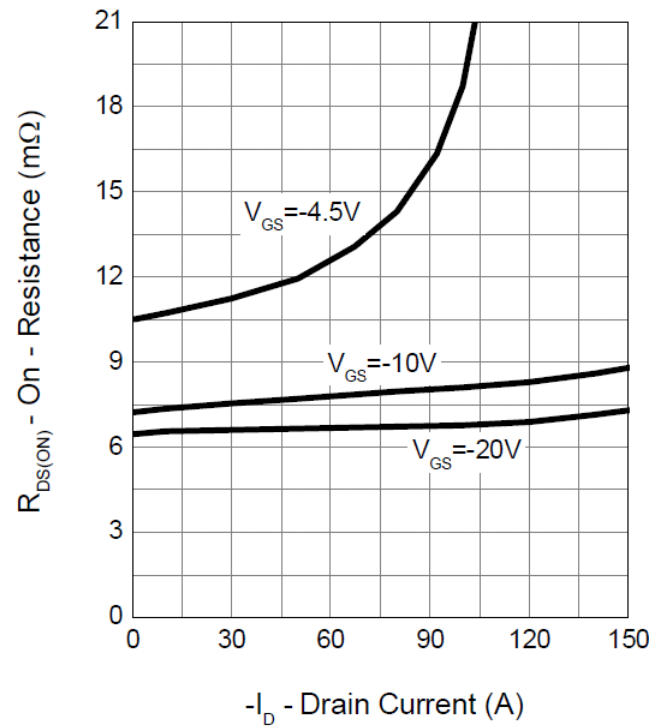


Typical Operating Characteristics(Cont.)

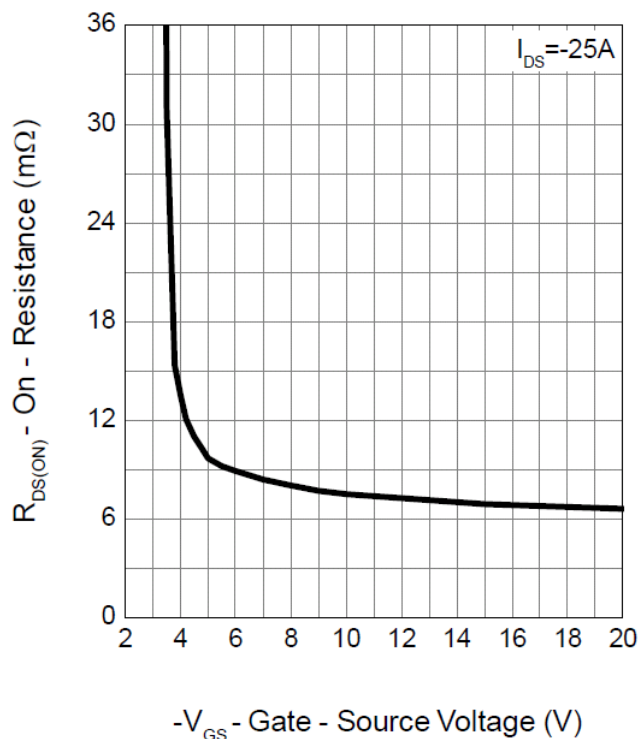
Output Characteristics



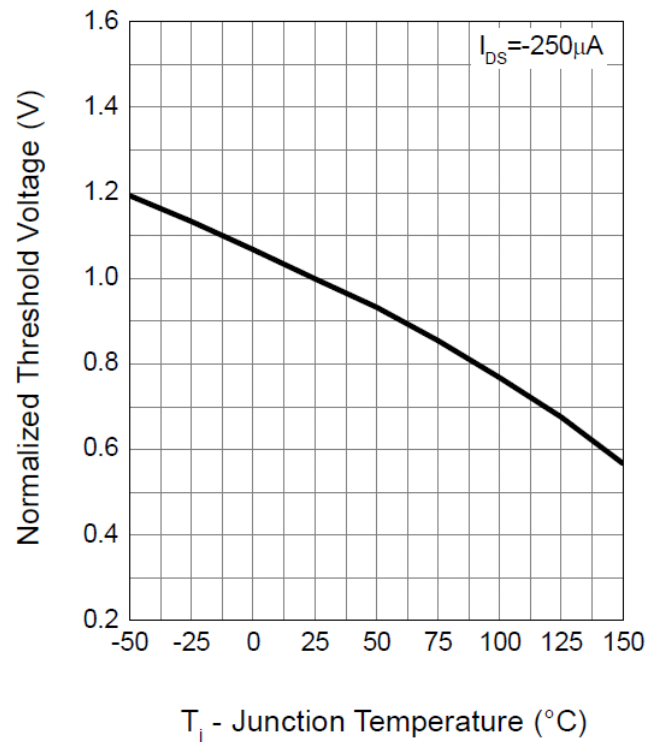
Drain-Source On Resistance



Gate-Source On Resistance

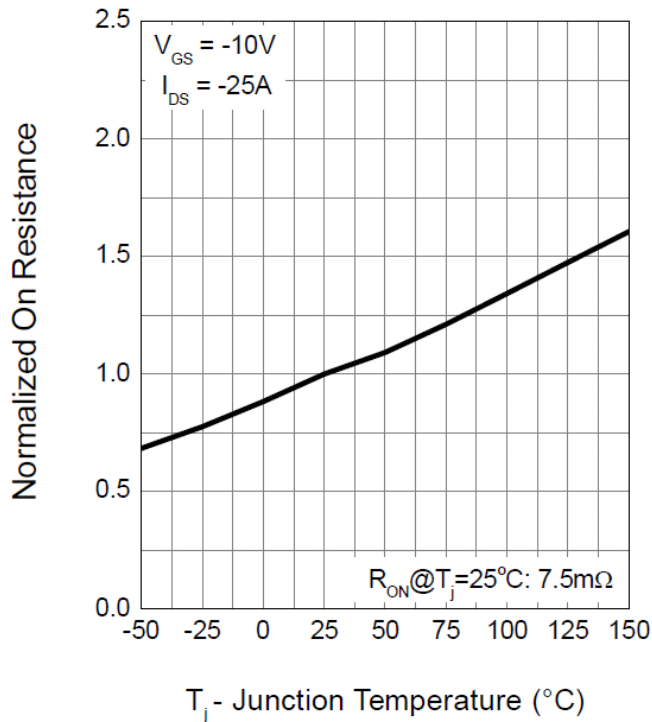


Gate Threshold Voltage

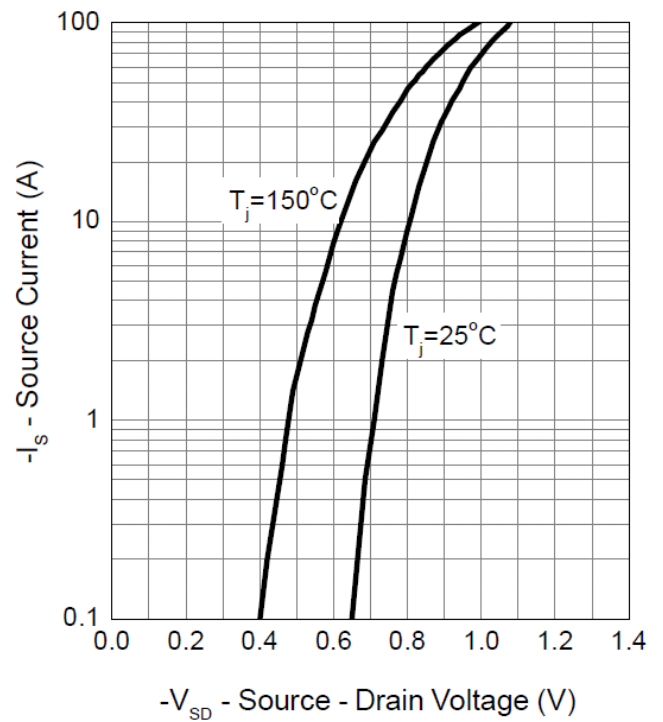


Typical Operating Characteristics (Cont.)

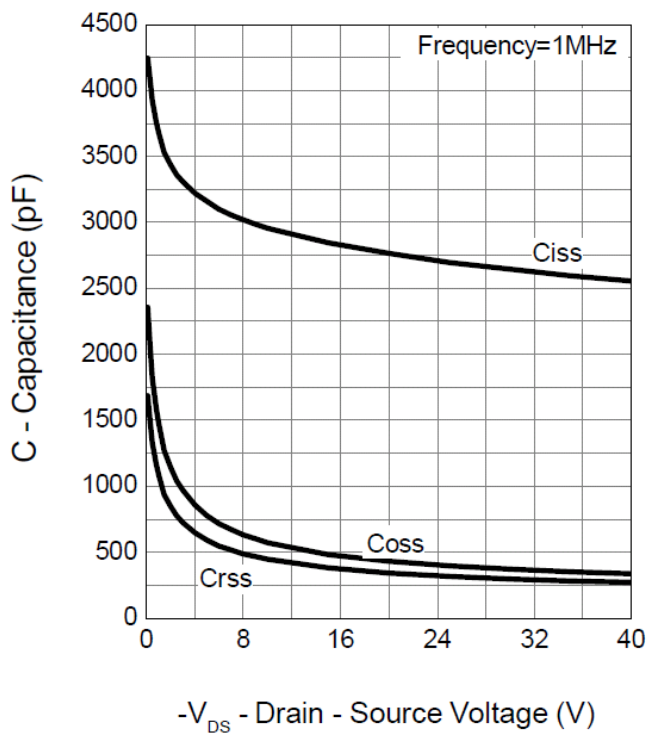
Drain-Source On Resistance



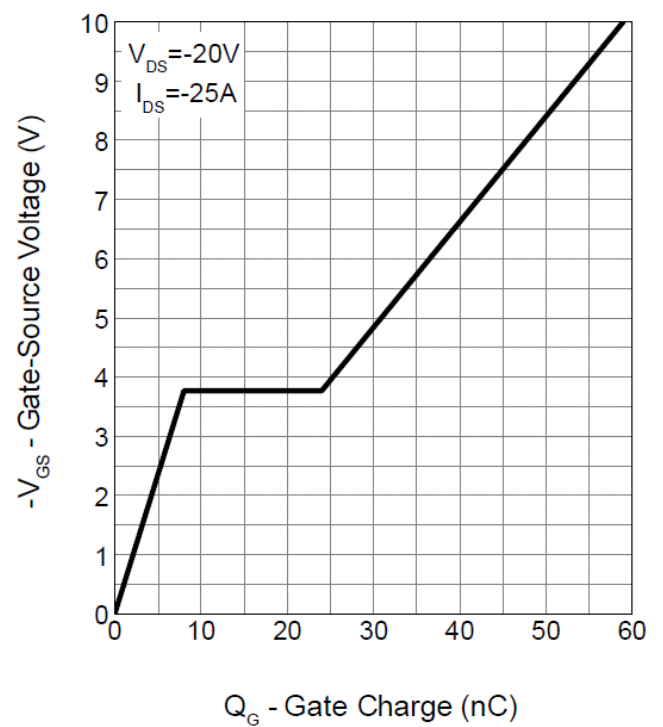
Source-Drain Diode Forward



Capacitance



Gate Charge

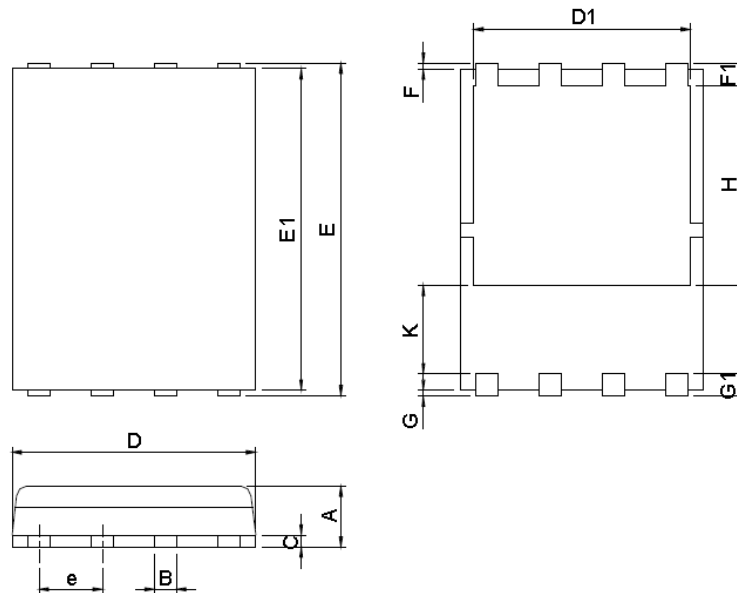


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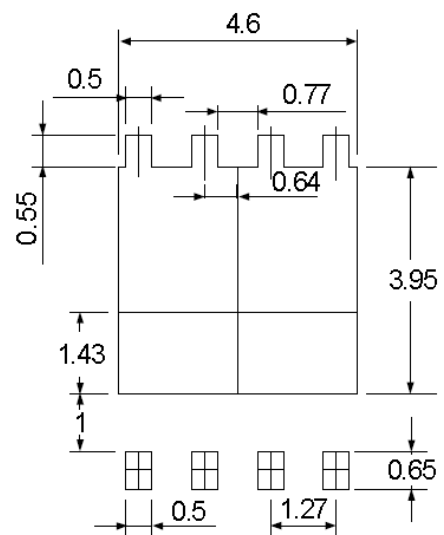
Package Information

DFN5*6-8 Package



DIMENSIONS	DFN5x6-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.20	0.035	0.047
B	0.3	0.51	0.012	0.020
C	0.19	0.25	0.007	0.010
D	4.80	5.30	0.189	0.209
D1	4.00	4.40	0.157	0.173
E	5.90	6.20	0.232	0.244
E1	5.50	5.80	0.217	0.228
e	1.27 BSC		0.050 BSC	
F	0.05	0.30	0.002	0.012
F1	0.35	0.75	0.014	0.030
G	0.05	0.30	0.002	0.012
G1	0.35	0.75	0.014	0.030
H	3.34	3.9	0.131	0.154
K	0.762	-	0.03	-

RECOMMENDED LAND PATTERN



UNIT: mm

Note : 1.Dimension D, D1,D2 and E1 do not include mold flash or protrusions.
Mold flash or protrusions shall not exceed 10 mil.

Design Notes