

N-Channel Enhancement Mode MOSFET

TDM3646

DESCRIPTION

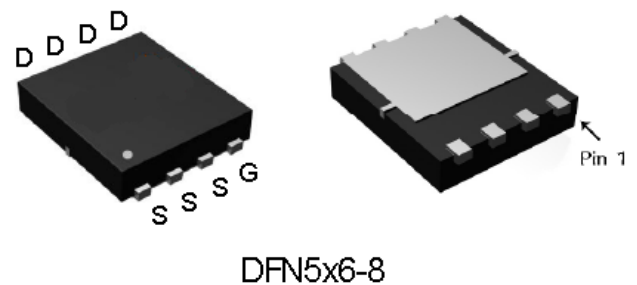
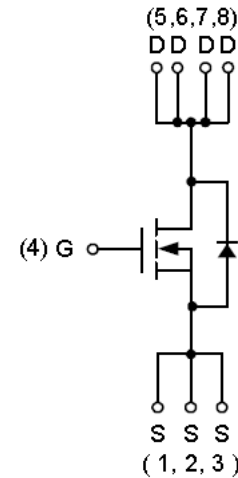
The TDM3646 uses advanced trench technology to provide excellent RDS(ON) and low gate charge. This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

- RDS(ON) < 7mΩ @ VGS=4.5V
RDS(ON) < 5.6mΩ @ VGS=10V
- High Power and current handling capability
- Surface Mount Package
- Lead Free and Green Devices available(RoHS Compliant)

Application

- PWM applications
- Load switch
- Power management
- Motor Control



ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Diode Continuous Forward Current	I _S (T _C =25°C)	40	A
Drain Current @ Continuous(Note 1)	I _D (T _C =25°C)	80	A
	I _D (T _C =100°C)	59	A
Drain Current @ Current-Pulsed (Note 2)	I _{DM} (T _C =25°C)	300	A
Maximum Power Dissipation	P _D (T _C =25°C)	96	W
	P _D (T _C =100°C)	38	W
Drain Current @ Continuous(Note 1)	I _D (T _A =25°C)	13	A
	I _D (T _A =70°C)	11	A
Maximum Power Dissipation	P _D (T _A =25°C)	2	W
	P _D (T _A =70°C)	1.3	W
Maximum Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55 To 150	°C

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THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA} (t _s ≤10s)	20	°C/W
	R _{θJA} (Steady State)	60	°C/W
Thermal Resistance, Junction-to-Case	R _{θJC} (Steady State)	1.3	°C/W

ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

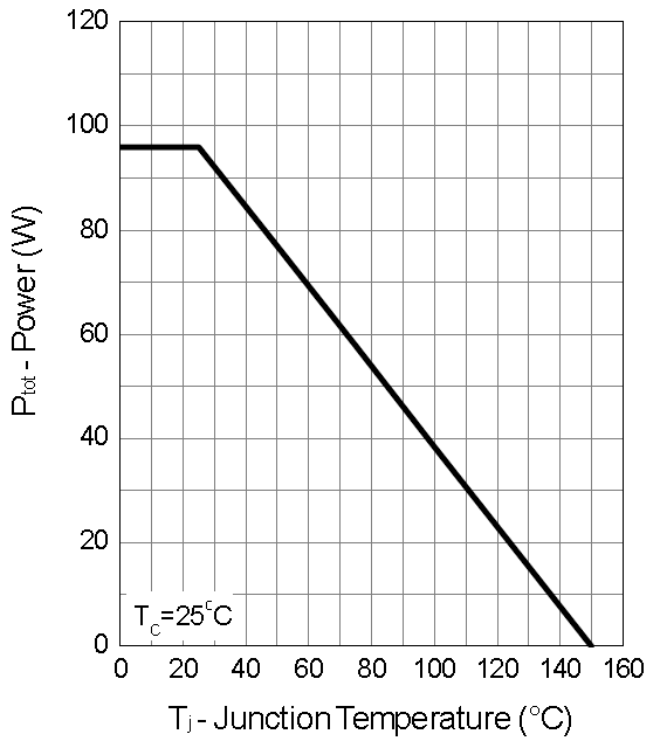
Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =48V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	2	3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =25A	-	5.6	7	mΩ
		V _{GS} =10V, I _D =25A	-	4.9	5.6	mΩ
DYNAMIC CHARACTERISTICS (Note 4)						
Gate Resistance	R _G	V _{DS} =0V, V _{GS} =0V, F=1.0MHz	-	1.0	-	Ω
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V, F=1.0MHz	-	4347	6100	PF
Output Capacitance	C _{oss}		-	428	-	PF
Reverse Transfer Capacitance	C _{rss}		-	220	-	PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DS} =30V, R _L =30Ω, V _{GEN} =10V, R _G =6Ω I _D =1A	-	25	45	nS
Turn-on Rise Time	t _r		-	12	22	nS
Turn-Off Delay Time	t _{d(off)}		-	90	162	nS
Turn-Off Fall Time	t _f		-	38	69	nS
Total Gate Charge	Q _g	V _{DS} =30V, I _D =30A, V _{GS} =10V	-	83	117	nC
Gate-Source Charge	Q _{gs}		-	17	-	nC
Gate-Drain Charge	Q _{gd}		-	15	-	nC
Body Diode Reverse Recovery Time	T _{rr}	I _F =30A, di/dt=100A/μs	-	33	-	nS
Body Diode Reverse Recovery Charge	Q _{rr}		-	41	-	nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =20A	-	0.8	1.3	V

NOTES:

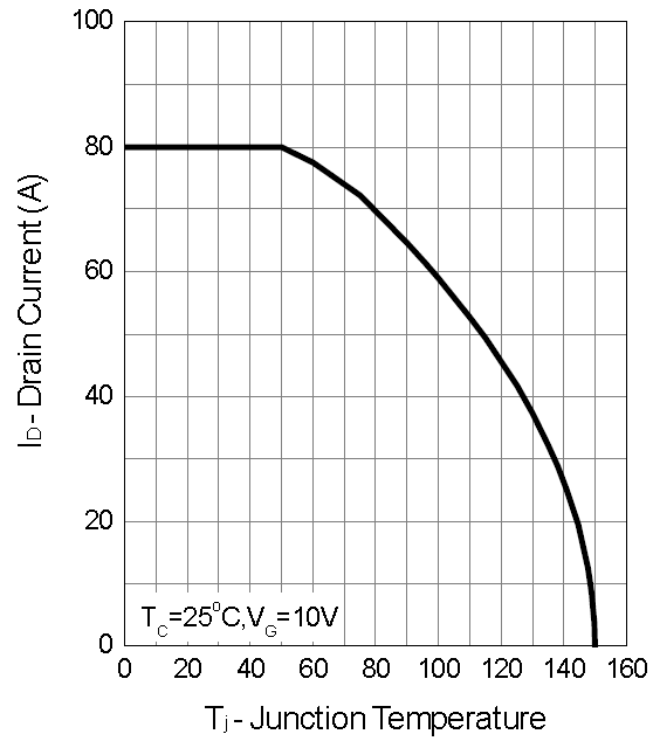
1. Current limited by bonding wire
2. Pulse width limited by max. Junction temperature.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production testing

Typical Operating Characteristics

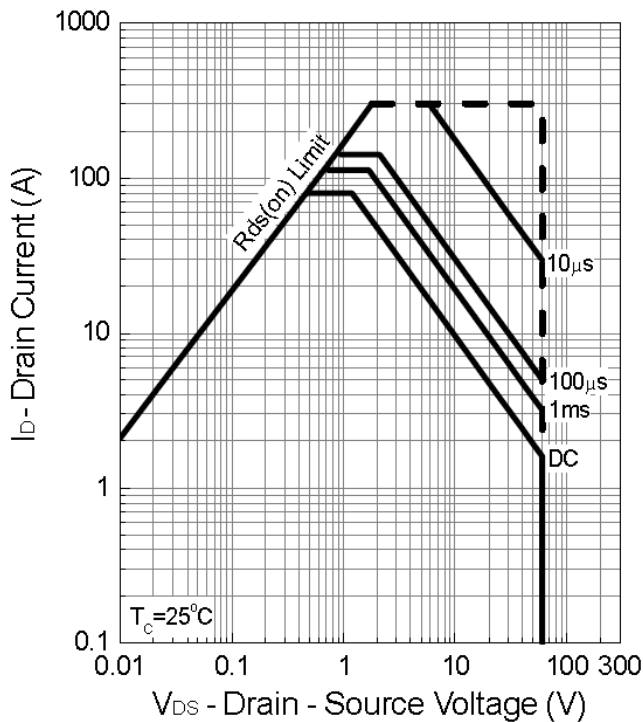
Power Dissipation



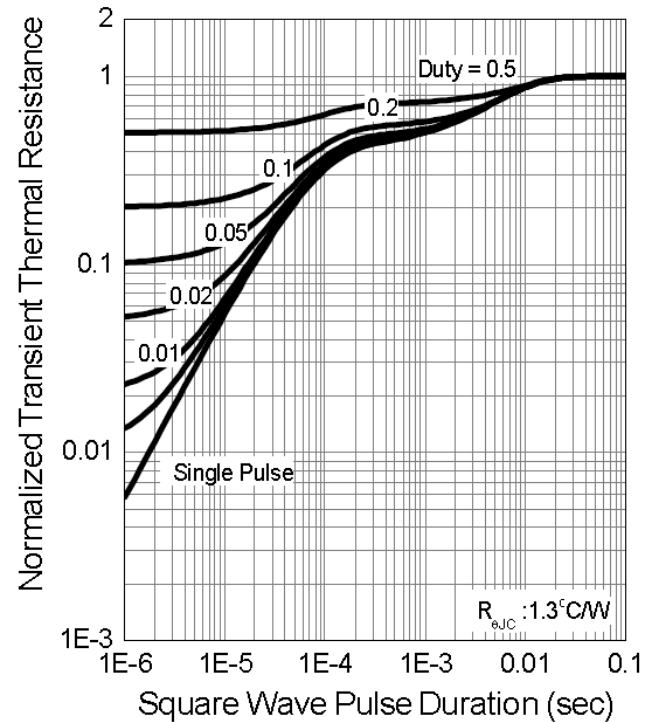
Drain Current



Safe Operation Area

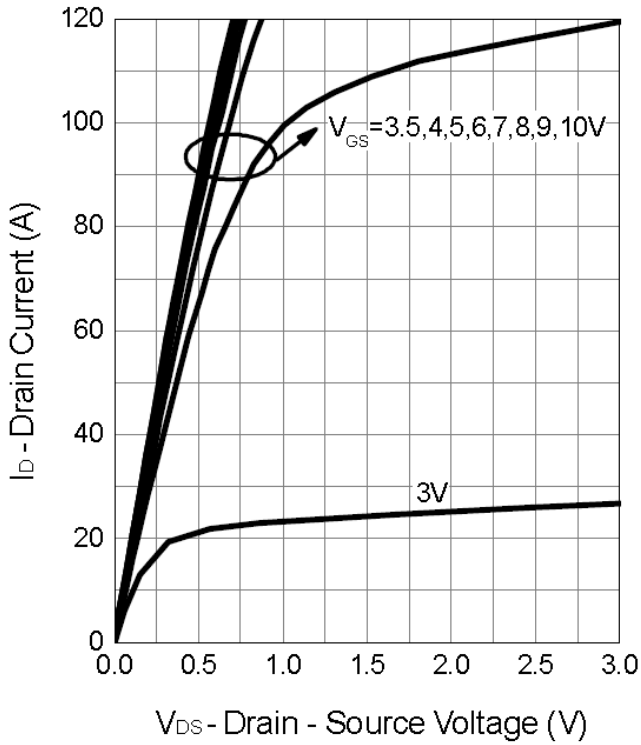


Thermal Transient Impedance

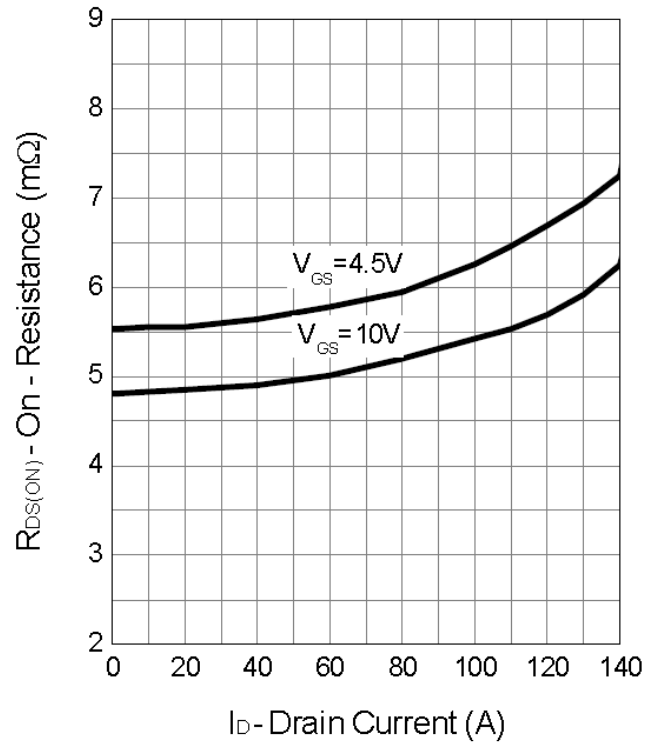


Typical Operating Characteristics(Cont.)

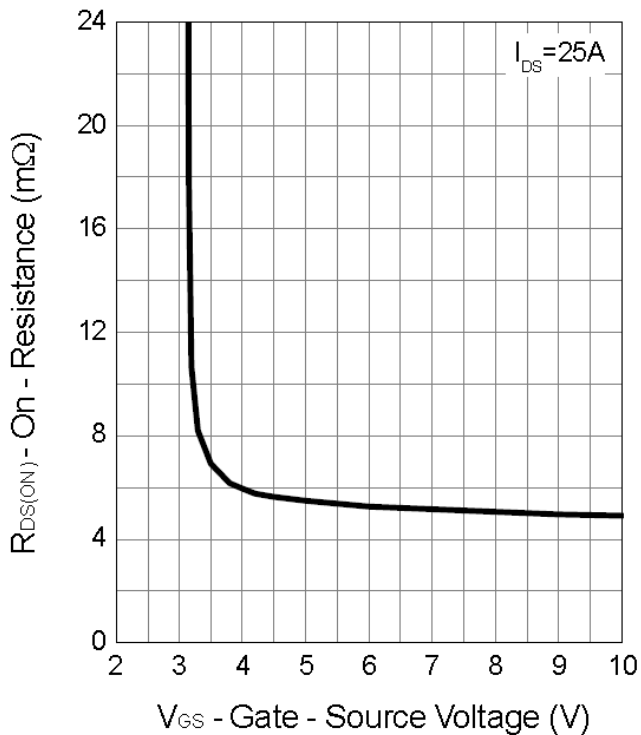
Output Characteristics



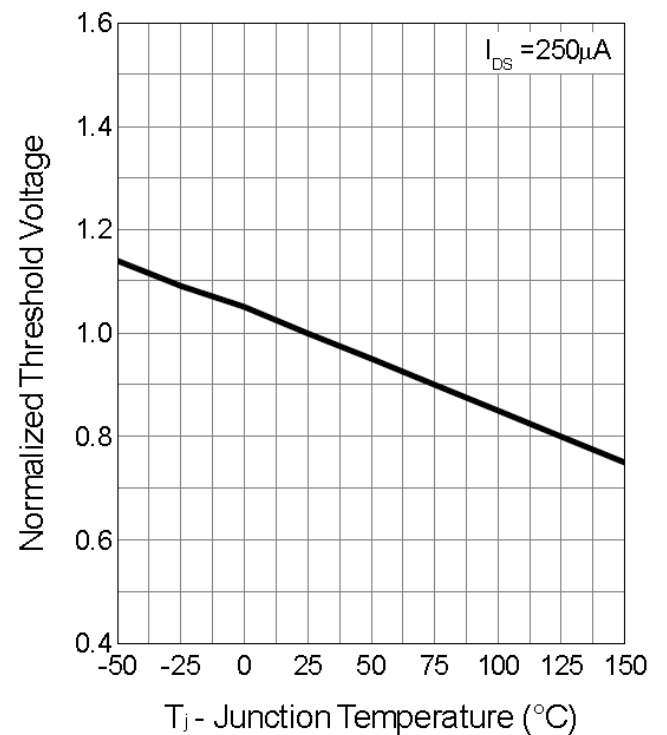
Drain-Source On Resistance



Gate-Source On Resistance

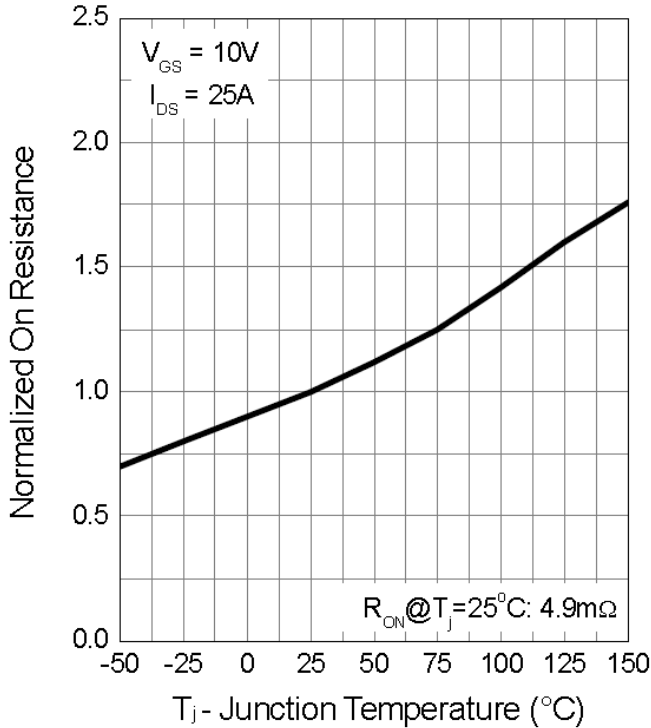


Gate Threshold Voltage

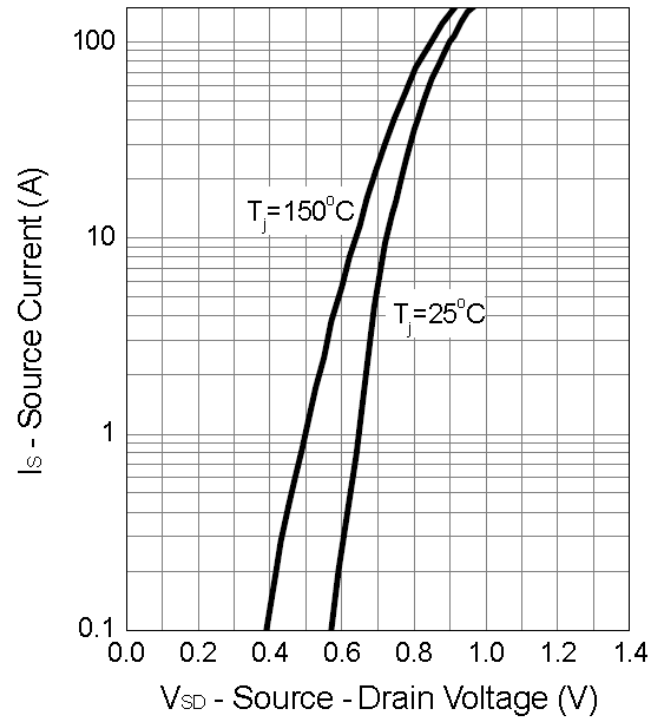


Typical Operating Characteristics (Cont.)

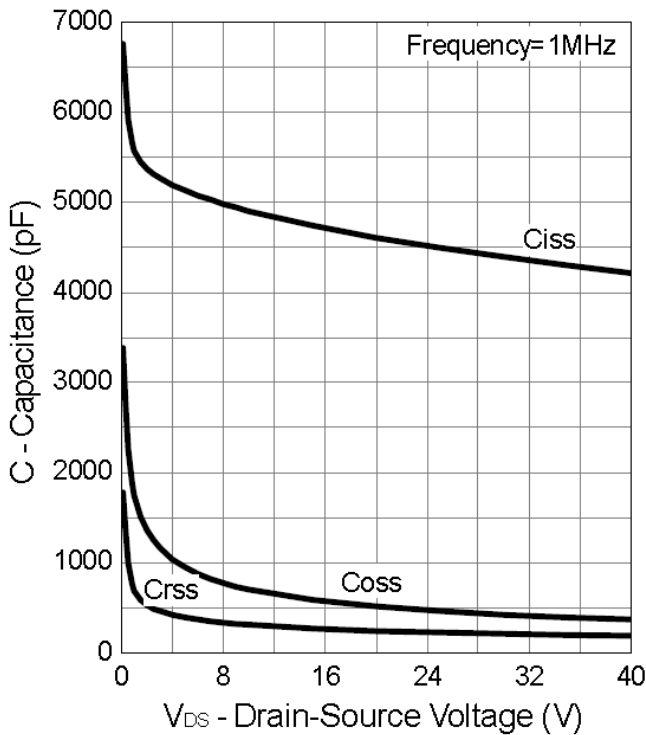
Drain-Source On Resistance



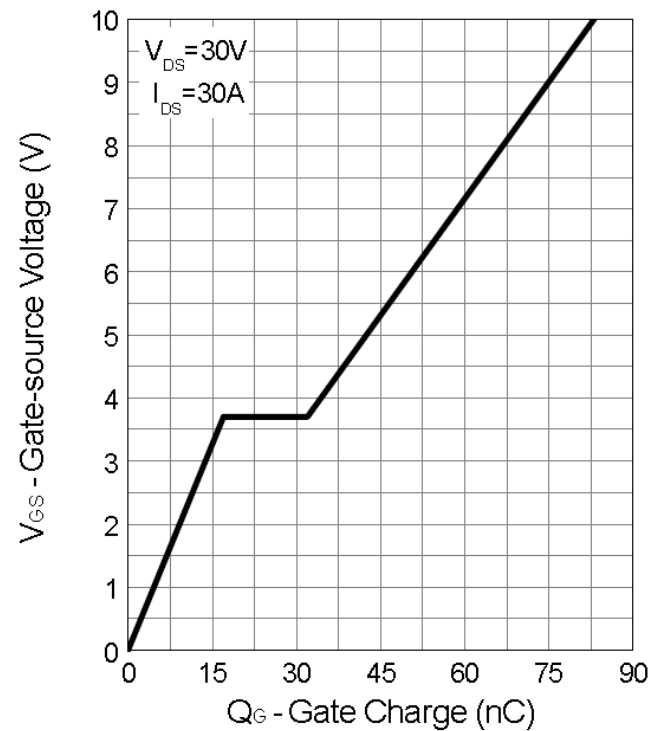
Source-Drain Diode Forward



Capacitance



Gate Charge



Design Notes