



P-Channel 20-V (D-S) MOSFET

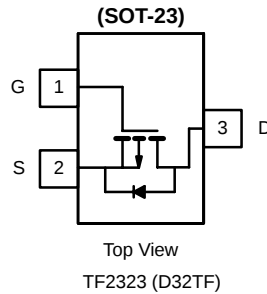
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-20	0.043 @ $V_{GS} = -4.5$ V	-4.0
	0.054 @ $V_{GS} = -2.5$ V	-4.0
	0.075 @ $V_{GS} = -1.8$ V	-2.0

FEATURES

- TrenchFET® Power MOSFET

APPLICATIONS

- Load Switch
- PA Switch



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	5 sec	Steady State	Unit
Drain-Source Voltage		V _{DS}	-20		V
Gate-Source Voltage		V _{GS}	±8		
Continuous Drain Current (T _J = 150°C) ^{a, b}	T _A = 25°C	I _D		-4.0	A
Pulsed Drain Current		I _{DM}	-20		
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	-1.0	-0.6	
Maximum Power Dissipation ^{a, b}	T _A = 25°C	P _D	1.25	0.75	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 5$ sec	R_{thJA}	75	100	$^\circ\text{C/W}$
	Steady State		120	166	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	40	50	

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
b. Pulse width limited by maximum junction temperature.



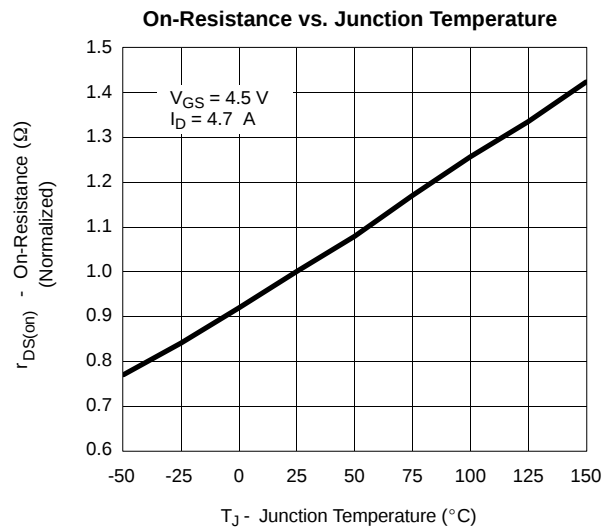
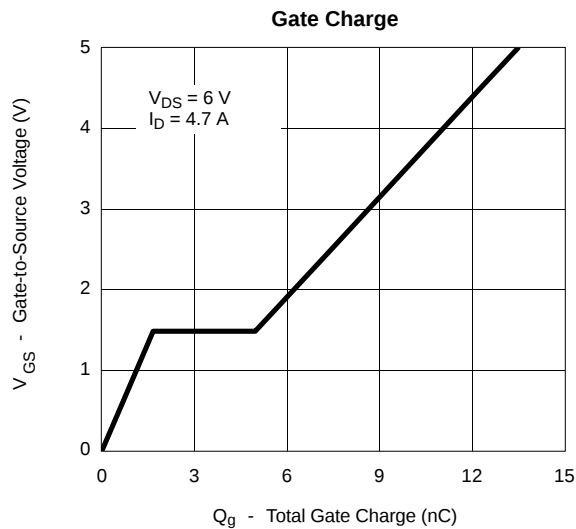
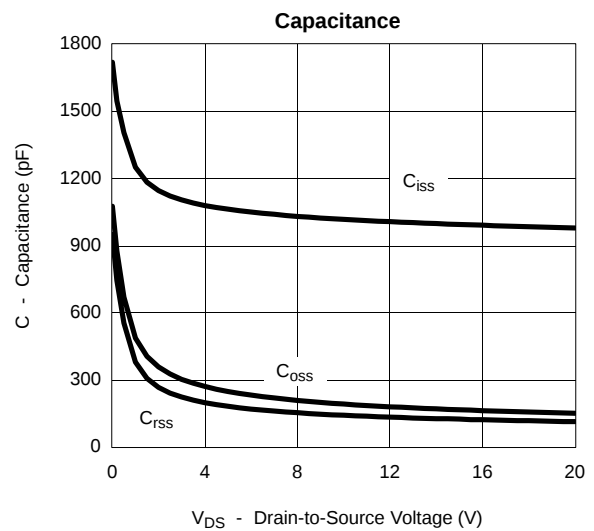
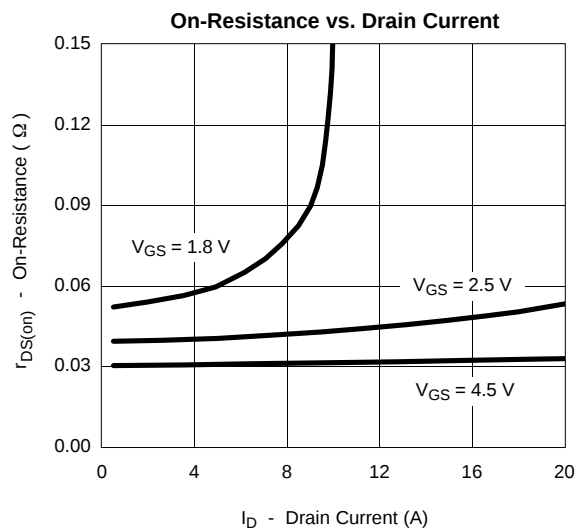
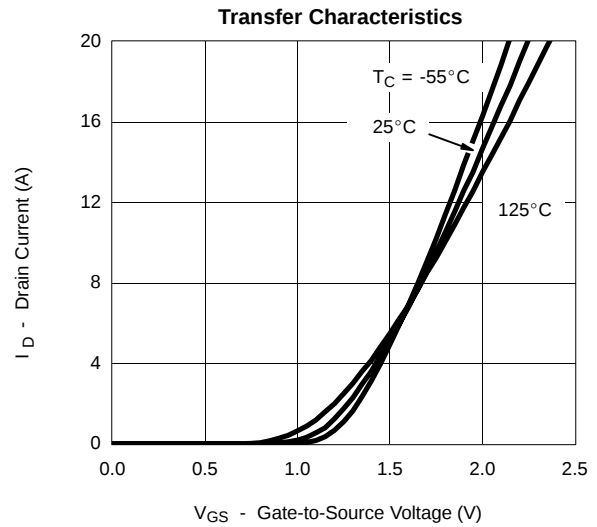
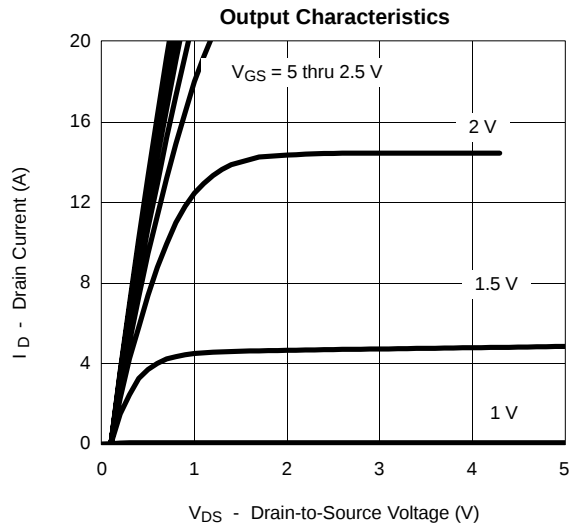
SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = -250 μA	-20			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-0.4		-0.8	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -16 V, V _{GS} = 0 V			-50	nA
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≤ -5 V, V _{GS} = -4.5 V	-20			A
Drain-Source On-Resistance ^a	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -4.0 A		0.041	0.043	Ω
		V _{GS} = -2.5 V, I _D = -4.0A		0.052	0.054	
		V _{GS} = -1.8 V, I _D = -2.0 A		0.073	0.075	
Forward Transconductance ^a	g _{fs}	V _{DS} = -5 V, I _D = -4.0 A		16		S
Diode Forward Voltage	V _{SD}	I _S = -0.5A, V _{GS} = 0 V			-1.28	V
Dynamic ^b						
Total Gate Charge	Q _g	V _{DS} = -10 V, V _{GS} = -4.5 V I _D ≡ -4.0 A		12.5	19	nC
Gate-Source Charge	Q _{gs}			1.7		
Gate-Drain Charge	Q _{gd}			3.3		
Input Capacitance	C _{iss}	V _{DS} = -10 V, V _{GS} = 0, f = 1 MHz		1020		pF
Output Capacitance	C _{oss}			191		
Reverse Transfer Capacitance	C _{rss}			140		
Switching ^c						
Turn-On Time	t _{d(on)}	V _{DD} = -10 V, R _L = 10 Ω I _D ≡ -1.0 A, V _{GEN} = -4.5 V R _G = 6 Ω		25	40	ns
	t _r			43	65	
Turn-Off Time	t _{d(off)}			71	110	
	t _f			48	75	

Notes

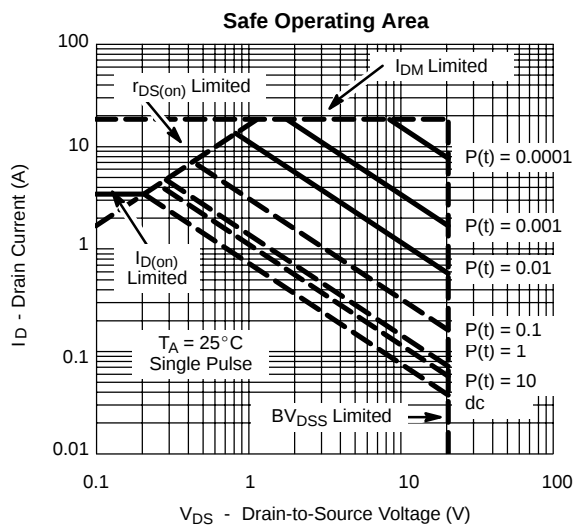
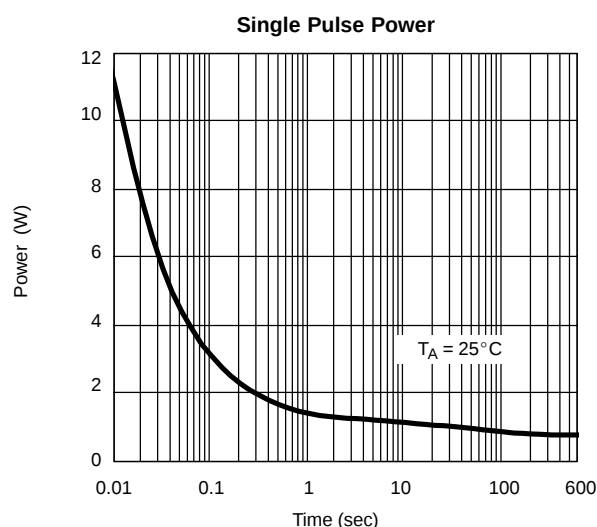
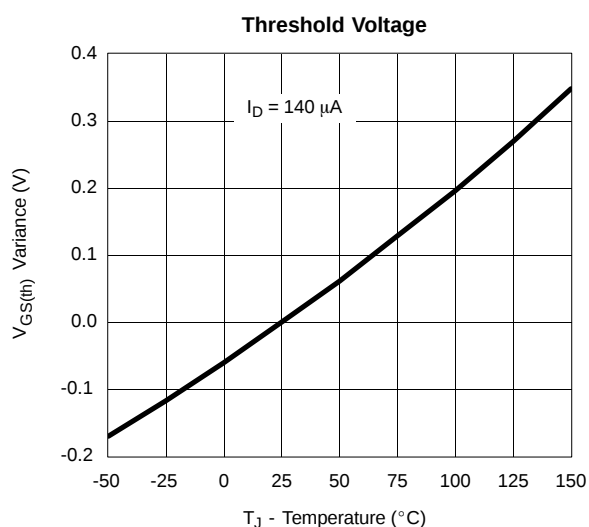
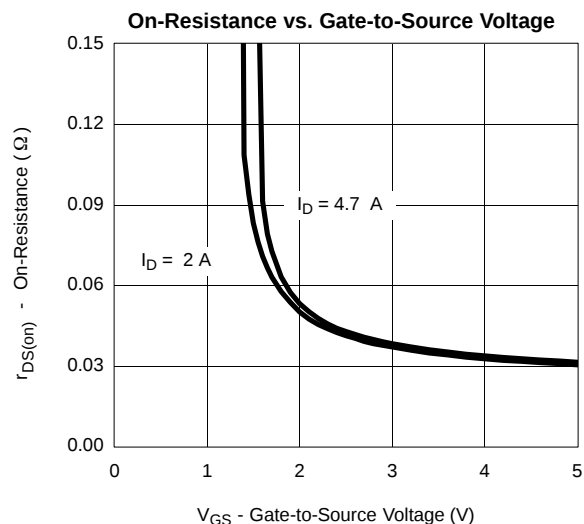
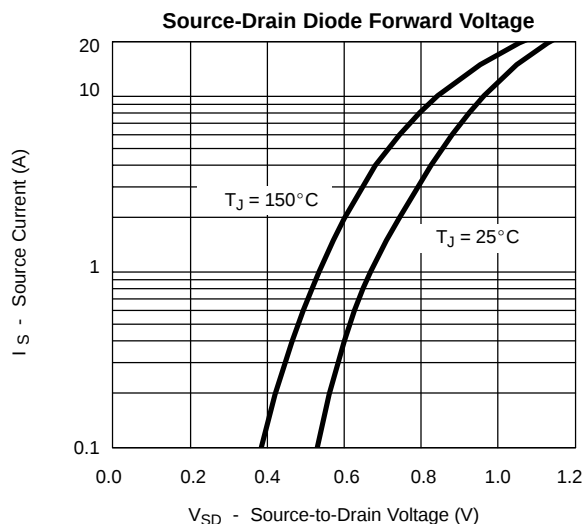
- Pulse test: PW ≤ 300 μs duty cycle ≤ 2%.
- For DESIGN AID ONLY, not subject to production testing.
- Switching time is essentially independent of operating temperature.



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



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