

# SPECIFICATION FOR TFT MODULE

**MODULE NO. : TFT-0180** 

# **Customer Approval:**

	SIGNATURE	DATE
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APPROVED BY	Xeon Wang	2016.06.28

# **REVISION RECORD**

REV NO.	REV DATE	CONTENTS	REMARKS
0	2015-08-12	First release	Preliminary
А	2016-05-13	Modify the Interface Type	Page4
В	2016-06-28	Add the typical value of LED life time	Page4

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### 1. GENERAL INFORMATION

No.	Item	Contents	Unit
1	LCD size	8.0 inch	1
2	LCD type	IPS/TRANSMISSIVE/ Normal Black	1
3	View direction	FREE	1
4	Gray scale inversion direction	FREE	1
5	Resolution	800*3(RGB)*1280	1
6	Module size (W*H*T)	114.5*184*2.6	mm3
7	Active area (W*H)	107.64*172.22	mm2
8	Pixel pitch (W*H)	0.13455*0.13455	mm2
9	Interface Type	MIPI Interface	1
10	Module Power consumption	TBD	w
11	Back light Type	LED	1
12	Driver IC	HX8394-D	1
13	Weight	TBD	g

# 2. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Max	Unit
Power supply voltage(LCD Module)	VDD	-0.3	3.6	V
Back-light current (normal temp.)	ILED	-	75	mA
Operation temperature	Тор	-20	70	°C
Storage temperature	Tst	-30	80	°C
Humidity	RH	-	90%(Max60 °C)	RH

# 3. ELECTRICAL CHARACTERISTICS

### DC CHARACTERISTICS (at Ta=25°C)

Item	Symbol	Min	Тур	Max	Unit	Note
Power Supply Input Voltage	VDD	2.5	3.3	3.6	V	
I/O logic voltage	VDDI	-	-	-	V	=VDD
Power Supply Current	IVDD	-	TBD	-	V	
Input voltage 'H' level	VIH	0.8VDDI	-	VDDI	V	
Input voltage 'L' level	VIL	0	-	0.2VDDI	٧	
TFT gate on voltage	VGH		-		V	
TFT gate off voltage	VGL		-		V	
Analog power supply voltage	AVDD		-		V	
Differential input common mode voltage	Vcom		-		V	

Note1: The value is just the reference value. The customer can optimize the setting value by the different D-IC Vcom must be adjusted to optimize display quality, as Crosstalk and Contrast Ratio etc..

#### 4. BACKLIGHT CHARACTERISTICS

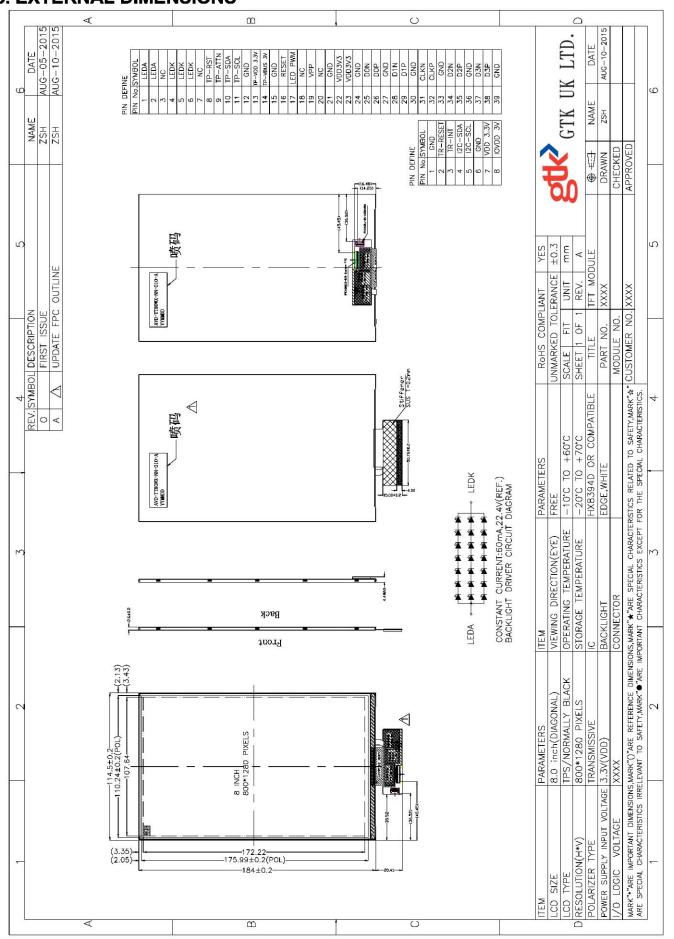
(at Ta=25°C RH=60%)

(at 1a-25 0, k11-00 %)						
Item	Symbol	Min.	Тур.	Max.	Unit	Note
LED Forward Voltage	VF	21	22	23.3	V	IF=60mA
LED Forward Current	IF	-	60	-	mA	
LED power consumption	PLED	-	1.32	-	W	Note1
Number of LED	-		21		PCS	
Connection mode	-	7 in series * 3 in parallel		1		
LED Life-Time	_	20000	30000	-	Hrs	Note2

Note1.Calculator Value for reference ILED×VLED×LED Quantity = PLED

Note2. The LED Life-time define as the estimated time to 50% degradation of initial brightness at Ta=25 $^{\circ}$ C and IF =60mA. The LED lifetime could be decreased if operating IF is larger than 60mA.

### 5. EXTERNAL DIMENSIONS



#### 6. ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Тур	Max	Unit	Remark	Note
Response time	Tr+ Tf		-	30	40	ms	Fig.1	4
Contrast ratio	Cr	] <del>-</del>	640	800	-		FIG 2.	1
Surface Luminance	Lv	⊗ = 0°	280	320	-	cd/m2	FIG 2.	2
Luminance uniformity	-		75	80	-	%	FIG 2.	3
NTSC	-		-	60	-	%	FIG 2.	5
	θ	Ø = 90°	70	80	-	deg	FIG 3.	
Viewing angle		Ø = 270°	70	80	-	deg	FIG 3.	3.
range			70	80	-	deg	FIG 3.	
		Ø = 180°	70	80	-	deg	FIG 3.	
	Red x		TBD	TBD	TBD	-		
	Red y		TBD	TBD	TBD	-		
	Green x	θ=0°	TBD	TBD	TBD	-	1	
CIE (x, y)	Green y	Ø=0°	TBD	TBD	TBD	-	FIG 2.	5
chromaticity	Blue x	Ta=25°C	TBD	TBD	TBD	-	CIE1931	5
	Blue y	1a-25 C	TBD	TBD	TBD	-		
	White x		TBD	TBD	TBD	-		
	White y		TBD	TBD	TBD	-		

#### Note1. Definition of contrast ratio

Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5,P6,P7,P8,P9)

Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5,P6,P7,P8,P9)

#### Note2.Definition of surface luminance

Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)

#### **Note3.Definiton of luminance uniformity**

The luminance uniformity in surface luminance ( $\delta$  WHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

Yu= Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5,P6,P7,P8,P9)

Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5,P6,P7,P8,P9)

#### Note4. Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time  $(T_{ON})$  is the time between photo detector output intensity changed from 90% to 10%. And fall time  $(T_{OFF})$  is the time between photo detector output intensity changed from 10% to 90%. For additional information see FIG1.

#### Note5. Definition of color chromaticity (CIE1931)

CIE (x, y) chromaticity ,The x,y value is determined by screen active area center position P5, For more information see FIG 2.

#### Note6. Definition of Viewing ange.

Viewing angle is the angle at which the contrast ratio is greater than 10. angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope or DMS series Instruments or compatible. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-7 or BM-5 photo detector or compatible.

Note: For TFT module, Gray scale reverse occurs in the direction of panel viewing angle.

### FIG.1. The definition of Response Time

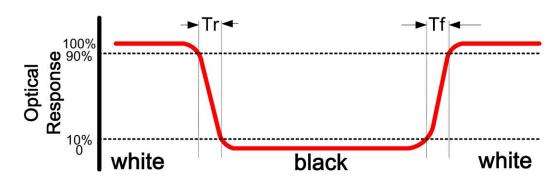


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

Size:S≤5"(see Figure a)

A:5 mm B:5 mm

H,V: Active Area

Light spot size  $\varnothing$ =5mm(BM-5), $\varnothing$ =7.7mm (BM-7)500mm distance or compatible distance from the LCD surface to detector lens.

test spot position: see Figure a.

measurement instrument : TOPCON's luminance meter

BM-5 or BM-7 or compatible (see Figure c)

Size:5" < S≤12.3"(see figure b)

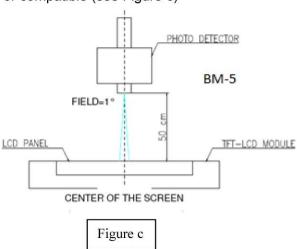
H,V: Active Area

Light spot size  $\varnothing$  = 5mm(BM-5),  $\varnothing$  = 7.7mm (BM-7)500mm distance or compatible distance from the LCD surface to detector lens

test spot position: see Figure b

measurement instrument : TOPCON's luminance meter

BM-5 or BM-7 or compatible (see Figure c)



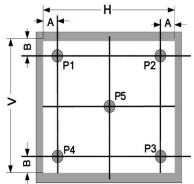


Figure a

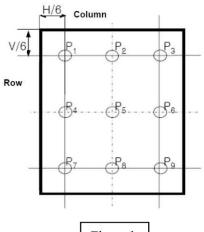
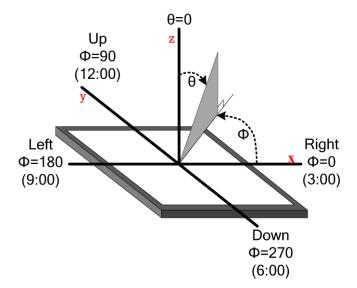


Figure b

# FIG.3. The definition of viewing angle



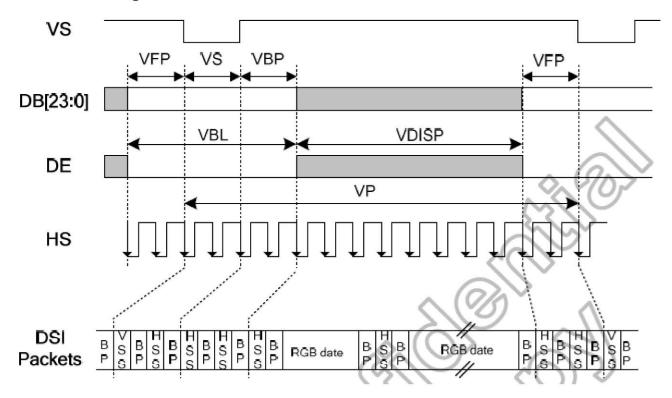
# 7. INTERFACE DESCRIPTION

# TFT Interface description

Interface No.	Name	I/O or connect to	Description
1-2	LEDA	Р	Power for LED backlight (Anode)
3	NC	-	Not connect
4-6	LEDK	Р	Power for LED backlight (Cathode)
7	NC	-	Not connect
8	TP-RST	I	TP-RST
9	TP-ATTN	I	TP-ATTN
10	TP-SDA	I/O	TP-SDA
11	TP-SCL	I	TP-SCL
12	GND	Р	Power Ground
13	TP-VDD 3.3V	Р	TP-VDD 3.3V
14	TP-VBUS 3V	Р	TP-VBUS 3V
15	GND	P	Power Ground
16	RESET	1	Global reset pin
17	LED PWM	0	Backlight on/off control pin
18	NC	-	Not connect
19	VPP	I	External high voltage pin used in OTP mode
20	NC	-	Not connect
21	GND	P	Power Ground
22-23	VDD3V3	Р	Power Voltage for digital circuit
24	GND	P	Power Ground
25	D0N	I/O	MIPI data pair0 negative signal
26	D0P	I/O	MIPI data pair0 Positive signal
27	GND	P	Power Ground
28	D1N	I/O	MIPI data pair1 negative signal
29	D1P	I/O	MIPI data pair1 Positive signal
30	GND	P	Power Ground
31	CLKN	I/O	MIPI CLK negative signal
32	CLKP	I/O	MIPI CLK Positive signal
33	GND	P	Power Ground
34	D2N	I/O	MIPI data pair2 negative signal
35	D2P	I/O	MIPI data pair2 Positive signal
36	GND	Р	Power Ground
37	D3N	I/O	MIPI data pair3 negative signal
38	D3P	I/O	MIPI data pair3 Positive signal
39	GND	Р	Power Ground

# 8. AC CHARACTERISTICS

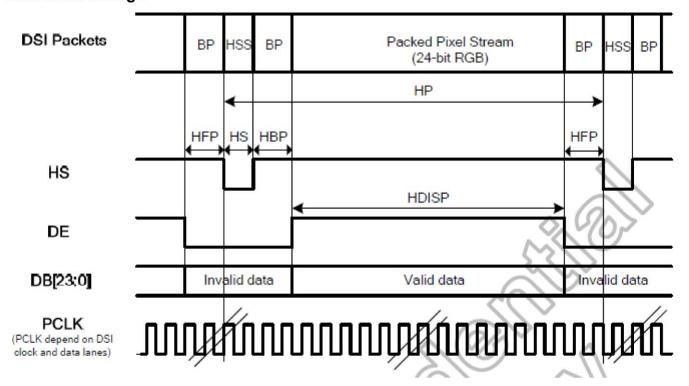
### 8.1 Vertical timings



Parameter	Symbol Condition —		Spec.			Unit
raiailletei			Min.	Typ.	Max.	Onit
Vertical cycle	VP	77 - 47	1286	-	-1	Line
Vertical low pulse width	VS	· (a)	2	-	Note(1)	Line
Vertical front porch	VFP		2	-	-	Line
Vertical back porch	VBP	7/10	2	-	Note(1)	Line
Vertical data start point	V -	VS+VBP	4	-	Note(1)	Line
Vertical blanking period	VBL	VS+VBP+VFP	6	-	-	Line
Vertical active area	-6	VDISP	=	1280	-	Line
Vertical Refresh rate	VRR	)) -	=	60	-	Hz

Note: (1) The VS and VBP pulse width are related to GSP and GCK timing. The GSP and GCK must be set at corresponding position for LCD normal display.

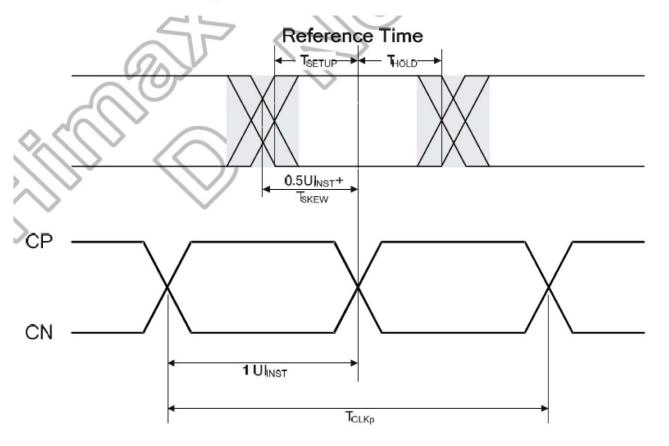
### **Horizontal timings**



Parameter	Symbol	ymbol Condition		Spec.	Unit	
i didilictei	Symbol	Condition	Min.	Тур.	Max.	Ollit
HS low pulse width	HS	(C) - ((	8	-	-	pclk(note1)
Horizontal back porch	HBP		30	-	-	pclk(note1)
Horizontal front porch	HFP	~	10	_	-	pclk(note1)
Horizontal data start point	- ( )	HS+HBP	38	-	-	pclk(note1)
Horizontal blanking period	HBLK	HS+HBP+HFP	48	-	-	pclk(note1)
Horizontal active area	HDISP	((0)	-	800	-	DCK

Note: (1) Depend on 550Mbps\*4-Lane@60Hz

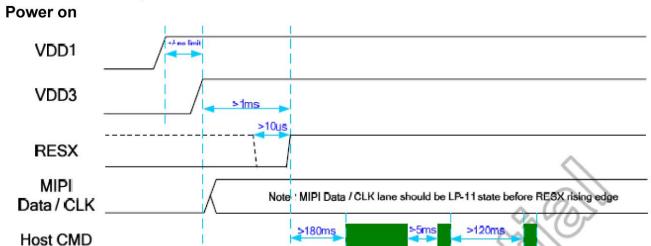
# 8.2 Data to clock timing definitions



### 9. POWER SEQUENCE

Video Packet

Backlight



Note: (1) For External Power Mode not need VDD3

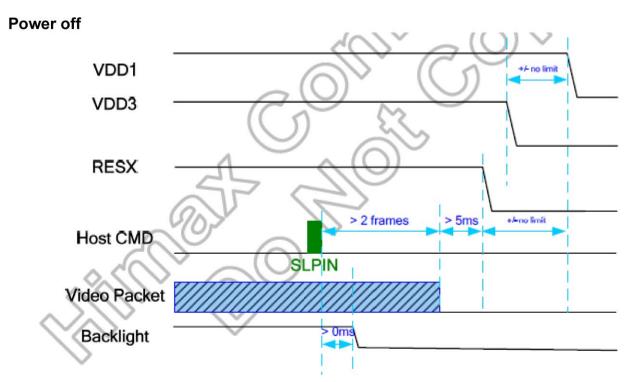
Figure 5.20: VDD1/VDD3 input power on sequence

SLPOUT

Initial

DISPON

10ms



Note: (1) For External Power Mode not need VDD3

Figure 5.21: VDD1/VDD3 input power off sequence

### 10. RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	80±2°C/240 hours	
2	Low Temperature Storage	-30±2°C/240 hours	Inspection after
3	High Temperature Operating	70±2°C/120 hours	2~4hours storage at room temperature, the
4	Low Temperature Operating	-20±2°C/120 hours	sample shall be free
5	Temperature Cycle	-20±2°C~25~70±2°C×10cycles (30min.) (5min.) (30min.)	from defects: 1.Current changing value before test and
6	Damp Proof Test	50°C×90% RH/120 hours	after test is 50% larger;
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz  Amplitude: 1.5mm, X, Y, Z direction for total 3hours (Packing condition)	2. function defect:Non-display ,abn ormal-display,missing lines,Short lines , ITO
8	Dropping test	Drop to the ground from 1m height, one time, every side of carton. (Packing condition)	corrosion; 3.visual defect:Air bubble in the LCD,Seal
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time	leak,Glass crack.

#### Remark:

- 1. The test samples should be applied to only one test item.
- 2.Sample size for each test item is 3~5pcs.
- 3.For Damp Proof Test, Pure water(Resistance >  $10M\Omega$ ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

#### 11. INSPECTION CRITERION

#### 11.1 description

This specification is made to be used as the standard acceptance/rejection criteria for TFT LCM Product.

#### 1.Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1 :1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65 Minor defect: AQL 1.5

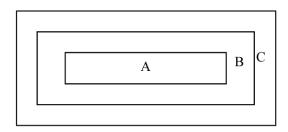
#### 2. Inspection condition

• Viewing distance for cosmetic inspection is about 30±5cm with bare eyes, and under an environment 600~1000lux for visual inspection and 0~200lux for function test., all directions for inspecting the sample should be within 45°against perpendicular line. (Normal temperature 18~28°C and normal humidity 60±15%RH).

#### Driving voltage

The Vop value from which the most optical contrast can be obtained near the specified Vop in the specification (Within  $\pm 0.5$ V of the typical value at 25°C.).

#### 3. Definition of inspection zone in LCD



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

#### 11.2 Inspection criterion

#### 11.2.1 function defect

Items to be inspected	Inspection criterion	Classification of defects
All functional defects	No display     Display abnormally	
	3) Missing vertical, horizontal segment	
	<ul> <li>4) Short circuit</li> <li>5) Back-light no lighting, flickering and abnormal lighting.</li> <li>6) obvious striation</li> <li>7) Current beyond specification value</li> </ul>	MA
Missing	Missing component	
Outline dimension	Overall outline dimension exceed the drawing is not allowed.	

# 11.2.2 LCD pixel defect ( bad dot) (defect type:MI)

Checking item	Judgment criterion			
Item\LCD size	S ≤5.0 Inch	5.0 < S≤7.0 Inch	7 < S≤12.3 Inch	
Color bad dot-bright dot(R、G、B)	1	2	3	
two adjacent bright point	0	1	2	
three or more adjacent point	0	0	0	
total points for bad dot-bright dot	1	2	5	
Bad dot-dark dot	2	4	5	
two adjacent dark point	1	2	3	
three or more adjacent point	0	1	1	
total points for bad dot -dark dot	3	6	7	
patch bright dot		Invisible with ND5%	it is OK.	

11.2.3 Do	11.2.3 Dot and line defect (defect type:MI )					
Checking	Judgment criterion					
item	Diameter(mm	)\LCD Size	S ≤5.0 Inch	5 < S≤7 Inch	7 < S≤12.3 Inch	Figure
	D≤	≦0.1	allowed	allowed	allowed	
	0.1 < D≤0.2		4	allowed	allowed	
Dot defect	0.2 < D≤0.3		0	5	6	1 to
Dot delect	0.3 < D≤0.5		0	0	0	4 a
	D>	> 0.5	0	0	0	D=(a+b)/2
	the distance between the two defect dot:DS≥5mm					
	Length(mm) Width(mm)			Judgment criterion		
line defect	disregard	W≤0.05	allowed	allowed	allowed	<del>\</del> L →
	L≤5	0.05 < W≤0.1	4	5	7	\$ w
	L>5	W > 0.1	0	0	0	
Concave	LCD Size(mm)		Judgment criterion			
point and	D≤0.3		allowed	allowed	allowed	
air bubble	0.3 < D≤1.0		3	4	5	Ţ b
for	1.0 < D≤1.5		1	2	3	a 1
polarizer	D>	<b>-</b> 1.5	0	0	0	D=(a+b)/2
	Length(mm) Width(mm)			Judgment cri	terion	
	disregard	W≤0.05	allowed	allowed	allowed	
	1 < L≤5	0.05 < W≤0.2	3	4	5	L L
Fold mark、 linear scar	L>5	W > 0.2	0	0	0	<b>↓</b> •
	for polarizer Notes: 1. If the fold mark and linear scar for polarizer is visible with operating condition, the definition is judged with line judge; 2. If the fold mark and linear scar for polarizer is visible non-operating condition, the defect is judged with the above judgment standard.				zer is visible with	

# 11.2.4 Corner and others crack for LCD (defect type:MI)

Checking item	Judgment criterion	Figure
electric conduction crack	X≤3.0mm,Y≤1/4w,Z≤t,N≤2	TI III III
corner crack	X≤3.0mm,Y≤3.0mm , Z≤t,N≤3  Corner crack extended to ITO PIN,none allowed	
surface crack	X≤1.5mm,Y≤1.0mm, Z≤t, N≤4	Z T T T

# 11.2.5 Module cosmetic criterion (defect type:MI)

Item	Judgment criterion		
Difference in Spec.	None allowed		
Pattern peeling	No substrate pattern peeling and floating		
Soldering defects	No soldering missing No soldering bridge No cold soldering		
	Notes:detail judgment referring to IPC-A-610 grade II		
Resist flaw on Printed Circuit Boards	visible copper foil ( 0.5mm or more) on substrate pattern, none allowed		
Accretion of metallic Foreign matter	No accretion of metallic foreign matters (Not exceed 0.2mm)		
Stain	No stain to spoil cosmetic badly		
Plate discoloring	No plate fading, rusting and discoloring		
Newton ring	Referring to limited sample		
Mura	Invisible with 5%ND,allowed		
Light leaks	Referring to limited sample		

#### 12. HANDLING PRECAUTIONS

#### 12.1 Mounting method

The LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[recommended below] and wipe lightly

- .lsopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- •.Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground—your body, work/assembly areas, assembly—equipment to protect against static electricity.

#### 12.4 Packing

Module employ LCD elements and must be treated as such.

- Avoid intense shock and falls from a height.
- •.To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- •.It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- •.An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- •.Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- •.If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- •.A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

#### 12.6 Storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storing in an ambient temperature 10°C to 30°C, and in a relative humidity of 45% to 75%. Don't expose to sunlight or fluorescent light.
- Storing in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

#### 13. PRECAUTION FOR USE

- **13.1** A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- **13.2** On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.
  - When a question is arisen in this specification
  - When a new problem is arisen which is not specified in this specifications
  - When an inspection specifications change or operating condition change in customer is reported to AVD, and some problem is arisen in this specification due to the change
  - When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

#### 14. PACKING SPECIFICATION

Please consult our technical department for detail information.