

Applications

- Defense & Aerospace
- Broadband Wireless

Product Features

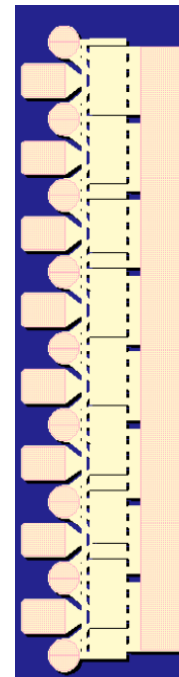
- Frequency Range: DC - 18 GHz
- 47.3 dBm Nominal P_{SAT} at 3 GHz
- 69.5% Maximum PAE
- 19.8 dB Nominal Power Gain at 3 GHz
- Bias: $V_D = 12 - 32$ V, $I_{DQ} = 200 - 1000$ mA
- Technology: TQGaN25 on SiC
- Chip Dimensions: 0.82 x 2.48 x 0.10 mm

General Description

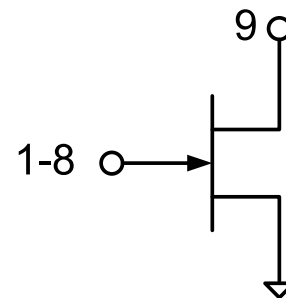
The TriQuint TGF2023-2-10 is a discrete 10 mm GaN on SiC HEMT which operates from DC-18 GHz. The TGF2023-2-10 is designed using TriQuint's proven TQGaN25 production process. This process features advanced field plate techniques to optimize microwave power and efficiency at high drain bias operating conditions.

The TGF2023-2-10 typically provides 47.4 dBm of saturated output power with power gain of 19.8 dB at 3 GHz. The maximum power added efficiency is 69.5 % which makes the TGF2023-2-10 appropriate for high efficiency applications.

Lead-free and RoHS compliant



Functional Block Diagram



Pad Configuration

| Pad No. | Symbol |
|----------|-----------------|
| 1-8 | V_G / RF IN |
| 9 | V_D / RF OUT |
| Backside | Source / Ground |

Ordering Information

| Part | ECCN | Description |
|--------------|------------|------------------|
| TGF2023-2-10 | 3A001b.3.b | 50 Watt GaN HEMT |

Absolute Maximum Ratings

| Parameter | Value |
|------------------------------------|--------------------|
| Drain to Gate Voltage (V_{DG}) | 100 V |
| Drain Voltage (V_D) | 40 V |
| Gate Voltage Range (V_G) | -50 to 0 V |
| Drain Current (I_D) | 10 A |
| Gate Current (I_G) | -10 to 28 mA |
| Power Dissipation (P_D) | See graph on pg.3. |
| CW Input Power (P_{IN}) | +40 dBm |
| Channel Temperature (T_{CH}) | 275°C |
| Mounting Temperature | 320°C |
| Storage Temperature | -65 to 150°C |

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

Recommended Operating Conditions

| Parameter | Value |
|--|---------------|
| Drain Voltage Range (V_D) | 12 – 32 V |
| Drain Quiescent Current (I_{DQ}) | 0.5 A |
| Drain Current Under RF Drive (I_D) | 3 A (Typ.) |
| Gate Voltage (V_G) | -3.0 V (Typ.) |
| Channel Temperature (T_{CH}) | 225°C (Max.) |

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

RF Characterization – Model Optimum Power Tune

Test conditions unless otherwise noted: T = 25°C, Bond wires not included.

| Parameter | Typical Value | | | | | | | | Units |
|---|--------------------|--------------------|-------------------|-------------------|--------------------|--------------------|-------------------|-------------------|--------------|
| Frequency (F) | 3 | | | | 6 | | | | GHz |
| Drain Voltage (V_D) | 12 | 12 | 28 | 28 | 12 | 12 | 28 | 28 | V |
| Bias Current (I_{DQ}) | 200 | 500 | 200 | 500 | 200 | 500 | 200 | 500 | mA |
| Output P3dB (P_{3dB}) | 43.3 | 42.9 | 47.4 | 47.3 | 43.8 | 43.6 | 47.5 | 47.3 | dBm |
| PAE @ P_{3dB} (PAE_{3dB}) | 53.3 | 52 | 61.4 | 61.8 | 48.9 | 53.0 | 57.5 | 59.9 | % |
| Gain @ P_{3dB} (G_{3dB}) | 13.3 | 15.8 | 16.6 | 19.8 | 7.4 | 10.4 | 11.3 | 14.1 | dB |
| Parallel Resistance ⁽¹⁾ (R_p) | 22.0 | 21.7 | 64.5 | 65.3 | 22.4 | 21.4 | 62.8 | 62.7 | Ω -mm |
| Parallel Capacitance ⁽¹⁾ (C_p) | 0.36 | 0.42 | 0.24 | 0.26 | 0.15 | 0.24 | 0.28 | 0.30 | pF/mm |
| Load Reflection Coefficient ⁽²⁾ (Γ_L) | 0.40 \angle 171° | 0.41 \angle 170° | 0.18 \angle 53° | 0.19 \angle 54° | 0.39 \angle 172° | 0.42 \angle 169° | 0.30 \angle 85° | 0.32 \angle 88° | -- |

Notes:

1. Large signal equivalent output network (normalized).
2. Characteristic Impedance (Z_0) = 5 Ω .

RF Characterization – Model Optimum Efficiency Tune

Test conditions unless otherwise noted: T = 25°C, Bond wires not included.

| Parameter | Typical Value | | | | | | | | Units |
|---|-------------------|-------------------|-------------------|-------------------|--------------------|--------------------|-------------------|-------------------|--------------|
| Frequency (F) | 3 | | | | 6 | | | | GHz |
| Drain Voltage (V_D) | 12 | 12 | 28 | 28 | 12 | 12 | 28 | 28 | V |
| Bias Current (I_{DQ}) | 200 | 500 | 200 | 500 | 200 | 500 | 200 | 500 | mA |
| Output P3dB (P_{3dB}) | 40.8 | 40.2 | 46.1 | 45.9 | 41.1 | 40.9 | 46.3 | 46.1 | dBm |
| PAE @ P_{3dB} (PAE_{3dB}) | 69.5 | 67.2 | 69.0 | 68.1 | 66.2 | 66.3 | 64.1 | 65.5 | % |
| Gain @ P_{3dB} (G_{3dB}) | 15.3 | 17.5 | 18.5 | 21.0 | 10.3 | 12.9 | 12.4 | 15.0 | dB |
| Parallel Resistance ⁽¹⁾ (R_p) | 77.3 | 78.1 | 118.1 | 120.3 | 67.9 | 62.7 | 107.0 | 105.7 | Ω -mm |
| Parallel Capacitance ⁽¹⁾ (C_p) | 0.46 | 0.43 | 0.37 | 0.38 | 0.46 | 0.45 | 0.37 | 0.38 | pF/mm |
| Load Reflection Coefficient ⁽²⁾ (Γ_L) | 0.33 \angle 66° | 0.32 \angle 62° | 0.46 \angle 45° | 0.47 \angle 46° | 0.47 \angle 100° | 0.44 \angle 102° | 0.54 \angle 78° | 0.54 \angle 79° | -- |

Notes:

1. Large signal equivalent output network (normalized).
2. Characteristic Impedance (Z_0) = 5 Ω .

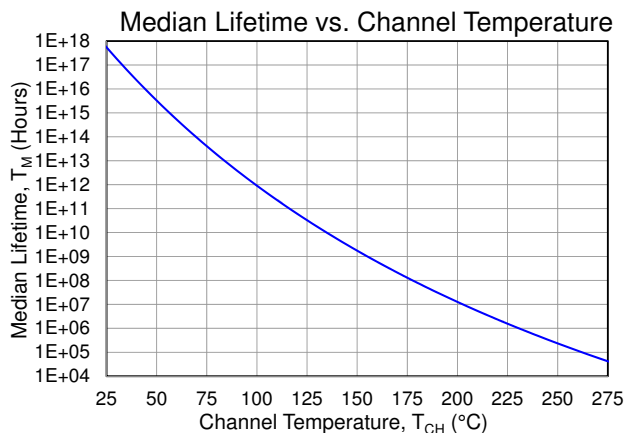
Thermal and Reliability Information ⁽¹⁾

| Parameter | Test Conditions | Value | Units |
|--|--|--------------------|--------------------|
| Thermal Resistance, θ_{JC} (No RF Drive) | $V_D = 28\text{ V}$, $I_D = 1\text{ A}$, $P_D = 28\text{ W}$, $T_{\text{baseplate}} = 70^\circ\text{C}$ | 3.03 | $^\circ\text{C/W}$ |
| Channel Temperature, T_{CH} (No RF Drive) | | 155 | $^\circ\text{C}$ |
| Median Lifetime, T_M (No RF Drive) | | 1.2×10^9 | Hrs |
| Thermal Resistance, θ_{JC} (Under RF Drive) | $V_D = 28\text{ V}$, $I_D = 3\text{ A}$, $P_{OUT} = 46.7\text{ dBm}$, $P_D = 37.6\text{ W}$, $T_{\text{baseplate}} = 70^\circ\text{C}$ | 3.24 | $^\circ\text{C/W}$ |
| Channel Temperature, T_{CH} (Under RF Drive) | | 192 | $^\circ\text{C}$ |
| Median Lifetime, T_M (Under RF Drive) | | 2.60×10^7 | Hrs |

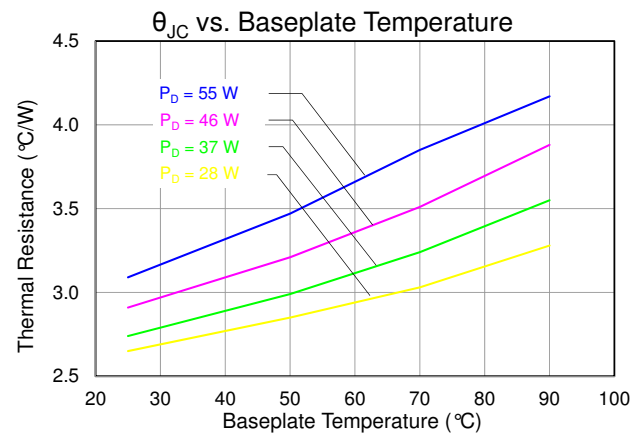
Notes:

- Assumes eutectic attach using 1mil thick 80/20 AuSn mounted to a 10 mil CuMo Carrier Plate.

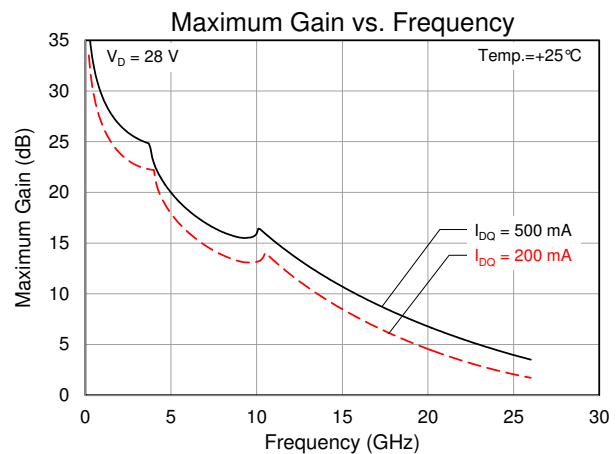
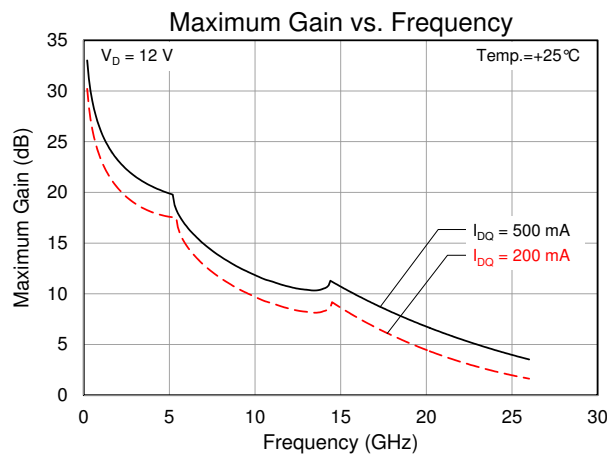
Median Lifetime



Thermal Resistance

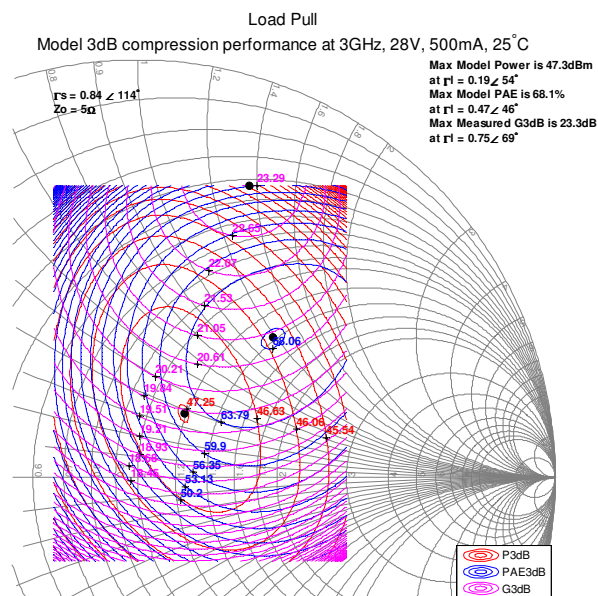
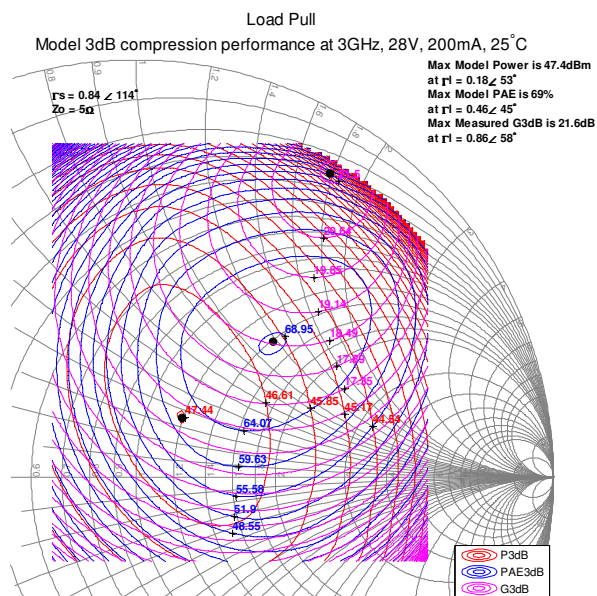
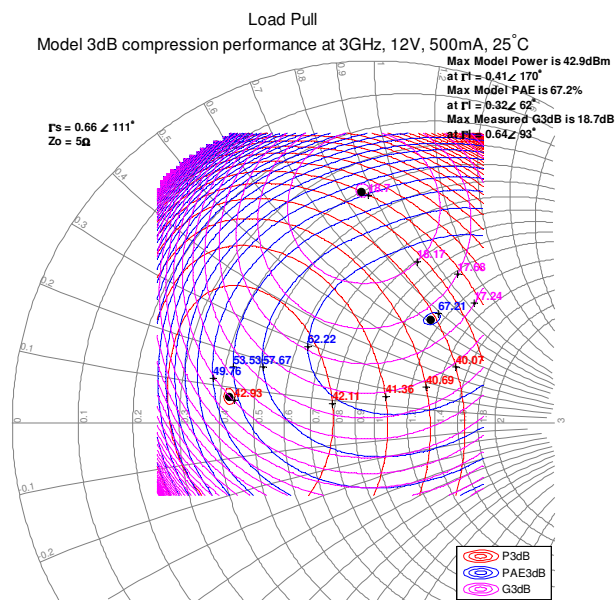
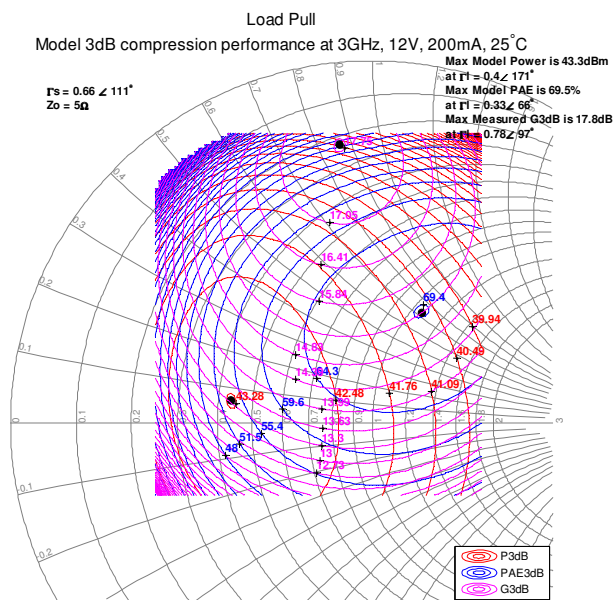


Model Maximum Gain



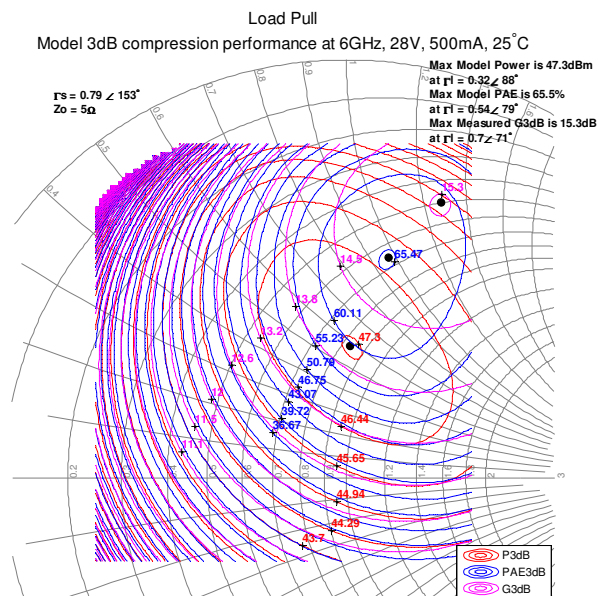
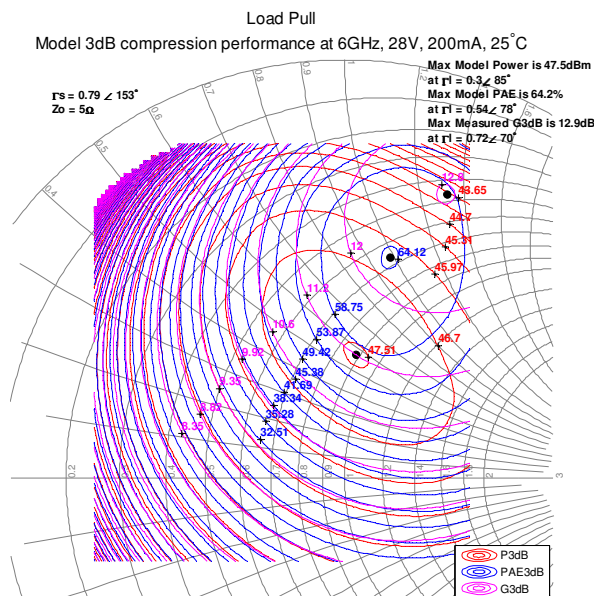
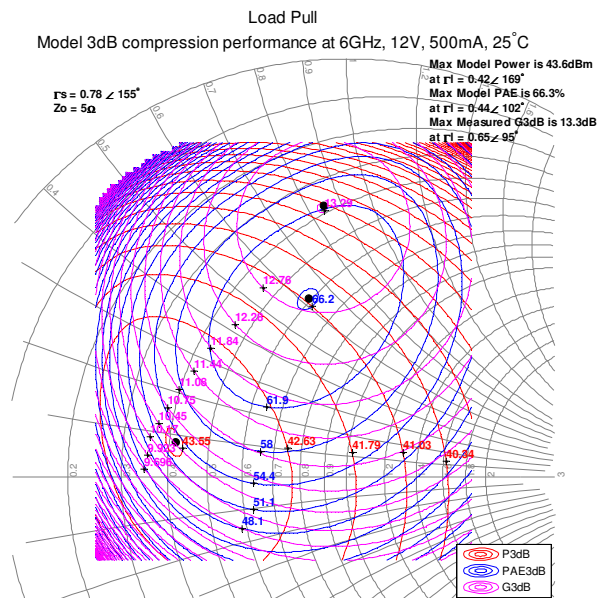
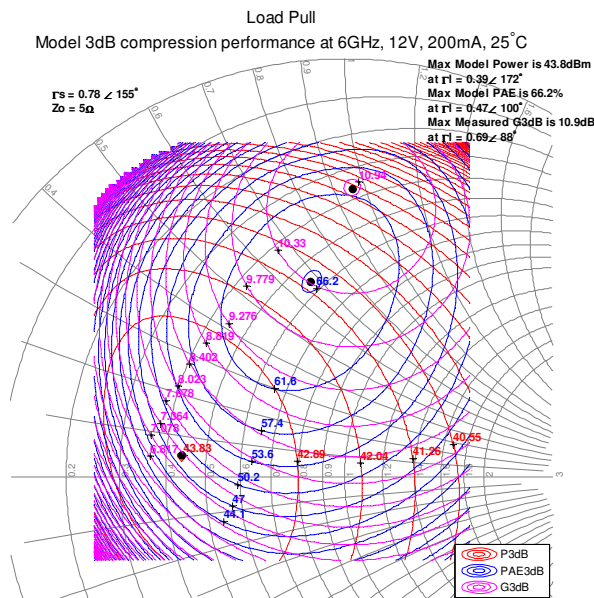
Model Load Pull Contours

Simulated signal: 10% pulses. Bond wires not included.



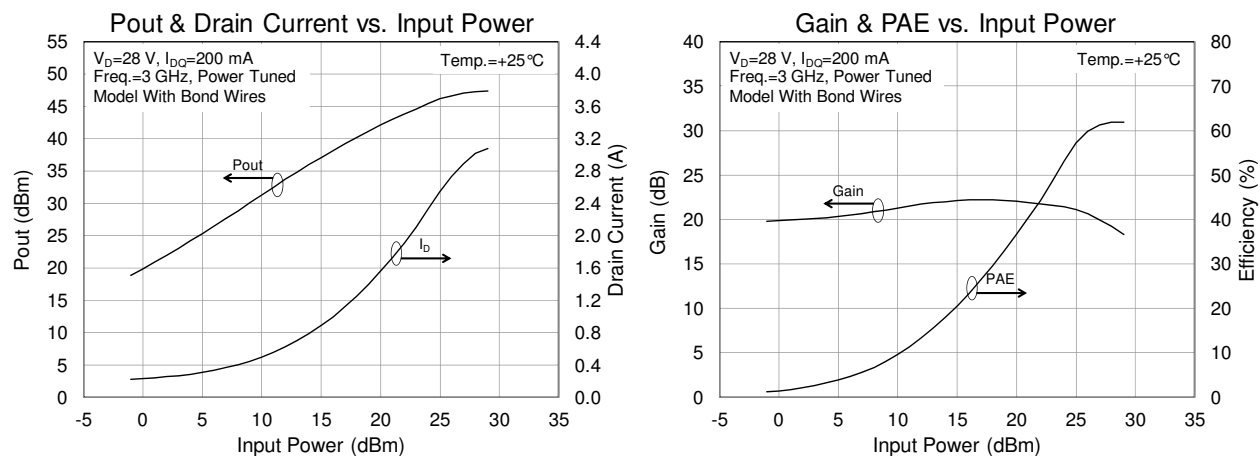
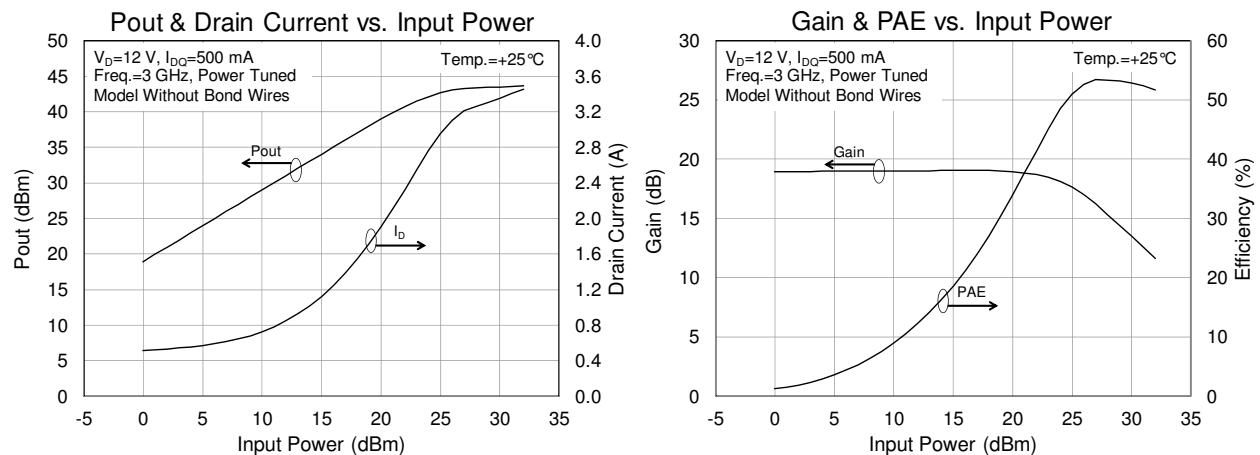
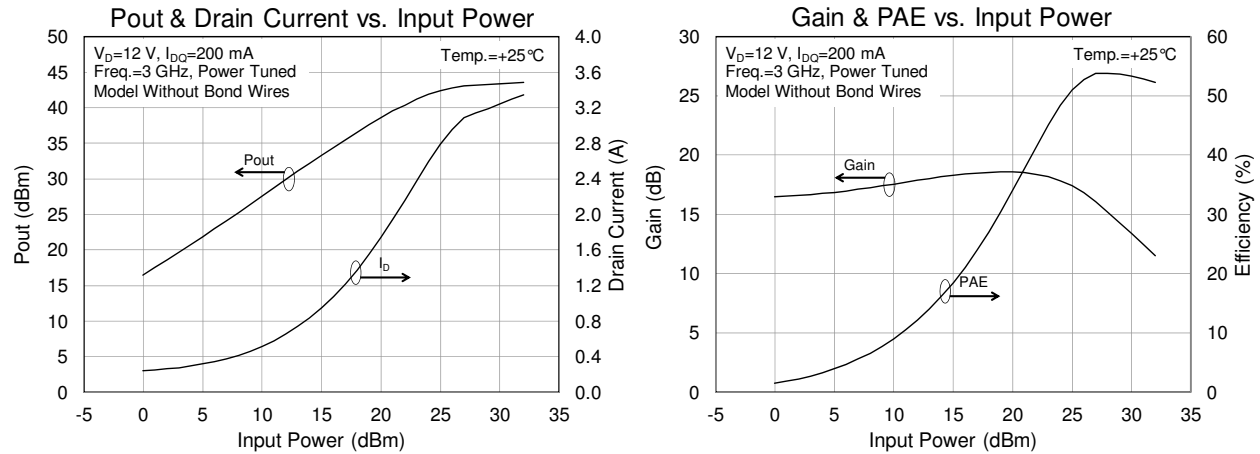
Model Load Pull Contours

Simulated signal: 10% pulses. Bond wires not included.



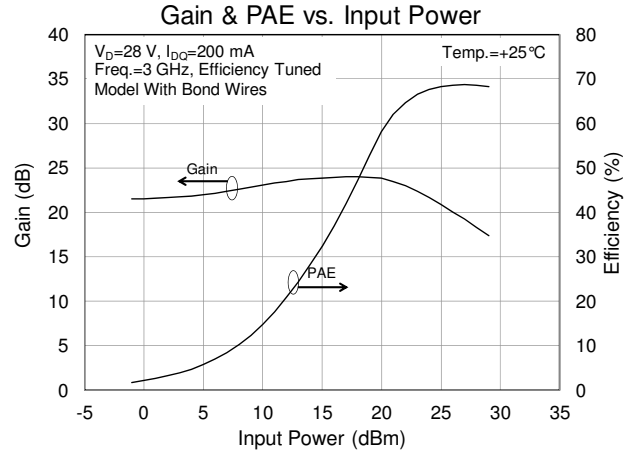
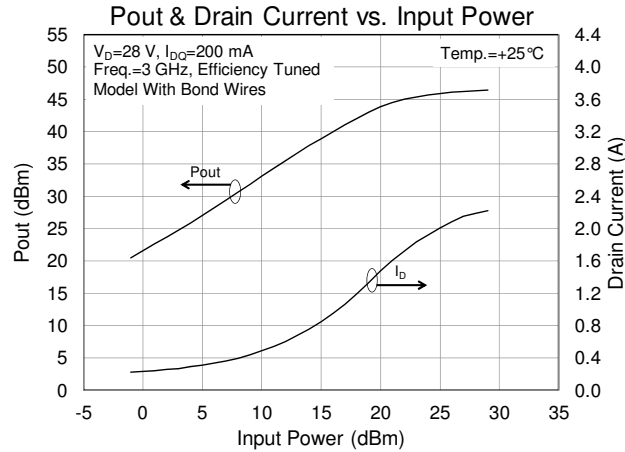
Model Power Tuned Data

Simulated signal: 10% pulses.

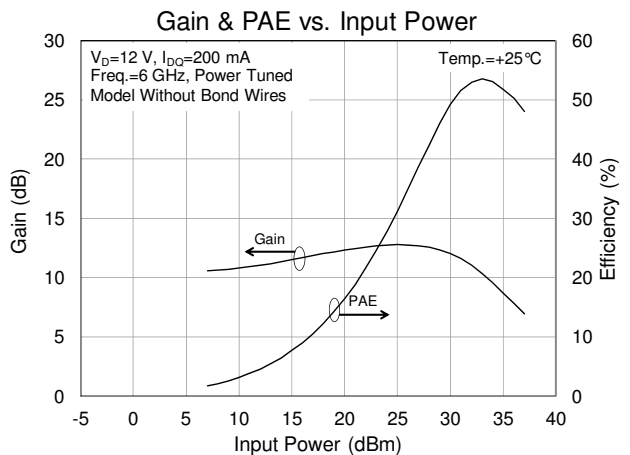
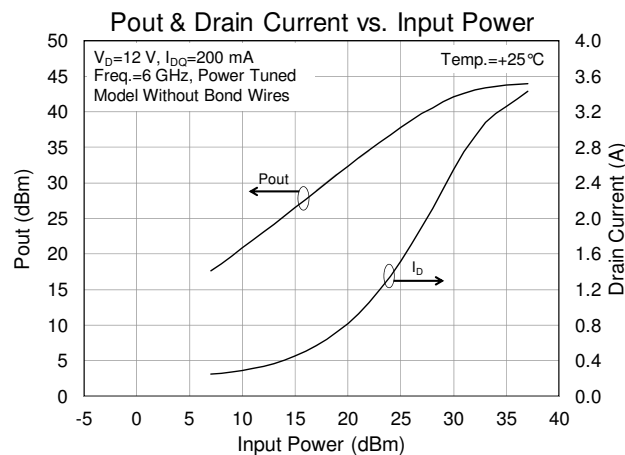


Model Power Tuned Data

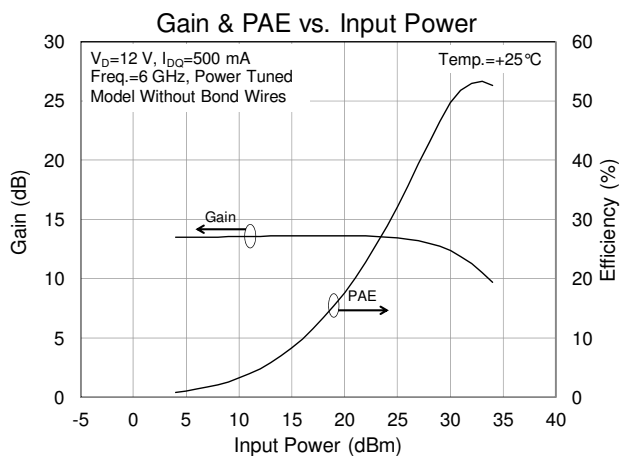
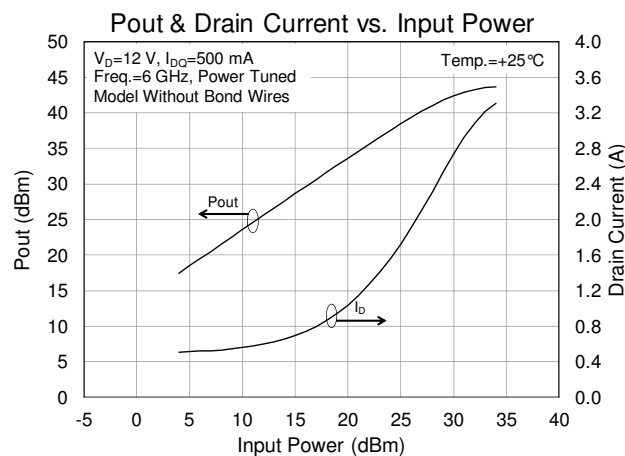
Simulated signal: 10% pulses.



Source Γ : fo: $0.84\angle 114^\circ$, 2fo: 0, 3fo: 0
Load Γ : fo: $0.19\angle 54^\circ$, 2fo: 0, 3fo: 0



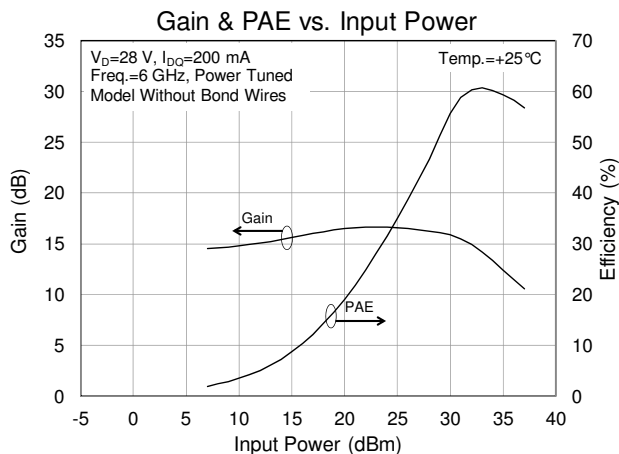
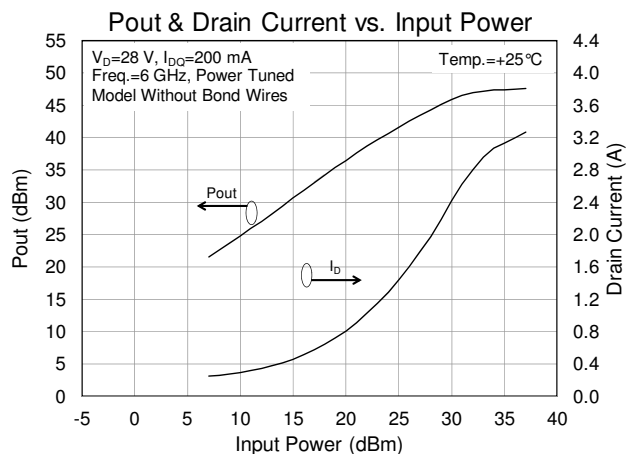
Source Γ : fo: $0.78\angle 155^\circ$, 2fo: 0, 3fo: 0
Load Γ : fo: $0.39\angle 172^\circ$, 2fo: 0, 3fo: 0



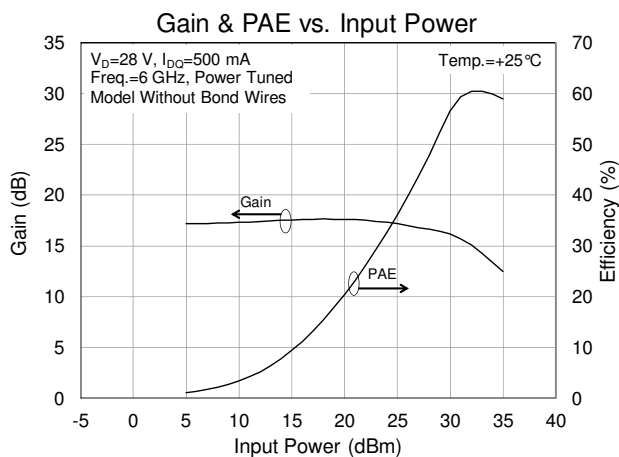
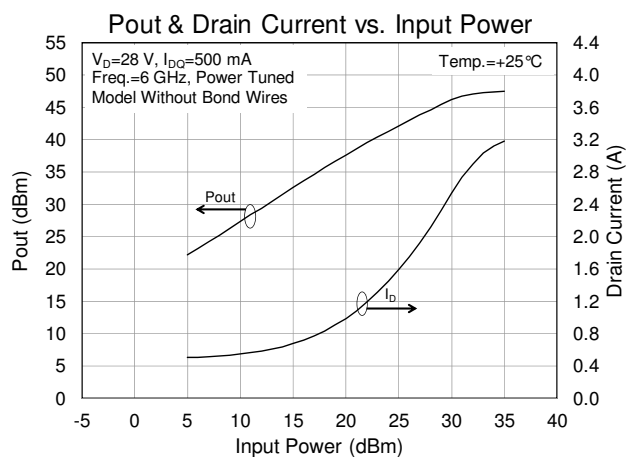
Source Γ : fo: $0.78\angle 155^\circ$, 2fo: 0, 3fo: 0
Load Γ : fo: $0.42\angle 169^\circ$, 2fo: 0, 3fo: 0

Model Power Tuned Data

Simulated signal: 10% pulses.



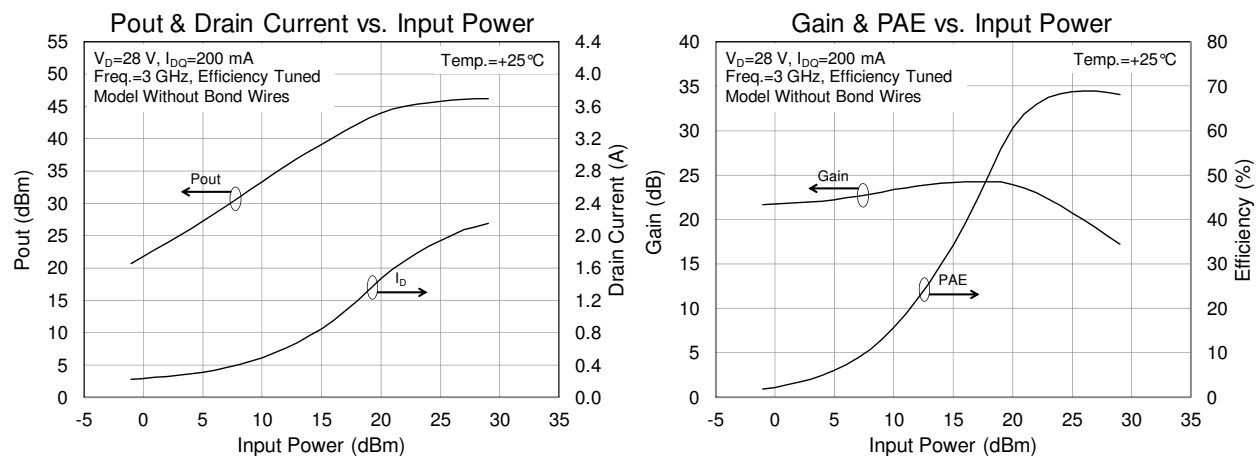
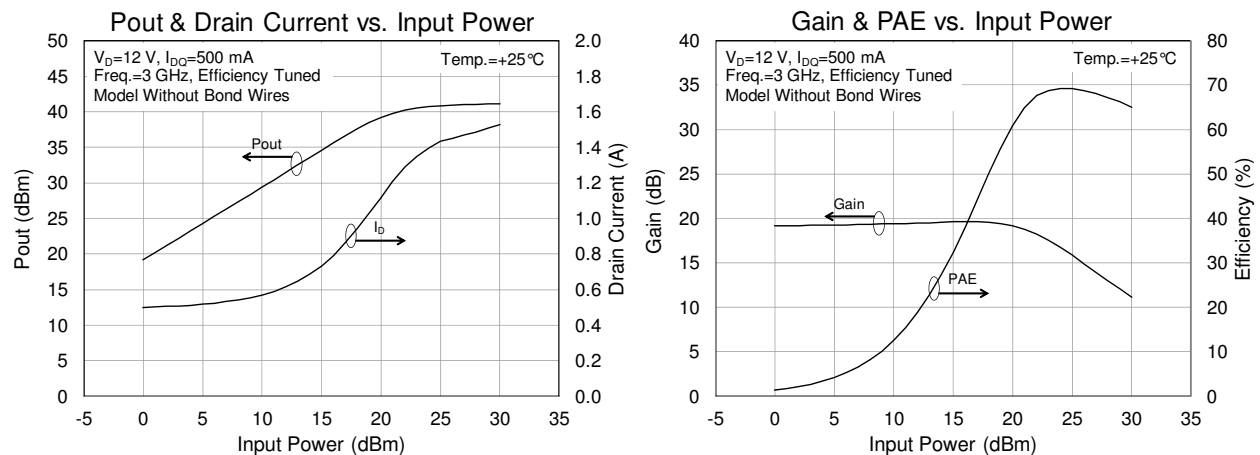
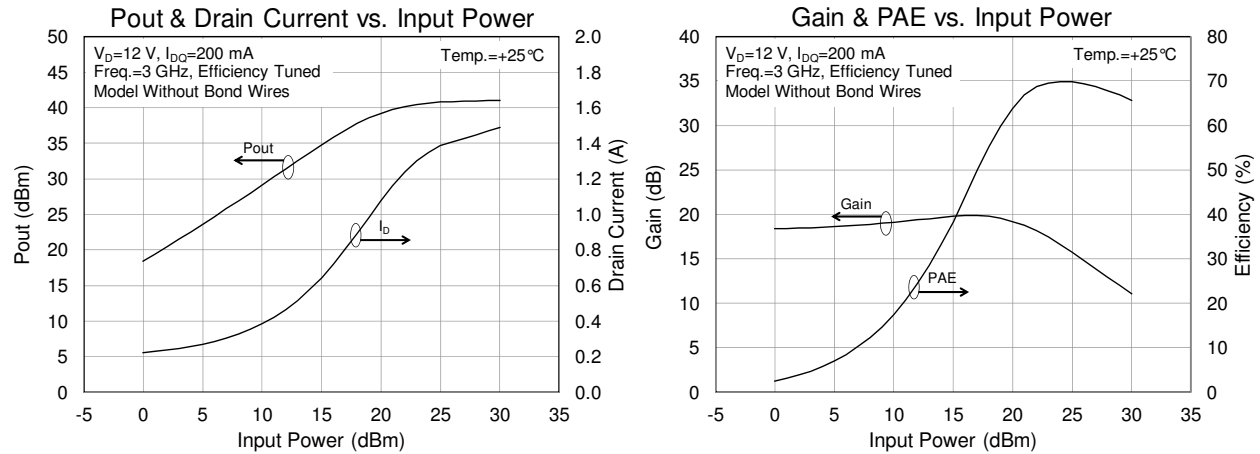
Source Γ : fo: 0.79 \angle 153°, 2fo: 0, 3fo: 0
Load Γ : fo: 0.30 \angle 85°, 2fo: 0, 3fo: 0



Source Γ : fo: 0.79 \angle 153°, 2fo: 0, 3fo: 0
Load Γ : fo: 0.32 \angle 88°, 2fo: 0, 3fo: 0

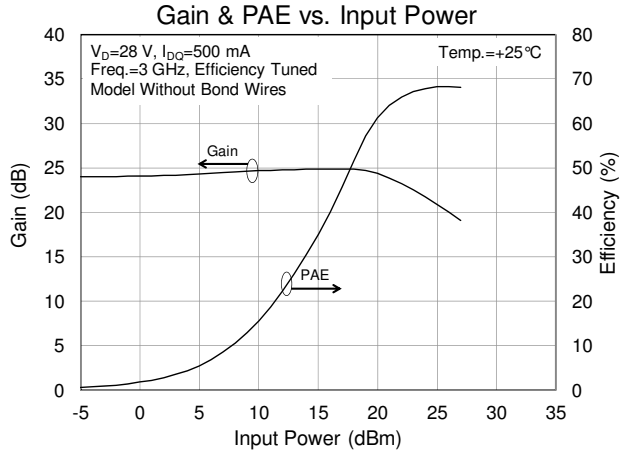
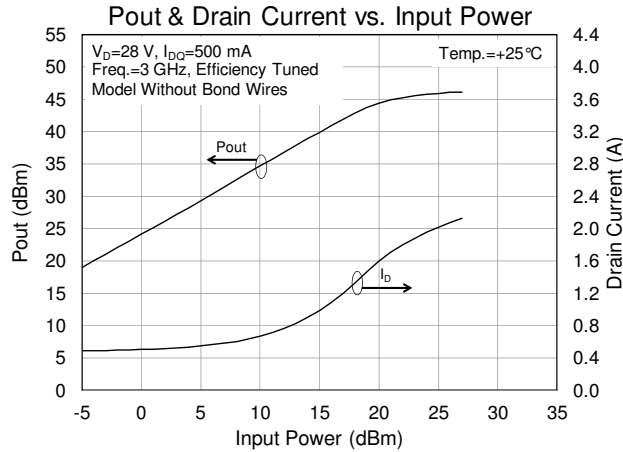
Model Efficiency Tuned Data

Simulated signal: 10% pulses.

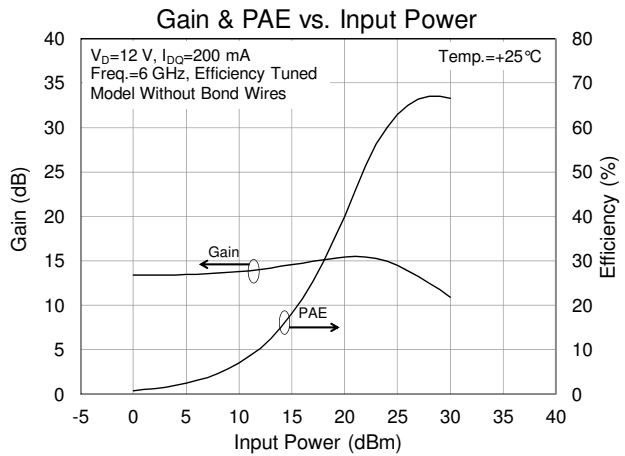
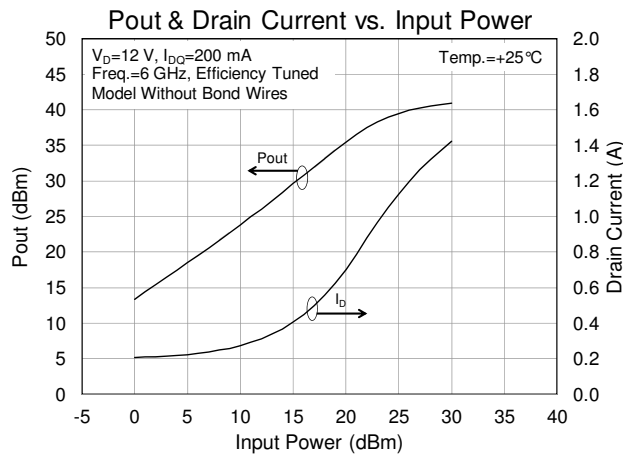


Model Efficiency Tuned Data

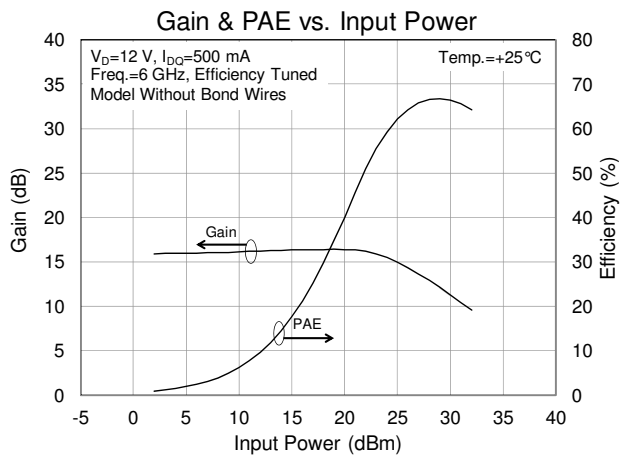
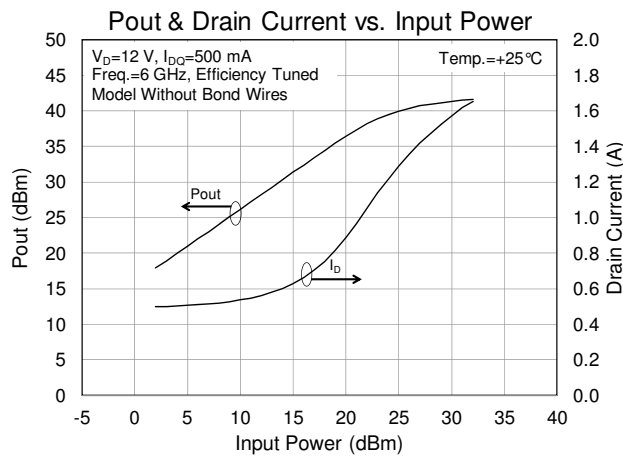
Simulated signal: 10% pulses.



Source Γ : fo: 0.84 \angle 114°, 2fo: 0, 3fo: 0
Load Γ : fo: 0.47 \angle 46°, 2fo: 0, 3fo: 0



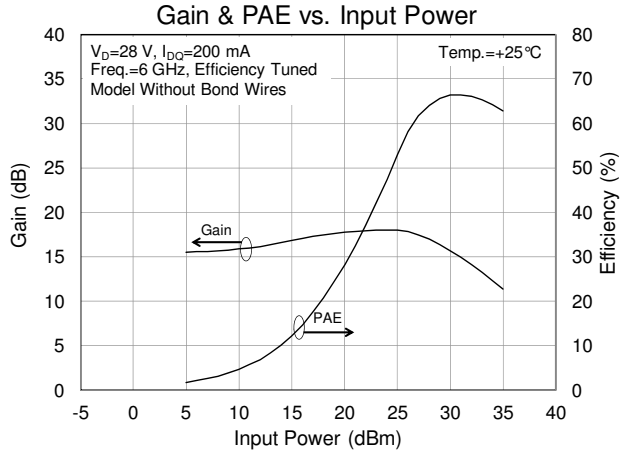
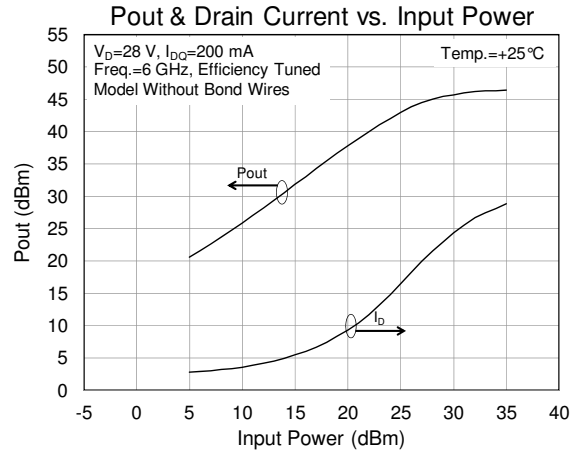
Source Γ : fo: 0.78 \angle 155°, 2fo: 0, 3fo: 0
Load Γ : fo: 0.47 \angle 100°, 2fo: 0, 3fo: 0



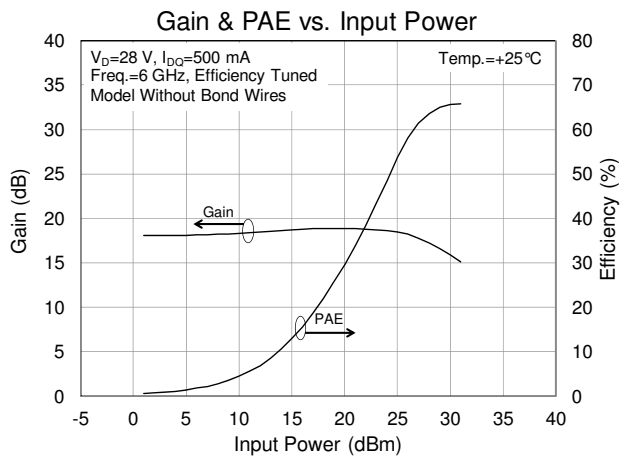
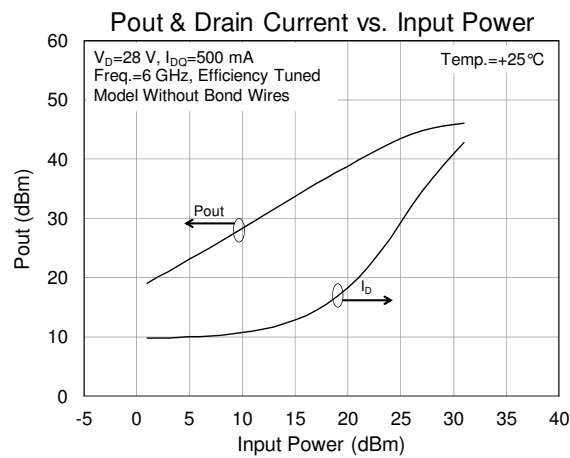
Source Γ : fo: 0.78 \angle 155°, 2fo: 0, 3fo: 0
Load Γ : fo: 0.44 \angle 102°, 2fo: 0, 3fo: 0

Model Efficiency Tuned Data

Simulated signal: 10% pulses.



Source Γ : fo: 0.79 \angle 153°, 2fo: 0, 3fo: 0
 Load Γ : fo: 0.54 \angle 78°, 2fo: 0, 3fo: 0

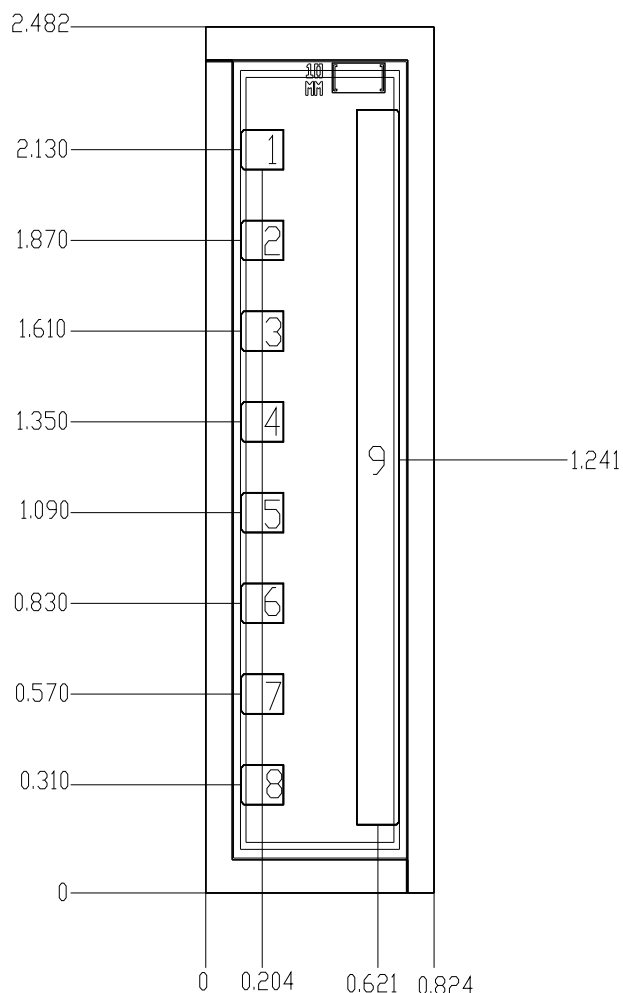


Source Γ : fo: 0.79 \angle 153°, 2fo: 0, 3fo: 0
 Load Γ : fo: 0.54 \angle 79°, 2fo: 0, 3fo: 0

Model

A model is available for download from Modelithics (at <http://www.modelithics.com/mvp/Triqunt&tab=3>) by approved TriQuint customers. The model is compatible with the industry's most popular design software including Agilent ADS and National Instruments/AWR applications. Once on the Modelithics web page, the user will need to register for a free license before being granted the download.

Mechanical Drawing



Bond Pads

| Pad No. | Description | Dimensions |
|--------------|-----------------|---------------|
| 1-8 | Gate | 0.154 x 0.115 |
| 9 | Drain | 0.154 x 2.05 |
| Die Backside | Source / Ground | 0.824 x 2.482 |

Notes:

1. Units: millimeters
2. Thickness: 0.100 mm
3. Die x,y size tolerance: ± 0.050 mm

Assembly Notes

Component placement and adhesive attachment assembly notes:

- Vacuum pencils and/or vacuum collets are the preferred method of pick up.
- Air bridges must be avoided during placement.
- The force impact is critical during auto placement.
- Organic attachment (i.e. epoxy) not recommended.

Reflow process assembly notes:

- Use AuSn (80/20) solder and limit exposure to temperatures above 300°C to 3-4 minutes, maximum.
- An alloy station or conveyor furnace with reducing atmosphere should be used.
- Do not use any kind of flux.
- Coefficient of thermal expansion matching is critical for long-term reliability.
- Devices must be stored in a dry nitrogen atmosphere.

Interconnect process assembly notes:

- Ball bonding is the preferred interconnect technique, except where noted on the assembly diagram.
- Force, time, and ultrasonics are critical bonding parameters.
- Aluminum wire should not be used.
- Devices with small pad sizes should be bonded with 0.0007-inch wire.

Disclaimer

GaN/SiC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

Bias-up Procedure

1. V_G set to -5 V.
2. V_D set to 28 V.
3. Adjust V_G more positive until quiescent I_D is 1 A.
4. Apply RF signal.

Bias-down Procedure

1. Turn off RF signal.
2. Turn off V_D and wait 1 second to allow drain capacitor dissipation.
3. Turn off V_G .

Product Compliance Information

ESD Sensitivity Ratings



Caution! ESD-Sensitive Device

ESD Rating: TBD
Value: TBD
Test: TBD
Standard: TBD

Solderability

Compatible with gold/tin (320°C maximum reflow temperature) soldering processes.

RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free

Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations, and information about TriQuint:

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Tel: +1.972.994.8465
Fax: +1.972.994.8504

For technical questions and application information: Email: info-products@triquint.com

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