

INCHANGE SEMICONDUCTOR

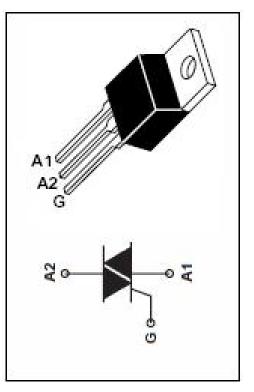
TIC225series

DESCRIPTION

- Sensitive Gate Triacs
- 8A RMS ,70A Peak
- Glass passivated Wafer
- 400V to 800V off-state Voltage
- Max I_{GT} of 5mA(Quadrants 1)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

| SYMBOL | PARAMETER | VALUE | UNIT | | |
|---------------------|--|-----------|------|-----|--|
| V _{DRM} | Repetitive peakoff-state voltage | TIC225D | 400 | | |
| | | TIC225M | 600 | | |
| | | TIC225S | 700 | | |
| | | TIC225N | 800 | | |
| V _{RRM} | Repetitive peakreverse voltage | TIC225D | 400 | - V | |
| | | TIC225M | 600 | | |
| | | TIC225S | 700 | | |
| | | TIC225N | 800 | | |
| I _{T(RMS)} | RMS on-state current (wave)T _C =70℃ | 8 | А | | |
| I _{TSM} | Non-repetitive peak on-stat | e current | 70 | А | |
| Р _{GM} | Peak gate power $P_W \leqslant 200$ | 2.2 | W | | |
| P _{G(AV)} | Average gate power | 0.9 | W | | |
| Tj | Operating Junction tempera | 110 | °C | | |
| T _{stg} | Storage temperature | -40 ~+125 | °C | | |



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isc Triacs

TIC225series

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | МАХ | UNIT |
|---------------------|---|------|------|
| Rth j-c | Thermal Resistance, Junction to Case | 2.5 | °C/W |
| R _{th j-a} | Thermal Resistance, Junction to Ambient | 62.5 | °C/W |

ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

| SYMBO L | PARAMETER | | CONDITIONS | MAX | UNIT |
|------------------|-----------------------------------|-----|--|------------|------|
| I _{RRM} | Repetitive peak reverse current | | V _{RM} =V _{RRM} , V _{RM} =V _{RRM} , Tj=110 ℃ | 0.4 2.0 | mA |
| I _{DRM} | Repetitive peak off-state current | | V _{DM} =V _{DRM} , V _{DM} =V _{DRM} , Tj=110 °C | 0.4 2.0 | mA |
| IGT | Gate trigger current | Ι | - V _{supply} = 12 V†; R _L = 10 Ω ; t _{p(g)} >20 μ s | 5 | mA |
| | | II | | 20 | |
| | | III | | 10 | |
| | | | | 30 | |
| I _H | Holding current | | V_{supply} = 12 V†, I _G = 0 initial I _{TM} = 100mA | 20 | mA |
| V _{GT} | Gate trigger voltage | Ι | - V _{supply} = 12 V†; R _L = 10 Ω ; t _{p(g)} >20 μ s | 2.0 | V |
| | | II | | 2.0 | |
| | | III | | 2.0 | |
| | | IV | | 2.0 | |
| V _{TM} | On-state voltage | | I _T = 12A; I _G = 50mA | 2.1 | V |



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