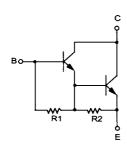
UTCTIP112 NPNEPITAXIAL PLANAR TRANSISTOR

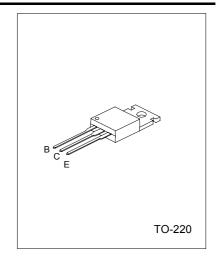
NPN EPITAXIAL SILICON **DARLINGTON TRANSISTOR**

FEATURES

- * High DC Current Gain : h_{FE} = 1000 @V_{CE}=4V, Ic=1A (Min)
- * Low Collector-Emitter Saturation Voltage
- * Industrial Use

EQUIVALENT TEST (R1 \cong 10k Ω , R2 \cong 0.6 Ω)





ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	VALUE	UNIT
Collector to Base Voltage	V _{CBQ.DataSheet4U.co}	100	V
Collector to Emitter Voltage	V_{CEO}	100	V
Emitter to Base Voltage	V_{EBO}	5	V
Collector Current (DC)	Ic	2	Α
Collector Current (Pulse)	Icp	4	Α
Base Current (DC)	I _B	50	mA
Collector Dissipation (Ta=25°C)	Pc	2	W
Collector Dissipation (Tc=25°C)	Pc	50	W
Junction Temperature	Tj	150	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

ELECTRICAL CHARACTERISTICS (Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Collector-Emitter Breakdown Voltage	V _{CEO(SUS)}	I _C =30mA, I _B =0	100			V
Collector Cut-Off Current	I _{CBO}	V _{CB} =100V, I _E =0			1	mA
Collector-Cut-Off Current	I _{CEO}	V_{CE} =50V, I_{B} =0			2	mA
Emitter Cut-Off Current	I _{EBO}	V_{BE} =5V, I_{C} =0			2	mA
DC Current Gain	h_{FE}	I _C =1A, V _{CE} =4V	1000			
		$I_C=2A$, $V_{CE}=4V$	500			
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	I _C =2A, I _B =8mA			2.5	V
Base-Emitter Saturation Voltage	$V_{BE(on)}$	V_{CE} =4V, I_{C} =2A			2.8	V
Output capacitance	Cob	V _{CB} =10V, I _E =0, f=0.1MHz			100	pF

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TYPICAL CHARACTERISTICS

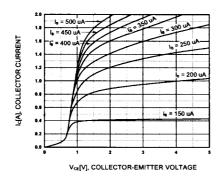


Figure 1. Static Characteristic

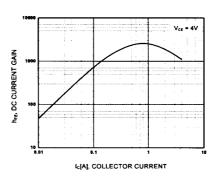


Figure 2. DC current Gain

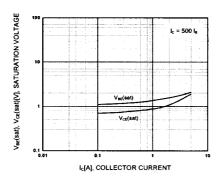


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

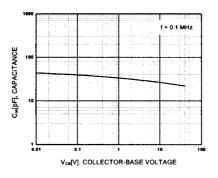


Figure 4. Collector Output Capacitance

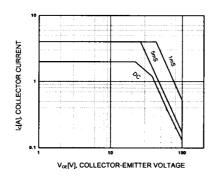


Figure 5. Safe Operating Area

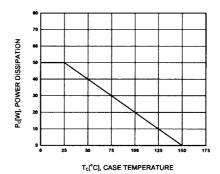


Figure 6. Power Derating

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