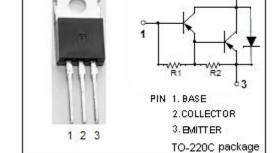


isc Silicon PNP Darlington Power Transistor

TIP136

DESCRIPTION

- · High DC Current Gain-
 - : $h_{FE} = 1000(Min)@I_{C} = -4A$
- · Collector-Emitter Sustaining Voltage-
 - : $V_{CEO(SUS)} = -80V(Min)$
- · Low Collector-Emitter Saturation Voltage-
 - : $V_{CE(sat)} = -2.0V(Max)@I_{C} = -4A$
- Complement to Type TIP131
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

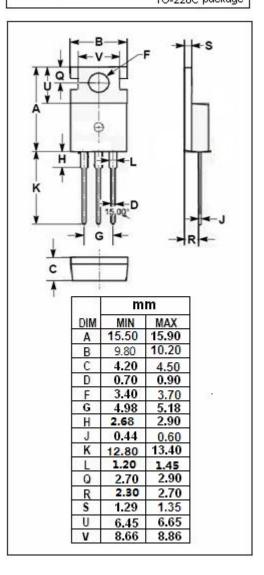
 Designed for general-purpose amplifier and low-speed switching applications

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-80	V
V _{CEO}	Collector-Emitter Voltage	-80	V
V _{EBO}	Emitter-Base Voltage	-5	V
Ic	Collector Current-Continuous	-8	Α
Ісм	Collector Current-Peak	-12	Α
l _Β	Base Current- Continuous	-0.3	Α
Pc	Collector Power Dissipation @T _C =25°C	70	
	Collector Power Dissipation @T _a =25℃	2	W
Tj	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-65~150	$^{\circ}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	1.785	°C/W
Rth j-a	Thermal Resistance, Junction to Ambient 63		°C/W





isc Silicon PNP Darlington Power Transistor

TIP136

ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -30mA, I _B = 0	-80		V
VCE(sat)-1	Collector-Emitter Saturation Voltage	I _C = -4A; I _B = -16mA		-2.0	V
VCE(sat)-2	Collector-Emitter Saturation Voltage	I _C = -6A, I _B = -30mA		-3.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -4A; V _{CE} = -4V		-2.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -80V, I _E = 0		-0.2	mA
I _{CEO}	Collector Cutoff Current	V _{CE} = -40V, I _B = 0		-0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0		-5	mA
h _{FE-1}	DC Current Gain	Ic= -1A; V _{CE} = -4V	500		
h _{FE-2}	DC Current Gain	I _C = -4A; V _{CE} = -4V	1000	15000	

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