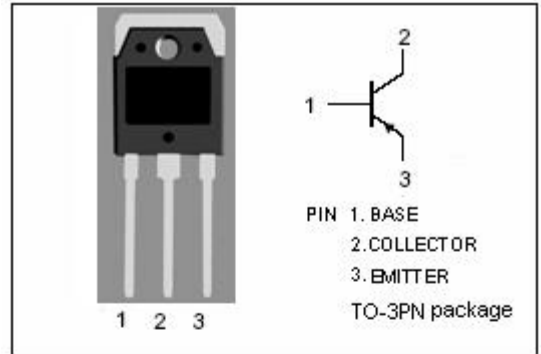


**isc Silicon PNP Power Transistor**
**TIP34**
**DESCRIPTION**

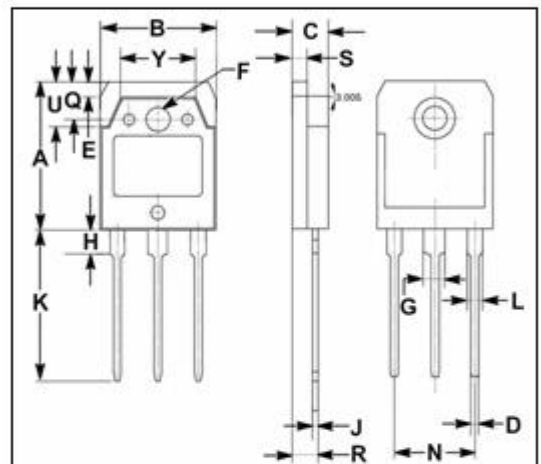
- DC Current Gain-  
:  $h_{FE} = 40(\text{Min}) @ I_C = -1\text{A}$
- Collector-Emitter Sustaining Voltage-  
:  $V_{CEO(\text{SUS})} = -40\text{V}(\text{Min})$
- Complement to Type TIP33
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**APPLICATIONS**

- Designed for use in general purpose power amplifier and switching applications.

**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-40	V
$V_{CEO}$	Collector-Emitter Voltage	-40	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current -Continuous	-10	A
$I_{CM}$	Collector Current-peak	-15	A
$I_B$	Base Current	-3	A
$P_C$	Collector Power Dissipation@ $T_C = 25^\circ\text{C}$	80	W
$T_j$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-65~150	$^\circ\text{C}$



DIM	mm	
	MIN	MAX
A	19.60	20.10
B	15.50	15.70
C	4.70	4.90
D	0.90	1.10
E	1.90	2.10
F	3.40	3.60
G	2.90	3.20
H	3.20	3.40
J	0.595	0.605
K	20.00	20.70
L	1.90	2.20
N	10.89	10.91
Q	4.90	5.10
R	3.35	3.45
S	1.995	2.100
U	5.90	6.10
Y	9.90	10.10

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	1.56	$^\circ\text{C}/\text{W}$

## isc Silicon PNP Power Transistor

## TIP34

## ELECTRICAL CHARACTERISTICS

T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = -30mA; I <sub>B</sub> = 0	-40		V
V <sub>CE(sat)-1</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.3A		-1.0	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -10A; I <sub>B</sub> = -2.5A		-4.0	V
V <sub>BE(on)-1</sub>	Base-Emitter On Voltage	I <sub>C</sub> = -3A; V <sub>CE</sub> = -4V		-1.6	V
V <sub>BE(on)-2</sub>	Base-Emitter On Voltage	I <sub>C</sub> = -10A; V <sub>CE</sub> = -4V		-3.0	V
I <sub>CEO</sub>	Collector Cutoff Current	V <sub>CE</sub> = -30V; I <sub>B</sub> = 0		-0.7	mA
I <sub>CES</sub>	Collector Cutoff Current	V <sub>CE</sub> = -40V; V <sub>EB</sub> = 0		-0.4	mA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0		-1.0	mA
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -1A; V <sub>CE</sub> = -4V	40		
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -3A; V <sub>CE</sub> = -4V	20	100	
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -10V; f <sub>test</sub> = 1.0MHz	3		MHz

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