

TISP4C015L1N THRU TISP4C035L1N

LOW CAPACITANCE BIDIRECTIONAL THYRISTOR OVERVOLTAGE PROTECTOR

This series is not recommended for new designs. The Model TISP4P0xxL1N Series is the recommended pin-to-pin replacement.



TISP4C0xxL1N Overvoltage Protector Series

Designed for ADSL, ADSL2, VDSL, VDSL2 protection

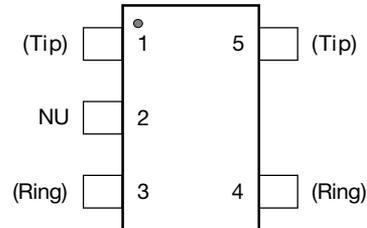
Ion-Implanted Breakdown Region
- Precise and Stable Voltage

Low Voltage Overshoot Under Surge

Low Off-State Capacitance

Device Name	V _{DRM} V	V _(BO) V
TISP4C015L1N	8	15
TISP4C020L1N	12	20
TISP4C025L1N	16	25
TISP4C035L1N	24	35

SOT23-5 Package (Top View)



Terminal typical application names shown in parenthesis.
NU - Non-usable; no external electrical connection should be made to this terminal.

MD-SOT23-5-001-a

Rated for International Surge Wave Shapes

Wave Shape	Standard	I _{PPSM} A
8/20	IEC 61000-4-5	30
10/1000	GR-1089-CORE	18

Device Symbol



SD-TISP4-002-a

Description

This range of devices is designed to protect xDSL line-driver interfaces from overvoltages. Overvoltages are normally caused by a.c. power-system or lightning-flash disturbances which are induced or conducted onto the telephone line. These symmetrical protectors are two-terminal thyristor-crowbar devices. They can be used to protect between conductors, or a pair of devices can be deployed to protect from line to ground. When placed between the xDSL line driver IC and the transformer, this

protector will clamp and switch into a low-impedance state, safely diverting the energy transferred by the xDSL coupling transformer. The low capacitance design makes this device suitable for designs from ADSL all the way up to 30 MHz VDSL2.

Telecom ports need protection against Common Mode (Longitudinal) and Differential (Metallic) surges, to comply with international standards such as ITU-T K.20, K.21 or K.45, Telcordia GR-1089-CORE and YD/T. Common Mode surges are resisted by the galvanic isolation of the coupling transformer which is commonly rated to 2 kV or greater. Differential surges can be transmitted by the transformer, and can stress the Line Driver Interface IC. As the xDSL interface circuit is designed to operate from 3 kHz to to 30 MHz, nearby high frequency events - such as cable flashover or primary protection activation - can generate damaging conditions for the interface requiring this type of protection.

Please contact your Bourns representative if the protection voltage you require is not listed.

How to Order

Device	Package	Carrier	Order As	Marking Code	Reel Quantity
TISP4C0xxL1N	SOT23-5	Embossed Tape Reeled	TISP4C0xxL1NR-S	Cxx	10000

Insert xx corresponding to device name.

JULY 2008 - REVISED NOVEMBER 2015

*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.

Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.

Users should verify actual device performance in their specific applications.

TISP4C0xxL1N Overvoltage Protector Series

BOURNS®

Absolute Maximum Ratings, $T_A = 25\text{ °C}$ (Unless Otherwise Noted)

Rating	Symbol	Value	Unit
Repetitive peak off-state voltage	V_{DRM}	'4C015L1N ±8 '4C020L1N ±12 '4C025L1N ±16 '4C035L1N ±24	V
Non-repetitive peak impulse current (see Notes 1, 2 and 3) 8/20 μs (IEC 61000-4-5, 1.2/50 μs voltage, 8/20 current combination wave generator) 10/1000 μs (GR-1089-CORE, 10/1000 μs voltage wave shape)	I_{PPSM}	±30 ±18	A
Junction temperature	T_J	-40 to +150	°C
Storage temperature range	T_{stg}	-65 to +150	°C

- NOTES: 1. Initially the device must be in thermal equilibrium with $T_J = 25\text{ °C}$.
 2. The surge may be repeated after the device returns to its initial conditions.
 3. Rated currents only apply if pins 1 & 5 (Tip) are connected together and pins 3 & 4 (Ring) are connected together.

Electrical Characteristics, $T_A = 25\text{ °C}$ (Unless Otherwise Noted)

Parameter	Test Conditions	Min	Typ	Max	Unit
I_{DRM} Repetitive peak off-state current	$V_D = V_{DRM}$			±1	μA
$V_{(BO)}$ Breakover voltage	$dv/dt = \pm 250\text{ V/ms}$, $R_{SOURCE} = 300\ \Omega$			'4C015L1N ±15 '4C020L1N ±20 '4C025L1N ±25 '4C035L1N ±35	V
I_H Holding current	$I_T = \pm 5\text{ A}$, $di/dt = \pm 30\text{ mA/ms}$		'4C015L1N ±50 '4C020L1N ±20 '4C025L1N ±50 '4C035L1N ±50		mA
C_O Off-state capacitance	$f = 1\text{ MHz}$, $V_d = 1\text{ V rms}$, $V_D = 2\text{ V}$			'4C015L1N 15 '4C020L1N 15 '4C025L1N 15 '4C035L1N 15	pF
ΔC Delta-capacitance	$f = 1\text{ MHz}$, $V_d = 1\text{ V rms}$, $V_D = 1\text{ V to } V_{DRM}$		'4C015L1N 3 '4C020L1N 3.5 '4C025L1N 3.5 '4C035L1N 4.5		pF

Parameter Measurement Information

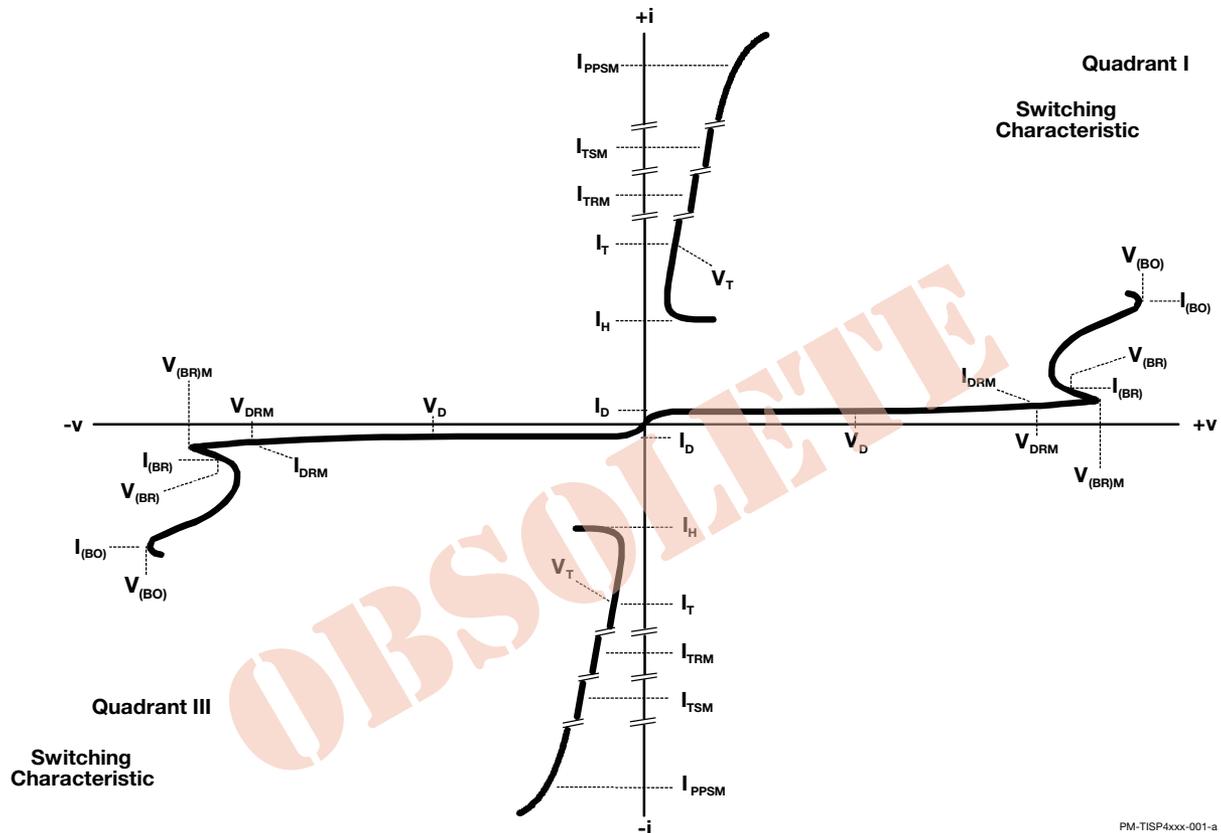
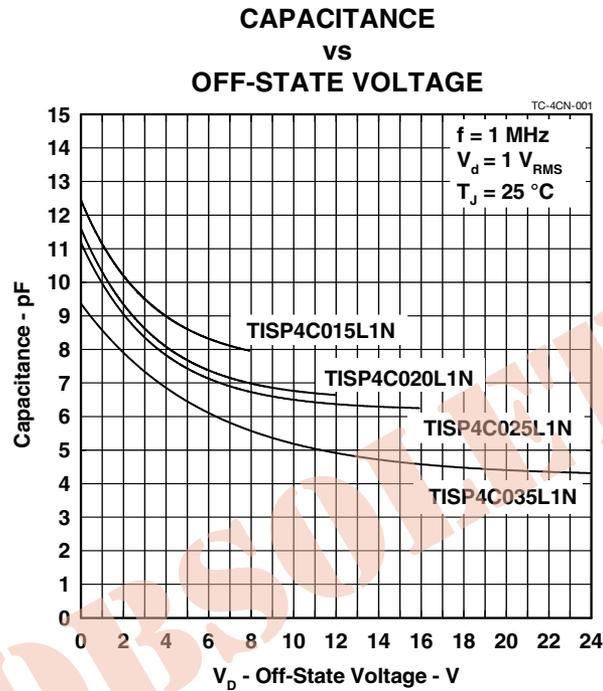


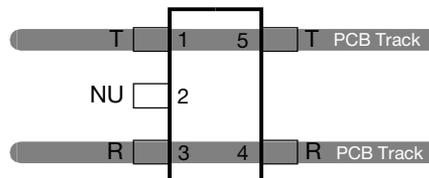
Figure 1. Voltage-Current Characteristic for Tip and Ring Terminals
All Measurements are Referenced to the Ring Terminal

PM-TISP4xxx-001-a

Typical Characteristics



Recommended PCB Layout



MD-SOT223-5-xxx

Figure 3.

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The Americas:	+1-951-781-5500	+1-951-781-5700
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