TOSHIBA Field Effect Transistor Silicon N Channel MOS Type $(\pi - MOSVII)$

TK11A45D

Switching Regulator Applications

• Low drain-source ON-resistance: RDS (ON) = 0.5Ω (typ.)

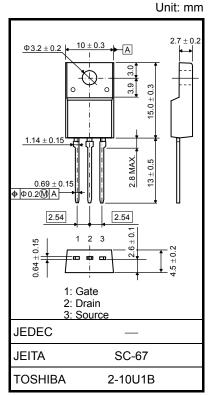
• High forward transfer admittance: $|Y_{fs}| = 3.2 \text{ S}$ (typ.)

• Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 450 V)$

• Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	450	V	
Gate-source voltage		V_{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	11	Α	
	Pulse (Note 1)	I _{DP}	44	A	
Drain power dissipati	on (Tc = 25°C)	P _D	40	W	
Single pulse avalanch	ne energy (Note 2)	E _{AS}	238	mJ	
Avalanche current		I _{AR}	11	Α	
Repetitive avalanche	energy (Note 3)	E _{AR}	4.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

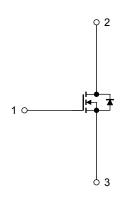
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 3.27 mH, R_G = 25 Ω , I_{AR} = 11 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



Start of commercial production 2009-09

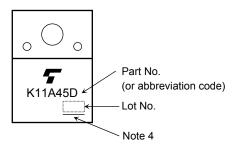
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current		I _{DSS}	V _{DS} = 450 V, V _{GS} = 0 V	_	_	10	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	450	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 5.5 A	_	0.5	0.62	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5.5 A	0.8	3.2	_	S
Input capacitance		C _{iss}		_	1050	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	5	_	pF
Output capacitance		Coss			100	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} \\ \text{V}_{GS} \\ 0 \text{ V} \\ \hline \\ 50 \Omega \end{array} \begin{array}{c} \text{I}_{D} = 5.5 \text{ A} \\ \text{V}_{OUT} \\ \\ \text{\approx} \\ \text{\approx} \\ \text{\approx} \\ \text{\approx} \\ \text{\sim} \\ \text$	_	25	_	
	Turn-on time	t _{on}			60	_	ns
	Fall time	t _f			10	_	
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \mu s$		75	_	
Total gate charge		Qg		_	20		
Gate-source charge		Q _{gs}	$V_{DD} \approx 360 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 11 \text{ A}$	_	13		nC
Gate-drain charge		Q _{gd}		_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

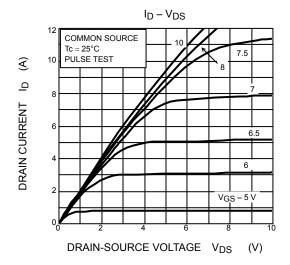
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note	1) I _{DR}	_	_	_	11	Α
Pulse drain reverse current (Note	1) I _{DRP}	_	_	_	44	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 11 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 11 A, V _{GS} = 0 V,	_	1350	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	14		μС

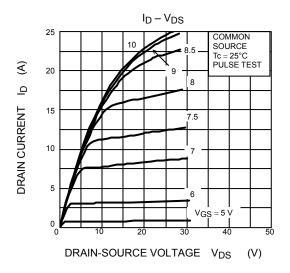
Marking

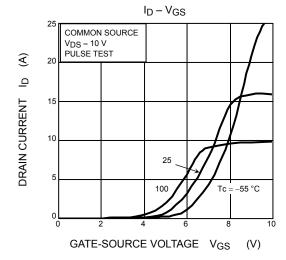


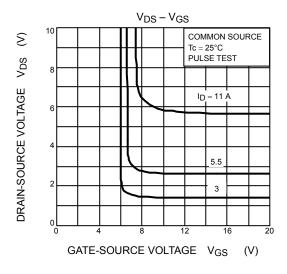
Note 4 : A line under a Lot No. identifies the indication of product Labels $\hbox{[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$

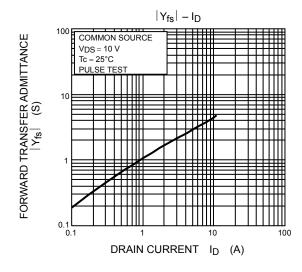
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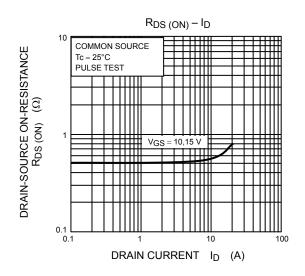


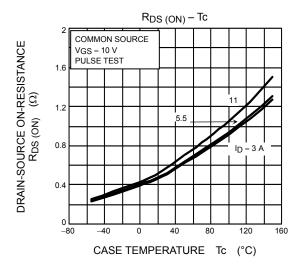


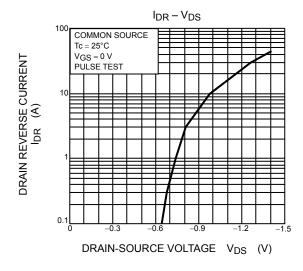


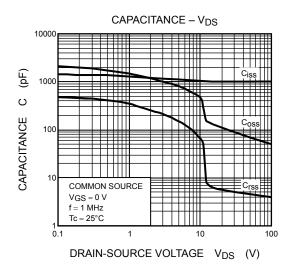


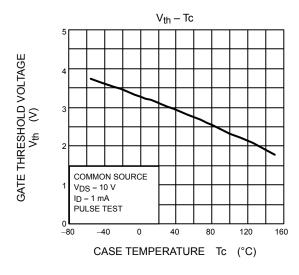


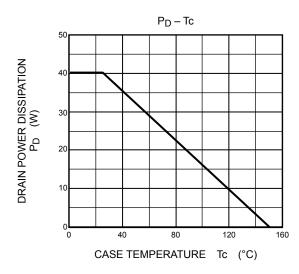


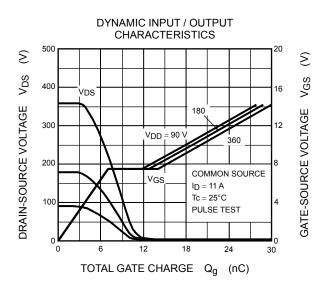


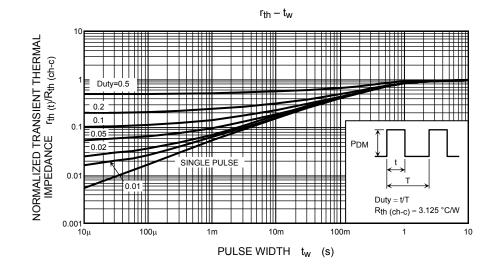


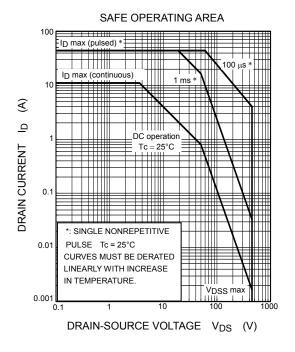


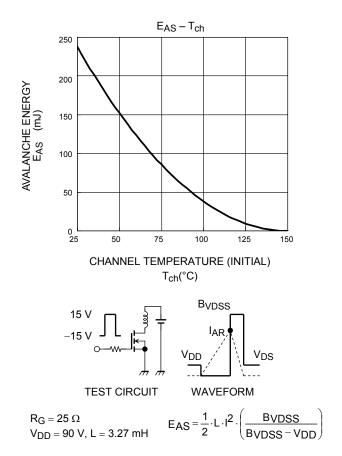












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