

Isc N-Channel MOSFET Transistor

TK12P60W

• FEATURES

- With To-252(DPAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

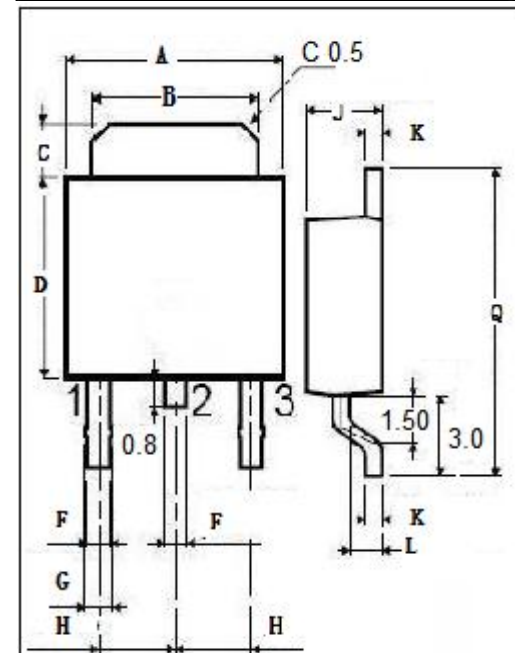
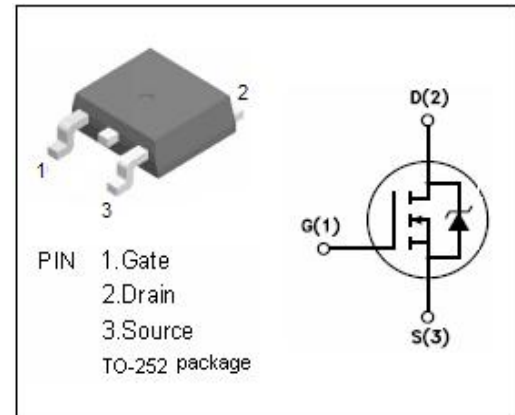
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous	11.5	A
I_{DM}	Drain Current-Single Pulsed	46	A
P_D	Total Dissipation @ $T_c=25^{\circ}\text{C}$	100	W
T_{ch}	Max. Operating Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature	-55~150	$^{\circ}\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.25	$^{\circ}\text{C/W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	50	$^{\circ}\text{C/W}$



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
F	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

Isc N-Channel MOSFET Transistor**TK12P60W****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D = 10mA	600			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ; I _D =0.6mA	2.7		3.7	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = 10V; I _D =5.8A		265	340	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±30V; V _{DS} = 0V			±1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =600V; V _{GS} = 0V			10	μA
V _{SDF}	Diode forward voltage	I _{SD} =11.5A, V _{GS} = 0 V			1.7	V

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