TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type ( $\pi$ -MOS VI)

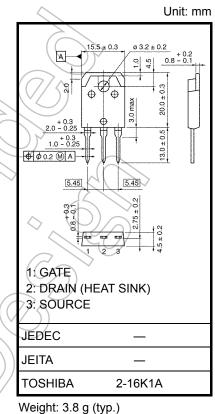
# **TK20H50C**

#### Switching Regulator Applications

- Low drain-source ON resistance :  $R_{DS (ON)} = 0.23 \Omega$  (typ.)
- High forward transfer admittance  $: |Y_{fs}| = 14 \text{ S (typ.)}$
- Low leakage current :  $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$ 
  - Enhancement mode :  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	500	$(N \land$	$\geq$
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	500	× V	
Gate-source voltage		V <sub>GSS</sub>	±30	Y	
Drain current	DC (Note 1)	I <sub>D</sub>	20	A	
Drain current	Pulse (Note 1)	I <sub>DP</sub>	80	A	
Drain power dissipation (Tc = 25°C)		PD	150	W	(
Single-pulse avalanche energy (Note 2)		EAS	960	mJ	
Avalanche current		IAR	20	A	
Repetitive avalanche energy (Note 3)		EAR	) 15	mJ	$\geq$
Channel temperature		Tch	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	)°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

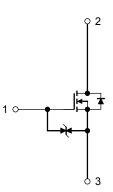
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	0.833	°C / W
Thermal resistance, channel to ambient	Rth (ch-a)	50	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.08 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 20 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



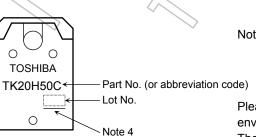
**Electrical Characteristics (Ta = 25°C)** 

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	$V_{GS}$ = ±25 V, $V_{DS}$ = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30		_	V
Drain cutoff curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	X	_	100	μA
Drain-source br	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500		_	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	)/	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		0.23	0.27	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A	7.0	14	_	S
Input capacitance		C <sub>iss</sub>			3100	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		20	_	pF
Output capacitance		Coss			270	1	
Switching time	Rise time	tr	V <sub>GS</sub> 10 V 」 10=10A	- (	70	> $ $	
	Turn on time	t <sub>on</sub>	$R_L = 20 \Omega$	N C	130	) –	
	Fall time	t <sub>f</sub>	\$ 1 m v <sub>DD</sub> ≈ 200 v		70		ns
	Turn off time	t <sub>off</sub>	Duty $\leq$ 1%, t <sub>w</sub> $\geq$ 10 $\mu$ s		280		
Total gate charge (gate-source plus gate-drain)		Qg		_	62	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	_	40	_	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>		_	22	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		Ι	Ι	20	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	<u> </u>	_	_	80	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V			-1.7	V
Reverse recovery time	trr	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V		1200	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs	_	18		μC

### Marking



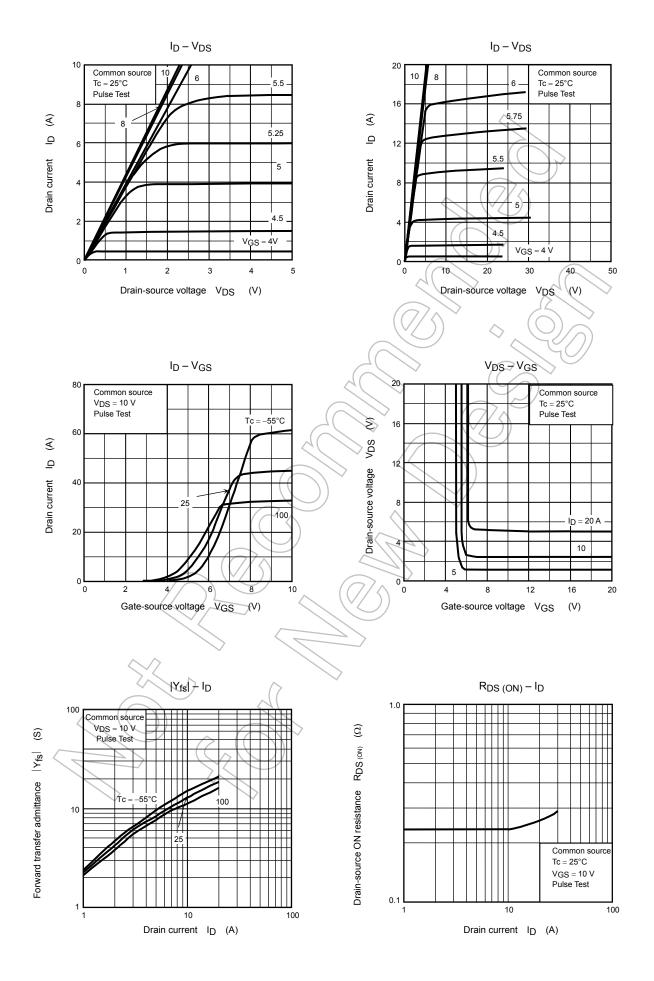
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

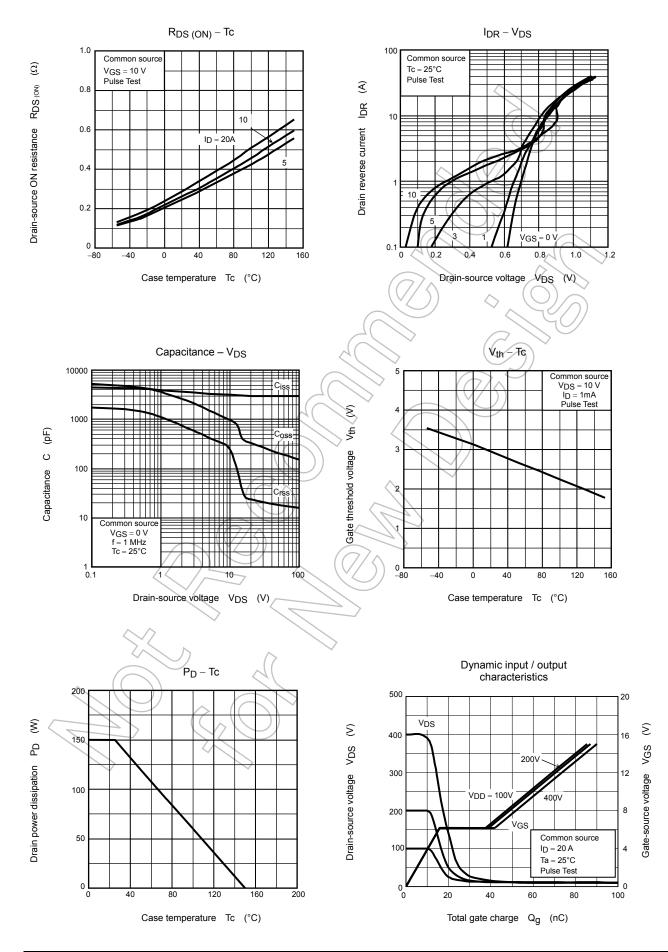
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

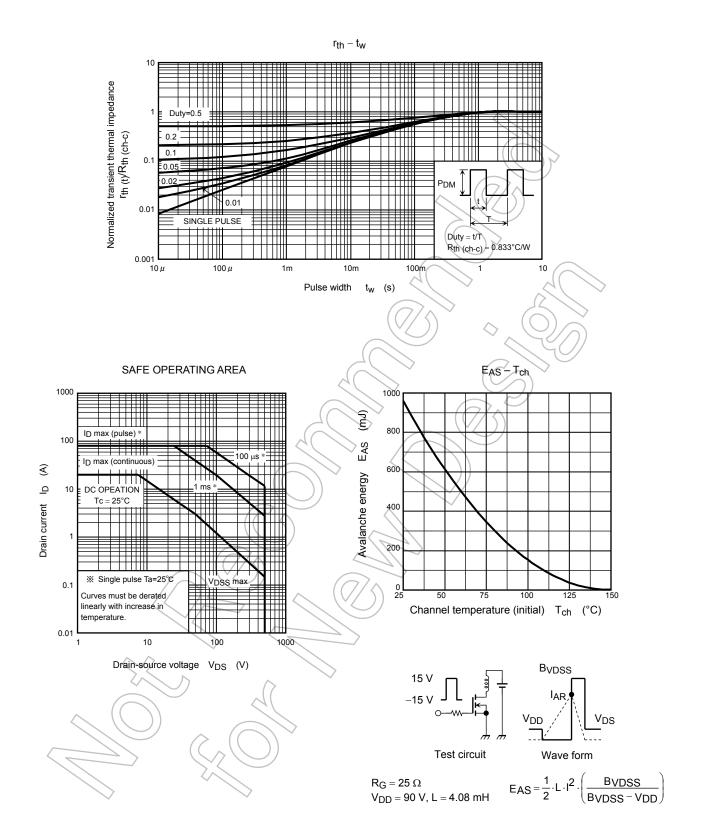
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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