TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOS VII)

TK20J50D

Switching Regulator Applications

• Low drain-source ON-resistance: RDS (ON) = 0.22Ω (typ.)

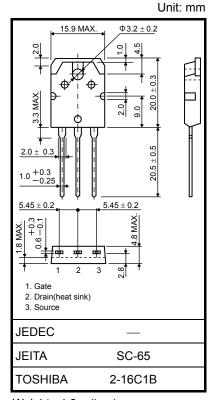
• High forward transfer admittance: $|Y_{fs}| = 8.5 \text{ S (typ.)}$

• Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 500 V)$

• Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	500	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I _D	20	Α
	Pulse (Note 1)	I _{DP}	80	A
Drain power dissipati	on (Tc = 25°C)	P_{D}	280	W
Single pulse avalanche energy (Note 2)		E _{AS}	470	mJ
Avalanche current		I _{AR}	20	Α
Repetitive avalanche energy (Note 3)		E _{AR}	28	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

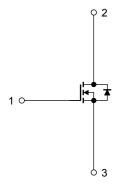
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.446	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $~V_{DD}=90~V,~T_{ch}=25^{\circ}C$ (initial), $L=2.0~mH,~R_{G}=25~\Omega,~I_{AR}=20~A$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



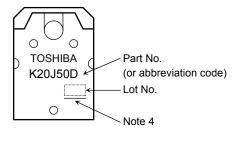
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current		I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	10	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	-resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 10 A		0.22	0.27	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 10 A	2.4	8.5	_	S
Input capacitance		C _{iss}			2600	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		11	_	
Output capacitance		Coss			280	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{ID} = 10 \text{ A} & \text{Vout} \\ \hline V_{GS} & \text{Vout} & \text{S} \\ 50 \Omega & \text{Vout} \\ \hline V_{DD} \approx 200 \text{ V} \\ \end{array}$ Duty $\leq 1\%, \ t_W = 10 \mu\text{s}$	_	50	_	- ns
	Turn-on time	t _{on}		_	100	_	
	Fall time	t _f		_	25	_	
	Turn-off time	t _{off}		_	150	_	
Total gate charge		Qg		_	45	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	_	28	_	nC
Gate-drain charge		Q _{gd}		_	17	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

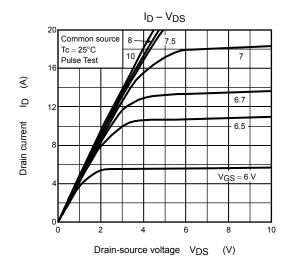
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	20	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	80	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 20 A, V _{GS} = 0 V	_	_	-1.7	٧
Reverse recovery time	t _{rr}	I _{DR} = 20 A, V _{GS} = 0 V,	_	1700	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	26	_	μС

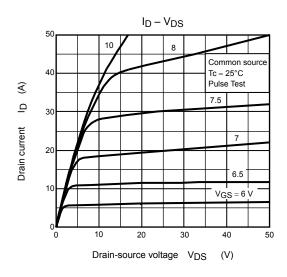
Marking

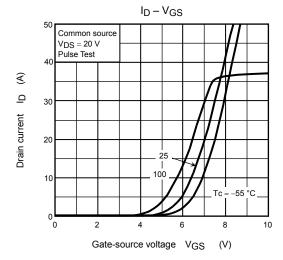


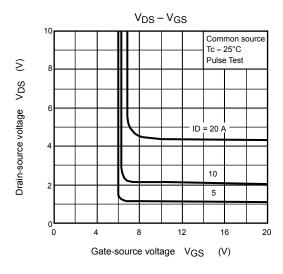
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

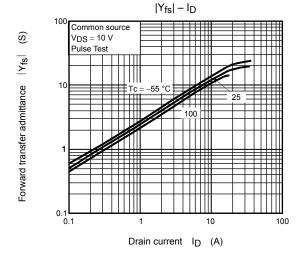
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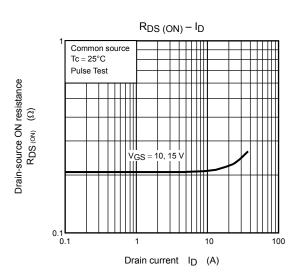




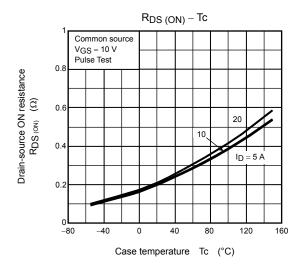


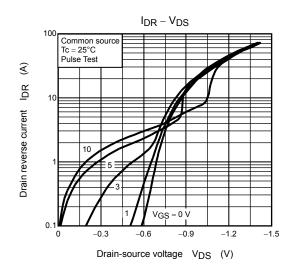


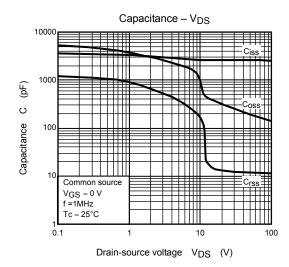


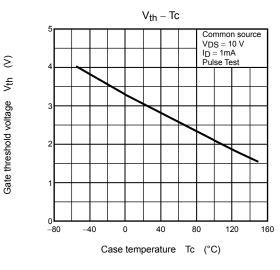


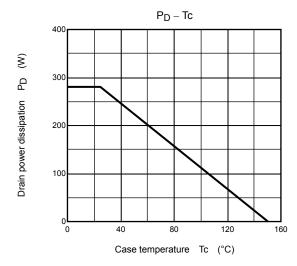
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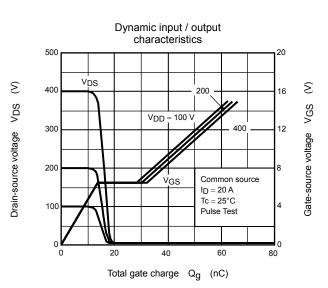


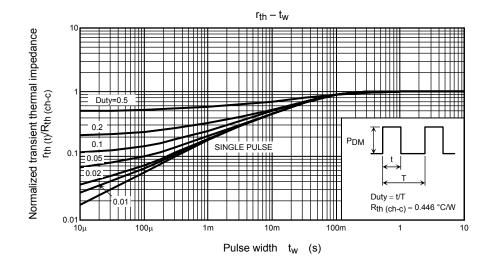


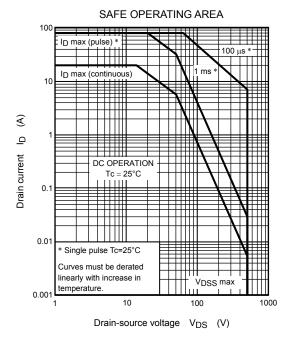


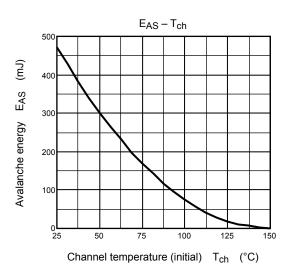


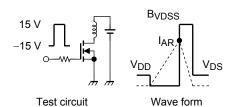












$$R_G = 25~\Omega$$
 $V_{DD} = 90~V,~L = 2.0~mH$

$$EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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