

# isc N-Channel MOSFET Transistor

# **TK39N60W5**

#### FEATURES

- Low drain-source on-resistance:
  R<sub>DS</sub>(on) ≤0.074Ω.
- Enhancement mode:
  Vth =3 to4.5V (Vbs = 10 V, Ib=1.9mA)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### DESCRITION

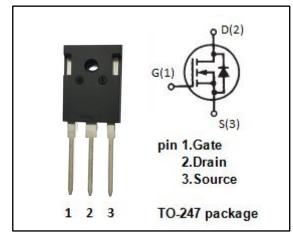
Switching Voltage Regulators

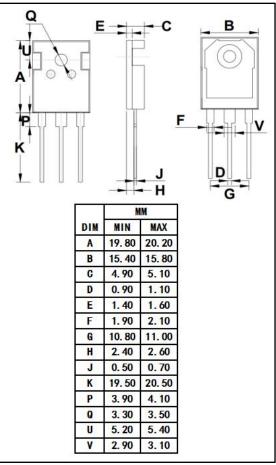
### • ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	600	V
V <sub>GS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub>	Drain Current-Continuous	38.8	Α
I <sub>DM</sub>	Drain Current-Single Pulsed	155	Α
P <sub>D</sub>	Total Dissipation @Tc=25℃	270	W
Tj	Max. Operating Junction Temperature 150		$^{\circ}$
T <sub>stg</sub>	Storage Temperature	-55~150	°C

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT					
Rth(ch-c)	Channel-to-case thermal resistance	0.463	°C/W					







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#### **ELECTRICAL CHARACTERISTICS**

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> =10mA	600			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =10V; I <sub>D</sub> =1.5mA	3		4.5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> =19.4A			74	mΩ
lgss	Gate-Source Leakage Current	V <sub>GS</sub> = ±30V;V <sub>DS</sub> = 0V			±1	μ <b>А</b>
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =600V; V <sub>GS</sub> = 0V			100	uA
V <sub>SDF</sub>	Diode forward voltage	I <sub>DR</sub> =38.8A, V <sub>GS</sub> = 0 V			1.7	V



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