

INCHANGE SEMICONDUCTOR

iscN-Channel MOSFET Transistor

TK75J04K3Z

RDS(• Enhand Vth = • 100% a • Minimu perform • DESCI • Switchi	ain-source on-resistance: ON) = $3m\Omega$ (MAX) cement mode: = 3 to 4V (V _{DS} = 10 V, I _D =1.0mA) avalanche tested Im Lot-to-Lot variations for robust de hance and reliable operation	I 2 3 I 2 3 pin 1.Gate 2.Drain 3.Source TO-3PN package Image: Second		
SYMBOL	PARAMETER	VALUE	UNIT	
VDSS	Drain-Source Voltage	40	V	
V _{GS}	Gate-Source Voltage	±20	V	
Ι _D	Drain Current-Continuous	75	А	
I _{DM}	Drain Current-Single Pulsed	300	A	mm DIM MIN MAX A 19.60 20.30
PD	Total Dissipation @Tc=25℃	150	w	B 15.50 15.70 C 4.70 4.90 D 0.90 1.10
Tj	Max. Operating Junction Temperature	150	°C	E 1.90 2.10 F 3.40 3.60 G 2.90 3.20
T _{stg}	Storage Temperature	-55~150	°C	H 3.20 3.40 J 0.595 0.605 K 19.80 20.70
THERMAL CHARACTERISTICS				L 1.90 2.20 N 10.89 10.91
SYMBOL	PARAMETER	МАХ	UNIT	R 3.35 3.45 S 1.995 2.100
Rth(ch-c)	Channel-to-case thermal resistance	1	°C/W	U 5.90 6.20 Y 9.90 10.10



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ELECTRICAL CHARACTERISTICS

$T_{\texttt{C}}\text{=}25^{\circ}\!\!\!\mathbb{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	МАХ	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D = 10mA	40			V
V _{GS} (th)	Gate Threshold Voltage	V _{DS} = 10V; I _D =1.0mA	3		4	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V; I _D =38A			3	mΩ
lgss	Gate-Source Leakage Current	V _{GS} = ±30V;V _{DS} = 0V			±1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =40V; V _{GS} = 0V			10	μA
V _{SDF}	Diode forward voltage	I _{DR} =75A, V _{GS} = 0 V			1.2	V



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