

## isc N-Channel MOSFET Transistor

TK9P65W

## • FEATURES

- Low drain-source on-resistance:  
 $R_{DS(on)} \leq 0.56\Omega$ .
- Enhancement mode:  
 $V_{th} = 2.5$  to  $3.5V$  ( $V_{DS} = 10V$ ,  $I_D = 0.35mA$ )
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

## • DESCRIPTION

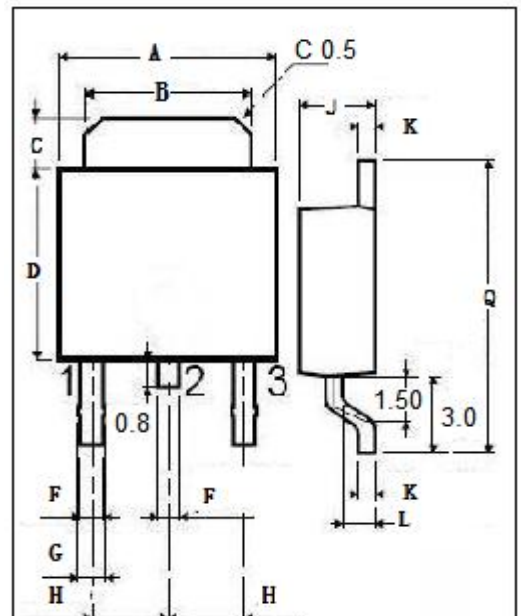
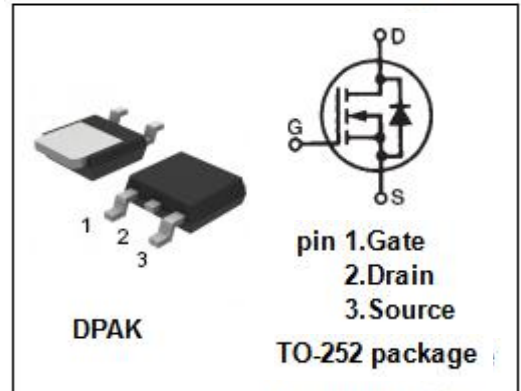
- Switching Voltage Regulators

• ABSOLUTE MAXIMUM RATINGS( $T_a = 25^\circ C$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current-Continuous	9.3	A
$I_{DM}$	Drain Current-Single Pulsed	37.2	A
$P_D$	Total Dissipation @ $T_c = 25^\circ C$	80	W
$T_j$	Max. Operating Junction Temperature	150	$^\circ C$
$T_{stg}$	Storage Temperature	-55~150	$^\circ C$

## • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.57	$^\circ C/W$



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
E	0.65	
F	0.75	
G	2.10	2.50
H	2.10	2.40
J	0.40	0.60
K	0.90	1.10
L	9.90	10.1
Q		

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## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> =10mA	650			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =10V; I <sub>D</sub> =0.35mA	2.5		3.5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> =4.6A			0.56	Ω
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±30V; V <sub>DS</sub> = 0V			±1	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =650V; V <sub>GS</sub> = 0V			10	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>DR</sub> =9.3A, V <sub>GS</sub> = 0 V			1.7	V

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