

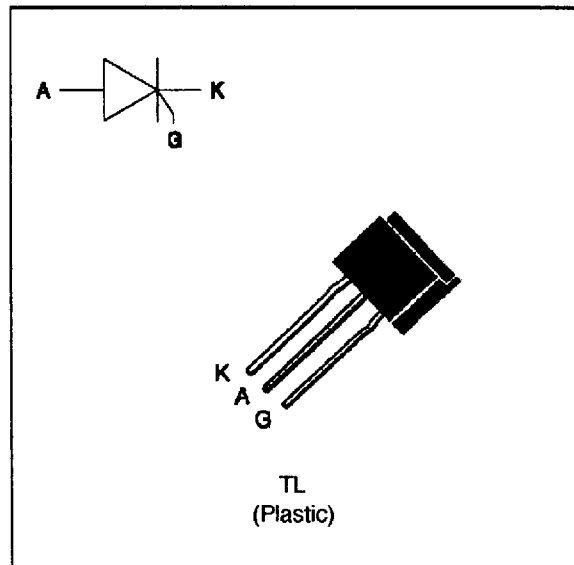
FEATURES

- HIGH SURGE CAPABILITY
- HIGH ON-STATE CURRENT
- HIGH STABILITY AND RELIABILITY

DESCRIPTION

The TL 1006 ----> TL 8006 Family of Silicon Controlled Rectifiers uses a high performance glass passivated technology.

This general purpose Family of Silicon Controlled Rectifiers is designed for power supplies up to 400Hz on resistive or inductive load.



ABSOLUTE RATINGS (limiting values)

Symbol	Parameter		Value	Unit
$I_{T(RMS)}$	RMS on-state current (180° conduction angle)	$T_I = 55\text{ °C}$	3	A
$I_{T(AV)}$	Average on-state current (180° conduction angle, single phase circuit)	$T_I = 55\text{ °C}$	2	A
I_{TSM}	Non repetitive surge peak on-state current (T_j initial = 25°C)	$t_p = 8.3\text{ ms}$	73	A
		$t_p = 10\text{ ms}$	70	
I^2t	I^2t value	$t_p = 10\text{ ms}$	25	A ² s
di/dt	Critical rate of rise of on-state current Gate supply : $I_G = 100\text{ mA}$ $di_G/dt = 1\text{ A}/\mu\text{s}$		100	A/ μs
T_{stg} T_j	Storage and operating junction temperature range		- 40 to + 150 - 40 to + 110	°C °C
T_I	Maximum lead temperature for soldering during 4 s at 4.5 mm from case		230	°C

Symbol	Parameter	TL					Unit
		1006	2006	4006	6006	8006	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage $T_j = 110\text{ °C}$	100	200	400	600	800	V

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
Rth (j-a)	Junction to ambient on printed circuit with Cu surface 1cm ²	50	°C/W
Rth (j-l) DC	Junction to leads for DC	15	°C/W

GATE CHARACTERISTICS (maximum values)

$P_G (AV) = 1W$ $P_{GM} = 10W$ ($t_p = 20 \mu s$) $I_{FGM} = 4A$ ($t_p = 20 \mu s$) $V_{RGM} = 5V$.

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions			Value	Unit
I _{GT}	V _D =12V (DC) R _L =33Ω	T _J =25°C	MAX	15	mA
V _{GT}	V _D =12V (DC) R _L =33Ω	T _J =25°C	MAX	1.5	V
V _{GD}	V _D =V _{DRM} R _L =3.3kΩ	T _J =110°C	MIN	0.2	V
t _{gt}	V _D =V _{DRM} I _G = 40mA dI _G /dt = 0.5A/μs	T _J =25°C	TYP	1.5	μs
I _L	I _G = 1.2 I _{GT}	T _J =25°C	TYP	40	mA
I _H	I _T = 100mA gate open	T _J =25°C	TYP	20	mA
V _{TM}	I _{TM} = 6A t _p = 380μs	T _J =25°C	MAX	1.9	V
I _{DRM} I _{RRM}	V _{DRM} Rated V _{RRM} Rated	T _J =25°C	MAX	0.01	mA
		T _J =110°C		1	
dV/dt	Linear slope up to V _D =67%V _{DRM} gate open	T _J =110°C	MIN	200	V/μs
t _q	V _D =67%V _{DRM} I _{TM} = 6A V _R = 10V dI _{TM} /dt=10 A/μs dV _D /dt= 20V/μs	T _J =110°C	TYP	70	μs

Fig.1 : Maximum average power dissipation versus average on-state current.

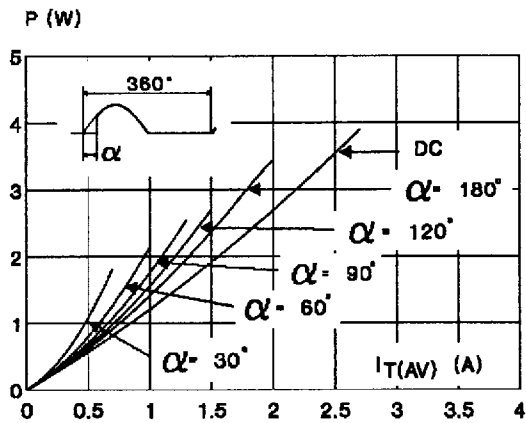


Fig.2 : Correlation between maximum average power dissipation and maximum allowable temperatures (T_{amb} and T_{lead}).

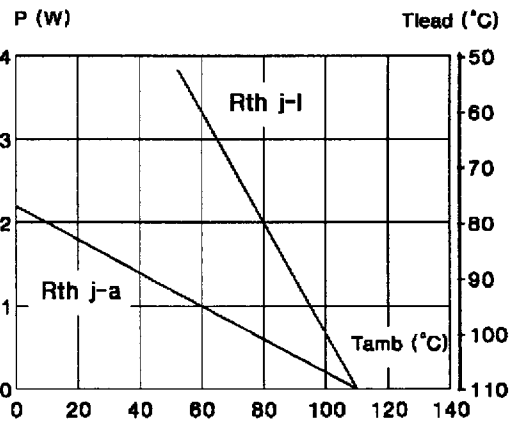


Fig.3 : Average on-state current versus leads temperature.

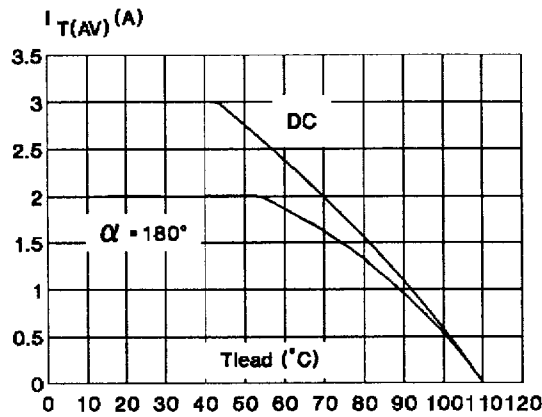


Fig.4 : Thermal transient impedance junction to ambient versus pulse duration.

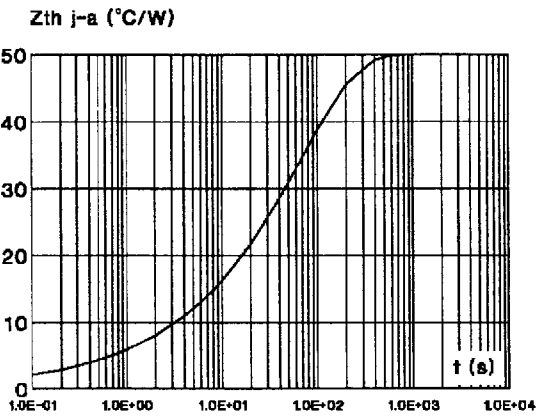


Fig.5 : Relative variation of gate trigger current versus junction temperature.

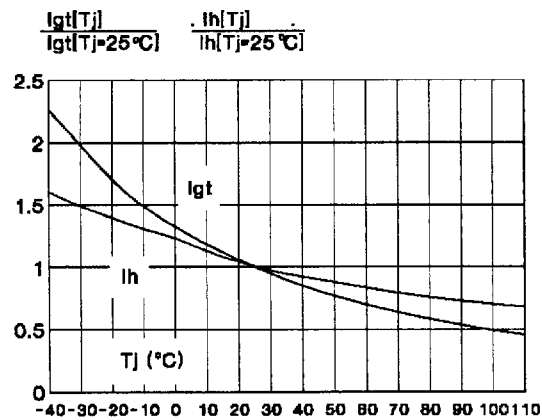


Fig.6 : Non repetitive surge peak on-state current versus number of cycles.

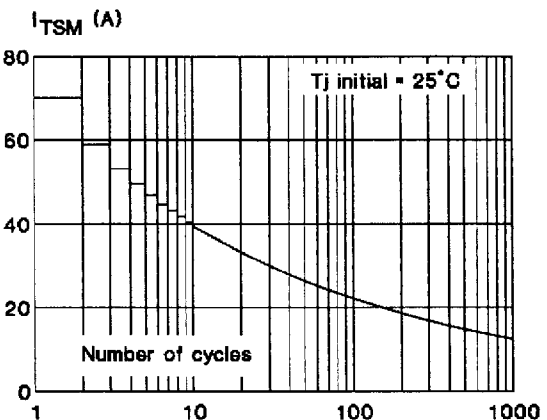


Fig.7 : Non repetitive surge peak on-state current for a sinusoidal pulse with width : $t \leq 10$ ms, and corresponding value of I^2t .

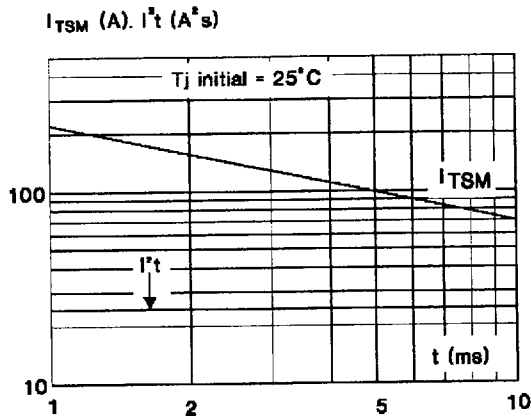
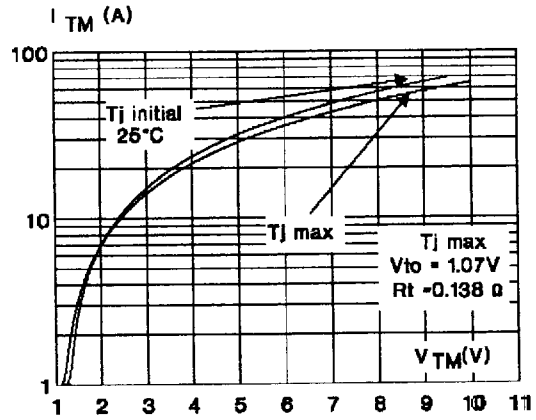
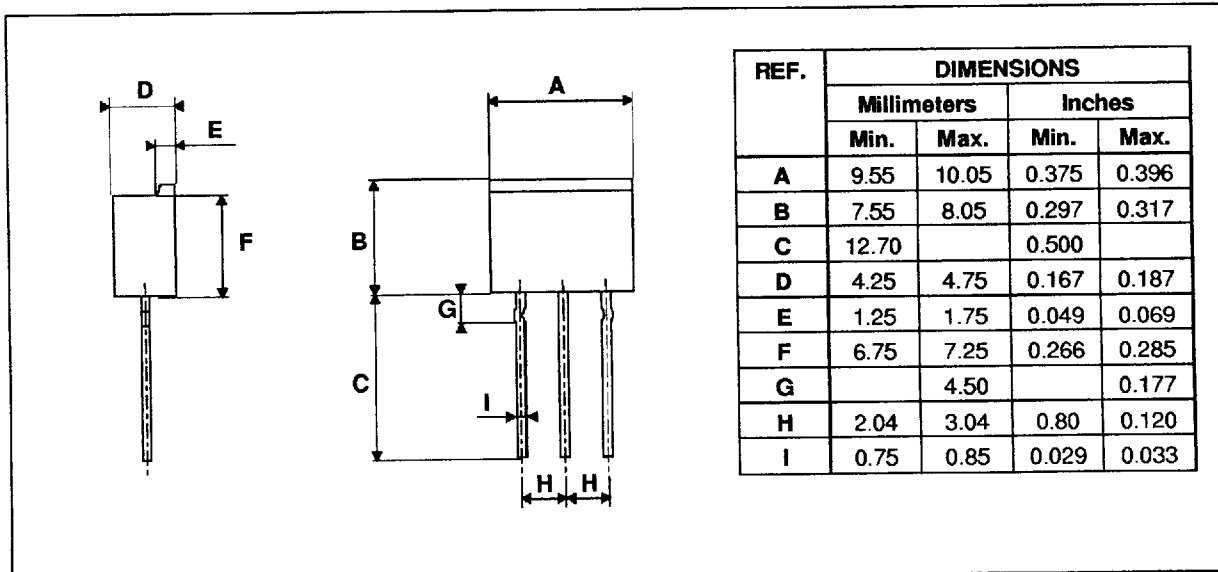


Fig.8 : On-state characteristics (maximum values).



PACKAGE MECHANICAL DATA

TL Plastic



Marking : type number
Weight : 0.8 g

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