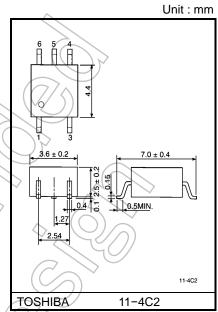
TOSHIBA Photocoupler IRED & Photo-IC

# **TLP112**

Interfaces of measuring and control instruments
Digital Logic Isolation
Line Receiver
Switching Power Supply Feedback Control
Industrial Inverter

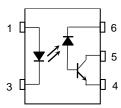
The TOSHIBA mini flat coupler TLP112 is a small outline coupler, suitable for surface mount assembly. TLP112 consists of an infrared emitting diode, optically coupled to a high speed detector of one chip photodiode–transistor.

- Isolation voltage: 2500 Vrms (min)
- Switching speed:  $t_{pHL}$  = 0.8 $\mu$ s,  $t_{pLH}$  = 2  $\mu$ s(max) (R<sub>L</sub> = 4.1 k $\Omega$ )
- TTL compatible by connecting external resistance
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349



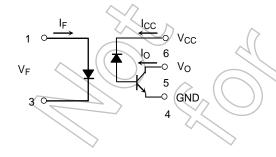
Weight: 0.09 g (typ.)

### Pin Configuration (top view)



- 1: ANODE
- 3 : CATHODE
- 4 : EMITTER (GND)
- 5 : COLLECTOR (OUTPUT)
- 6 : Vcc

#### **Schematic**



Start of commercial production 1989-03

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics			Symbol	Rating	Unit
LED	Forward current		lF	25	mA
	Forward Current Derating (Ta ≥ 70 °C)		ΔIF/°C	-0.8	mA/°C
	Pulse forward current	(Note 1)	lFP	50	mA
	Peak transient forward current	(Note 2)	IFPT		Α
	Reverse voltage		V <sub>R</sub>	5	V
	Diode power dissipation	(Note 3)	PD	<b>4</b> 5	mW
	Output current		10	8	mA
	Peak output current		lop	16	mA
ctor	Supply voltage		Vcc	-0.5 to 15	V
Detector	Output voltage	×	VQ	−0.5 to 15	A
	Output power dissipation		$P_0$	100	mW
	Output Power Dissipation Derating (Ta ≥ 70°C)	(7)	ΔPo/°C	-2	mW/°C
Оре	Operating temperature range		Topr	-55 to 100	Ç
Storage temperature range		T <sub>stg</sub>	-55 to 125	∫,°C	
Lea	Lead soldering temperature(10 s)			260	°C
Isola (AC	Isolation voltage (AC, 60 s., R.H ≤ 60 %) (Note 4)		BVs	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: 50 % duty cycle,1ms pulse width. Derate 1.6mA / °C above 70 °C.
- Note 2: Pulse width  $\leq 1 \mu s$ , 300 pps.
- Note 3: Derate 0.9 mW / °C above 70 °C.
- Note 4: This device is regarded as a two terminal device: pins 1 and 3 are shorted together, as are pins 4, 5 and 6.

## **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
LED	Forward voltage	VF	I <sub>F</sub> = 16 mA	_	1.65	1.85	V	
	Forward voltage temperature coefficient	ΔV <sub>F</sub> / ΔTa	IF = 16 mA	_	-2	_	mV / °C	
	Reverse current	IR	V <sub>R</sub> = 5 V	/-	_	10	μΑ	
	Capacitance between terminals	Ст	V <sub>F</sub> = 0 V, f = 1 MHz		45	_	pF	
Detector	High level output current	IOH (1)	IF = 0 mA, VCC = VO = 5.5 V	)    -  -	3	500	nA	
		IOH (2)	IF = 0 mA, VCC = VO = 15 V	) $+$	_	5		
		Іон	IF = 0 mA, VCC = VO = 15 V Ta = 70 °C	_	_	50	μΑ	
	High level supply current	Іссн	IF = 0 mA, VCC = 15 V	_	0.01	1	μА	
	Current transfer ratio	Io/IF	IF =16 mA, V <sub>CC</sub> = 4.5 V V <sub>O</sub> = 0.4 V	10		<u> </u>	%	
Coupled	Low level output voltage	Vol	IF = 16 mA, VOC = 4,5 V IO = 1.1mA	7		0.4	V	
	Isolation resistance	Rs	R.H. ≤ 60 % Vs = 500 V (Note 1)	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω	
	Stray capacitance between input to output	Cs	Vs = 0 V, f = 1 MHz (Note 1)	2)	0.8	_	pF	

Note 1: Device considered a two-terminal device: Pins 1 and 3 shorted together and Pin 4, 5 and 6 shorted together.

# Switching Characteristics (Ta = 25°C, Vcc=5V)

Characteristics	Symbol	Test Cir- cuit	Test Condition	Min	Тур.	Max	Unit
Propagation delay time (H→L)	tpHL	1	I <sub>F</sub> = 0→16mA VCC = 5V, R <sub>L</sub> = 4.1kΩ	_	-	0.8	μS
Propagation delay time (L→H)	7 t <sub>pLH</sub>	1	$I_F = 16 \rightarrow 0 \text{mA}$ VCC = 5V, R <sub>L</sub> = 4.1kΩ	_	1	2.0	μS
Common mode transient immunity at high output level (Note2)	CMH	2	$I_{P} = 0$ mA, $V_{CM} = 200V_{p-p}$ RL = 4.1k $\Omega$	1	1500	1	V / μs
Common mode transient immunity at low output level (Note2)	CML	2	$I_F = 16\text{mA}$ , $V_{CM} = 200V_{p-p}$ $R_L = 4.1\text{k}\Omega$	_	-1500	_	V / μs

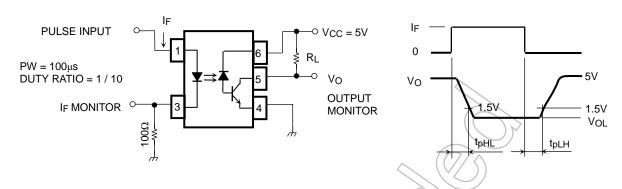
Note 2: CML is the maximum falling common mode voltage waveform (voltage/time)

that can keep low level (VO <0.8 V).

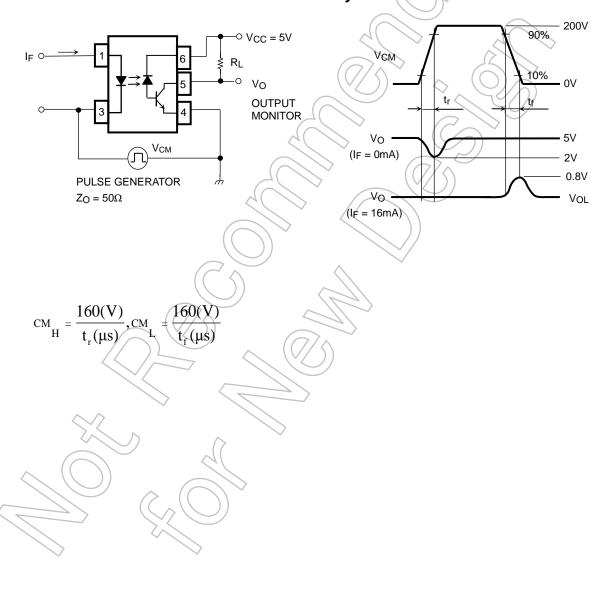
CMH is the maximum rising common mode voltage waveform (voltage/time)

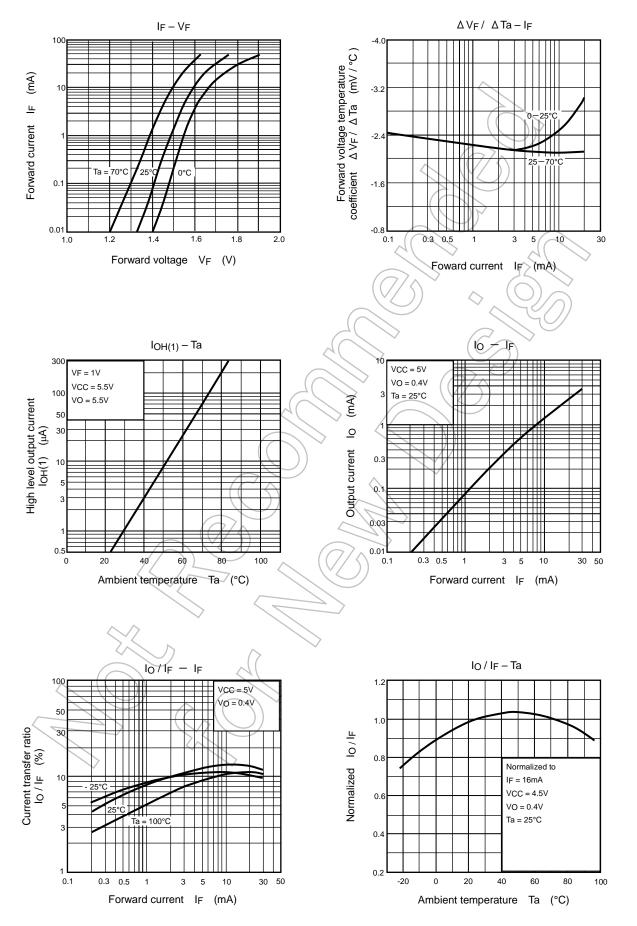
that can keep high level (VO> 2.0 V)

## **Test Circuit 1: Switching Time Test Circuit**



# Test Circuit 2: Common Mode Transient Immunity Test Circuit

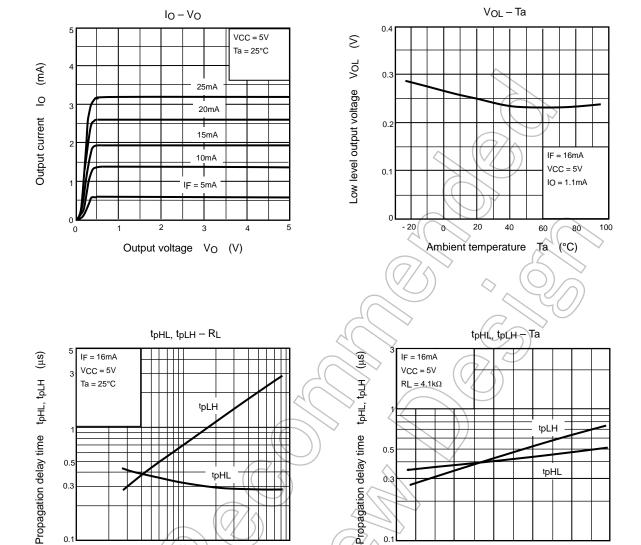




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

5

100



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

100

50

Load resistance  $R_L$  (k $\Omega$ )

0

20

Ambient temperature Ta (°C)

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