TOSHIBA PHOTOCOUPLER IRED & PHOTO-TRIAC

TLP666JF(S)

Office Equipment Household Appliances **Triac Drivers** Solid State Relays

The TOSHIBA TLP666JF(S) consists of an infrared emitting diode optically coupled to a triac-output photocoupler featuring a zero-cross voltage and is housed in a 6-pin DIP package.

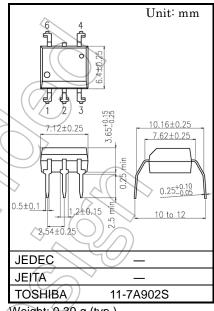
- Peak Off-State Voltage: 600 V (min)
- Trigger LED Current: 10 mA (max)
- On-State Current: 100 mA (max)
- Isolation Voltage: 5000 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved: EN 60747-5-5, EN 62368-1 (Note 1)

Note 1: When a VDE approved type is needed,

please designate the Option(D4).

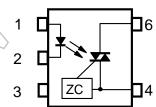
Construction mechanical rating

	10.16 mm pitch
	TLPxxxF type
Creepage Distance Clearance	8.0 mm (min) 8.0 mm (min)
Insulation Thickness	0.5 mm (min)



Weight: 0.39 g (typ.)

Pin configuration (top view)



- 1: Anode
- 2: Cathode
- 3: N.C.
- 4:Triac Terminal
- 6:Triac Terminal

ZC:Zero-cross circuit

Start of commercial production 1986-04

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
	Forward current	l _F	50	mA	
	Forward current derating (Ta ≥ 53°	ΔIF/°C	-0.7	mA / °C	
	Peak forward current (100 µs pulse, 100 pps)	IFP	1	Ą	
LED	Reverse voltage		VR	5	V >
	Power dissipation		PD	100	mW((
	Power dissipation derating (Ta ≥ 5	3°C)	ΔP _D /°C	-1.4	mW / °C
	Junction temperature	Tj	125	(°C/<	
	Off-state output terminal voltage	V _{DRM}	600		
	On-state RMS current	Ta = 25°C	IT(RMS)	100	mA
	on state range same in	Ta = 70°C	11(11(10)	50	\bigcup) $^{\circ}$
	On-state current derating (Ta ≥ 25°C)		ΔI _T / °C –1.1		mA / °C
Detector	Peak on–state current (100 μs pulse, 120 pps)		ITP	2	A
ă	Peak nonrepetitive surge current (P _w = 10 ms)		I _{TSM}	(1.2)	Ą
	Output Power dissipation	Po (300	mW	
	Output Power dissipation derating	ΔPo/°C	-3.0	mW/°C	
	Junction temperature	⟨r̄j (115	°C	
Storage	e temperature range	T _{stg}	-55 to 125	°C ′	
Operating temperature range			Topr	-40 to 100 (//°¢
Lead soldering temperature (10 s)			T _{sol}	260	Ŷ
Total package power dissipation			PT 330		mW
Total package power dissipation derating (Ta ≥ 25°C)			ΔPT/°C	4.4	mW / °C
Isolatio (AC, 60	on voltage 0 s, R.H. ≤ 60 %)	BVs	5000	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two terminal device: Pins 1, 2 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _A C	_	_	240	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	ITP	_	_	1	Α
Operating temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



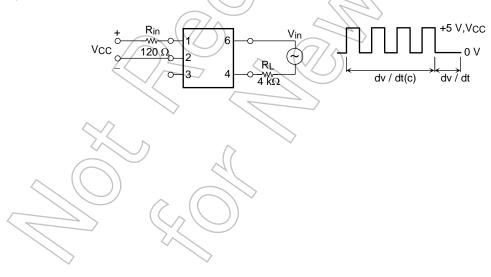
Individual Electrical Characteristics (Ta = 25°C)

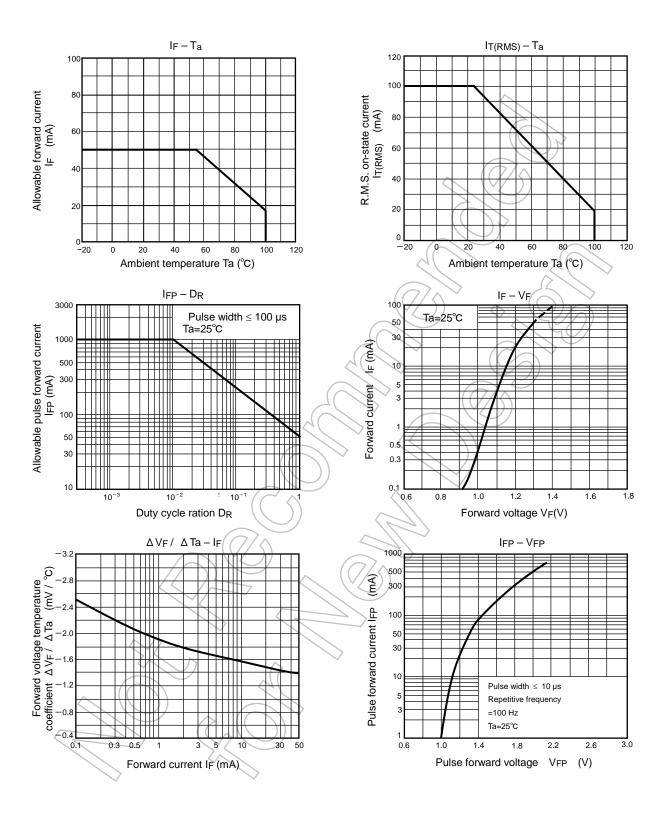
	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μА
	Capacitance	Ст	VF = 0 V, f = 1 MHz	/	30	_	pF
	Peak off-state current	IDRM	V _{DRM} = 600 V		10	1000	nA
	Peak on-state voltage	Vтм	I _{TM} = 100 mA		1.7	3.0	V
tor	Holding current	lн	(2))(0.6	_	mA
Detector	Critical rate of rise of off–state voltage	dv / dt	V _{in} = 240 Vrms, Ta = 85 °C (Fig.1)	200	500	_	V / μs
	Critical rate of rise of commutating voltage	dv / dt (c)	Vin = 60 Vrms, IT = 15 mA (Fig.1)	_	0.2	_	V / μs

Coupled Electrical Characteristics (Ta = 25°C)

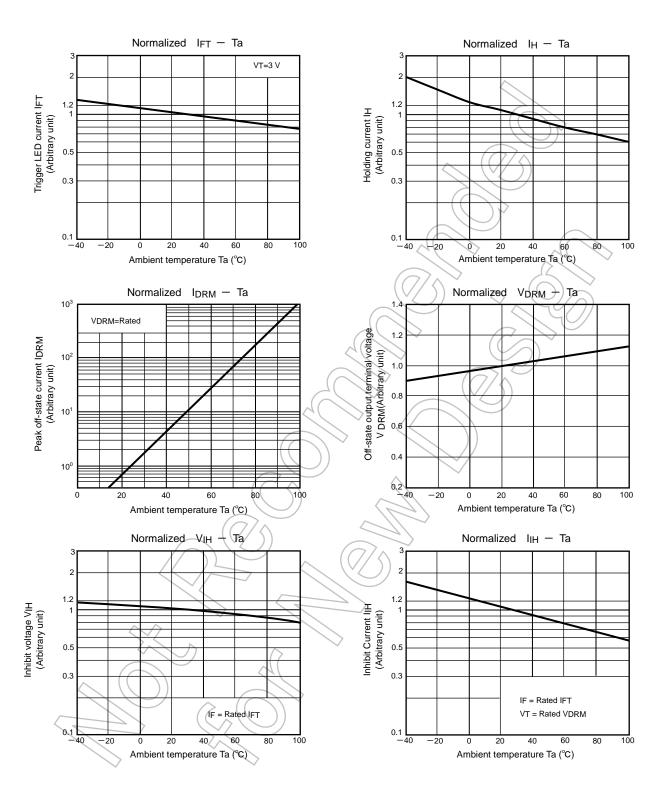
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	lfT	VT = 6 V	3 -//	5	10	mA
Inhibit voltage	VIH	IF = rated IFT	$\overline{\mathcal{A}}$	_	50	V
Leakage in inhibited state	lih (IF = rated IFT VT = rated VDRM		100	300	μА
Capacitance input to output	Cs	Vs = 0 V, f = 1 MHz	リー	0.8	_	pF
Isolation resistance	Rs	Vs = 500 V , R.H. ≤ 60 %	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 60 s	5000	_	_	Vrms

Fig. 1 dv / dt test circuit





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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