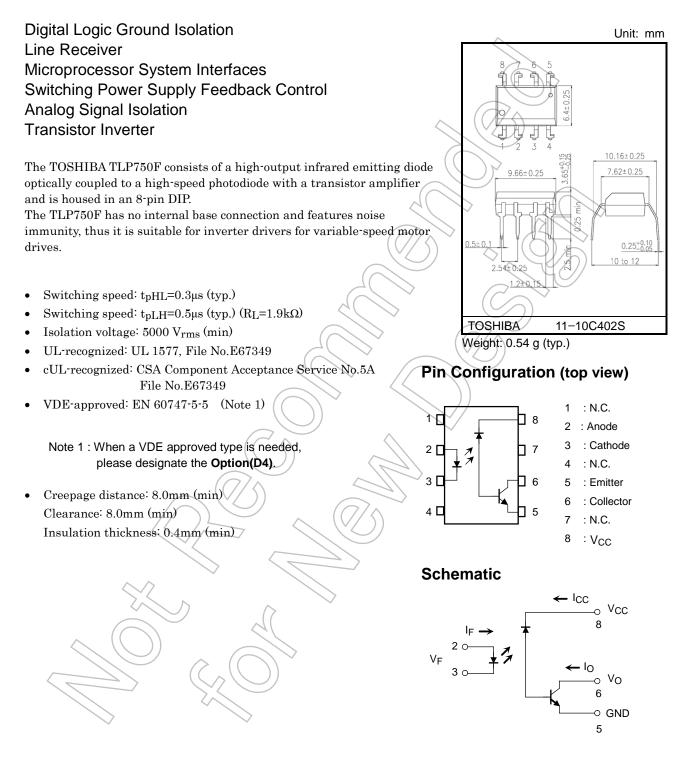
TOSHIBA Photocoupler IRED + Photo IC

TLP750F



Start of commercial production 1989-10

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Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit
LED	Forward current	(Note 1)	lF	25	mA
	Pulse forward current	(Note 2)	IFP	50	mA
	Peak transient forward current	(Note 3)	IFPT	1	А
	Reverse voltage		VR	5	V
	Diode power dissipation	(Note 4)	PD	45	mW
Detector	Output current		lo	(8)	mA
	Peak output current		IOP	16	mA
	Output voltage		Vo	-0.5 to 15	V
	Supply voltage		Vcc	-0.5 to 15	V
	Output power dissipation	(Note 5)	Po	100	mW
Operating temperature range			Topr	-55 to 100	°C
Stor	age temperature range		Tstg	-55 to 125	ç
Lea	d solder temperature(10 s)	(Note 6)	Isol	260	S.C.
Isolation voltage (AC, 60 s, R.H.=60 %)		(Note 7)	BVs	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- (Note 1) Derate 0.8 mA / °C above 70 °C.
- (Note 2) 50 % duty cycle, 1 ms pulse width. Derate 1.6 mA / °C above 70 °C.
- (Note 3) Pulse width \leq 1 µs, 300 pps.
- (Note 4) Derate 0.9 mW / °C above 70 °C.
- (Note 5) Derate 2 mW / °C above 70 °C.
- (Note 6) Soldering portion of lead: Up to 2 mm from the body of the device.
- (Note 7) Device considered a two terminal device: Pins 1, 2, 3 and 4 shorted together and pins 5, 6, 7 and 8 shorted together.

Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	VF	I _F = 16 mA	_	1.65	1.85	V
	Forward voltage temperature coefficient	$\Delta V_{F} / \Delta Ta$	IF = 1 6mA	_	-2	_	mV / °C
	Reverse current	I _R	V _R = 5 V	\searrow	—	10	μA
	Capacitance between terminal	CT	V _F = 0 V, f = 1 MHz	(-)	45	_	pF
Detector		IOH(1)	I _F = 0 mA, V _{CC} = V _O = 5.5 V	\mathcal{D}	3	500	nA
	High level output current	I _{OH(2)}	I _F = 0 mA, V _{CC} = V _O = 15 V	$\langle \uparrow \rangle$	_	5	μA
	IOF		$I_F = 0 \text{ mA}, V_C C = V_O = 15 \text{ V},$ Ta = 70 °C	<u>ل</u>	_	50	μΑ
	High level supply voltage	Іссн	IF = 0 mA, V _{CC} = 15 V	_	0.01	1	μA
Coupled/ Insulation	Current transfer ratio	lo/IF	$Ta = 25 °C$ $IF = 16 mA$ $V_{CC} = 4.5 V$ $V_{O} = 0.4 V$ $Ta = 0 to 70 °C$ Rank: O Rank: O	10 19 5 15	30 30 -		%
	Low level output voltage	V _{OL}	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V},$ $I_O = 1.1 \text{ mA} \text{ (rank O: } I_O = 2.4 \text{ mA} \text{)}$		50	0.4	V
	Isolation resistance	R _S	R.H.≤ 60 %, V _S = 500 V _{DC} (Note 7)	1×10 ¹²	10 ¹⁴	_	Ω
	Capacitance between input to output	Cs	Vs= 0 V, f = 1 MHz (Note 7)		0.8	_	pF
	Isolation Voltage	BVs	AC, 60 s (Note 7)	5000	—	_	Vrms

Switching Characteristics (Ta = 25°C, Vcc = 5V)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Dropogration dolou time (II)	tрнL	1	$I_F = 16 \text{ mA}, R_L = 4.1 \text{ k}\Omega$	_	0.2	0.8	μS
Propagation delay time ($H \rightarrow L$)			Rank O: $R_L = 1.9 k\Omega$	_	0.3	0.8	
Propagation delay time $(L \rightarrow H)$	7 t _{pLH}		$I_F = 16 \text{ mA}, R_L = 4.1 \text{ k}\Omega$	_	1	2	
			Rank O: $R_L = 1.9 \text{ k}\Omega$	—	0.5	1.2	μS
Common mode transient immunity at logic high output (Note 8)	CMH	0	IF = 0 mA, V _{CM} = 200 V _P -p R _L = 4.1 kΩ (Rank O: R _L = 1.9 kΩ)	_	1500	_	V / μs
Common mode transient immunity at logic low output (Note 8)	CML	2	I _F = 16 mA, V _{CM} = 200 V _{p-p} R _L = 4.1 kΩ (Rank O: R _L = 1.9 kΩ)	_	-1500	_	V / μs

(Note 8) CML is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic low state (VO < 0.8 V).

CMH is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic high state (VO > 2.0 V).

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5V

1.5V

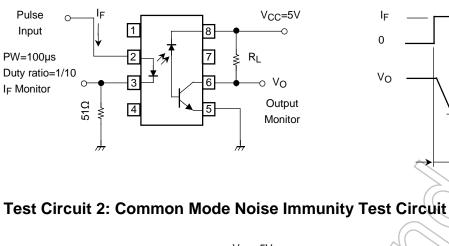
VOL

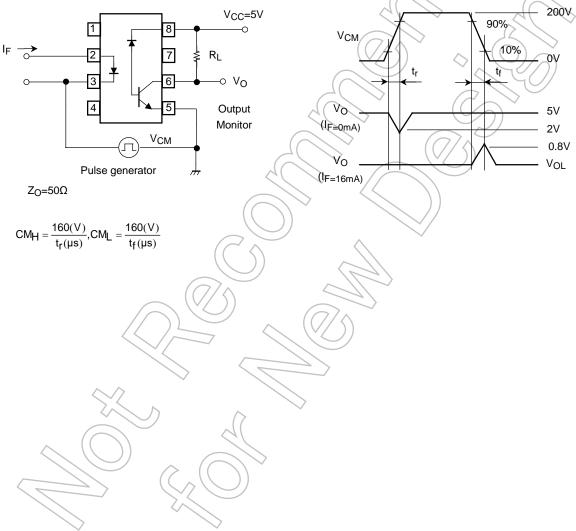
t_pLH

1.5V

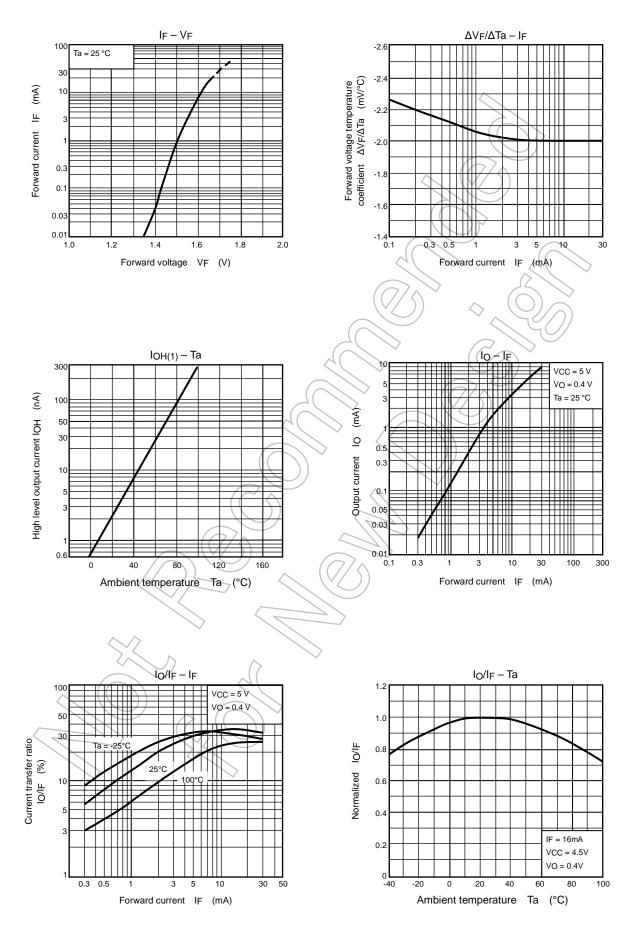
tpHL

Test Circuit 1: Switching Time Test Circuit



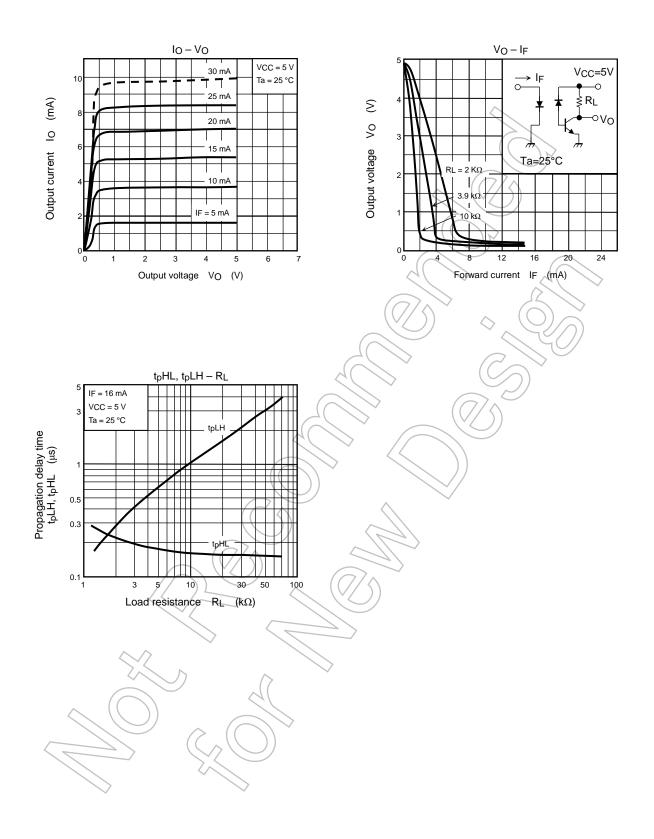


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