

Vishay Semiconductors

Description

The TELUX[™] series is a clear, non diffused LED for applications where supreme luminous flux is required. It is designed in an industry standard 7.62 mm square package utilizing highly developed (AS) AllnGaP technology.

The supreme heat dissipation of TELUX™ allows applications at high ambient temperatures.

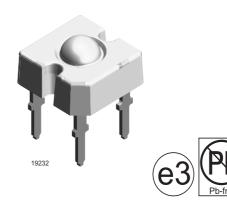
All packing units are binned for luminous flux, forward voltage and color to achieve the most homogenous light appearance in application.

SAE and ECE color requirements for automobile application are available for color red.

ESD resistivity 2 kV (HBM) according to MIL STD 883D, method 3015.7.

Features

- Utilizing one of the world's brightest (AS) AllnGaP technologies
- · High luminous flux
- Supreme heat dissipation: R_{thJP} is 90 K/W
- High operating temperature:
 T_{amb} = 40 to + 110 °C
- Meets SAE and ECE color requirements for the automobile industry for color red
- · Packed in tubes for automatic insertion
- Luminous flux, forward voltage and color categorized for each tube
- Small mechanical tolerances allow precise usage of external reflectors or lightguides
- · Lead-free device



Applications

Exterior lighting
Dashboard illumination
Tail-, Stop - and Turn Signals of motor vehicles
Replaces small incandescent lamps
Traffic signals and signs

Parts Table

| Part | Color, Luminous Intensity | Angle of Half Intensity (±φ) | Technology |
|----------|--|------------------------------|-----------------|
| TLWR9900 | Red, $\phi_V > 2500 \text{ mlm}$ | 45 ° | AllnGaP on GaAs |
| TLWR9901 | Red, φ _V > (2500 to 6100) mlm | 45 ° | AllnGaP on GaAs |

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Absolute Maximum Ratings

 T_{amb} = 25 °C, unless otherwise specified **TLWR990**.

| Parameter | Test condition | Symbol | Value | Unit |
|--------------------------------------|---|-------------------|---------------|------|
| Reverse voltage | I _R = 100 μA | V _R | 10 | V |
| DC Forward current | T _{amb} ≤ 85 °C | I _F | 70 | mA |
| Surge forward current | t _p ≤ 10 μs | I _{FSM} | 0.1 | А |
| Power dissipation | T _{amb} ≤ 85 °C | P _V | 187 | mW |
| Junction temperature | | Tj | 125 | °C |
| Operating temperature range | | T _{amb} | - 40 to + 110 | °C |
| Storage temperature range | | T _{stg} | - 55 to + 110 | °C |
| Soldering temperature | t ≤ 5 s, 1.5 mm from body preheat temperature 100 °C/ 30 sec. | T _{sd} | 260 | °C |
| Thermal resistance junction/ ambient | with cathode heatsink of 70 mm ² | R _{thJA} | 200 | K/W |

Optical and Electrical Characteristics

 $T_{amb} = 25$ °C, unless otherwise specified

Red

TLWR9900

| Parameter | Test condition | Symbol | Min | Тур. | Max | Unit |
|--|--|------------------|------|-------|-----|---------|
| Total flux | I _F = 70 mA, R _{thJA} = 200 °K/W | φγ | 2500 | 3200 | | mlm |
| Luminous intensity/Total flux | $I_F = 70$ mA, $R_{thJA} = 200$ °K/W | I_V/ϕ_V | | 0.7 | | mcd/mlm |
| Dominant wavelength | I _F = 70 mA, R _{thJA} = 200 °K/W | λ_{d} | 611 | 615 | 634 | nm |
| Peak wavelength | I _F = 70 mA, R _{thJA} = 200 °K/W | λ_{p} | | 624 | | nm |
| Angle of half intensity | I _F = 70 mA, R _{thJA} = 200 °K/W | φ | | ± 45 | | deg |
| Total included angle | 90 % of Total Flux Captured | Φ0.9V | | 100 | | deg |
| Forward voltage | I _F = 70 mA, R _{thJA} = 200 °K/W | V _F | 1.83 | 2.2 | 2.7 | V |
| Reverse voltage | I _R = 100 μA | V_{R} | 10 | 20 | | V |
| Temperature coefficient $< \lambda_d$ | I _F = 70 mA | $TC_{\lambda d}$ | | 17 | | nm/K |
| Temperature coefficient V _F | I _F = 70 mA, T > - 25 °C | TC _{VF} | | - 2.9 | | mV/K |

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Typical Characteristics ($T_{amb} = 25$ °C unless otherwise specified)

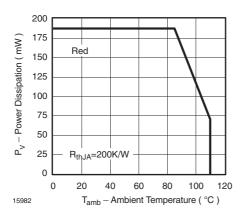


Figure 1. Power Dissipation vs. Ambient Temperature

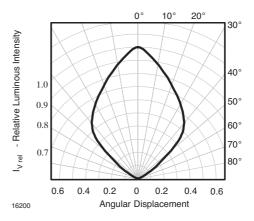


Figure 4. Rel. Luminous Intensity vs. Angular Displacement

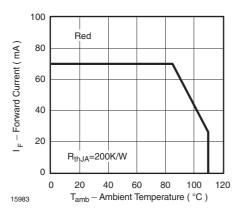


Figure 2. Forward Current vs. Ambient Temperature

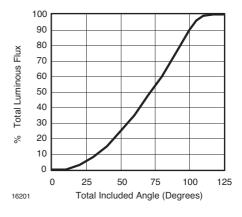


Figure 5. Percentage Total Luminous Flux vs. Total Included Angle for 90 $^{\circ}$ emission angle

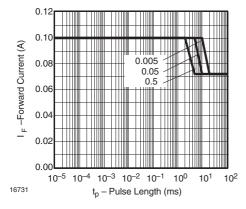


Figure 3. Forward Current vs. Pulse Length

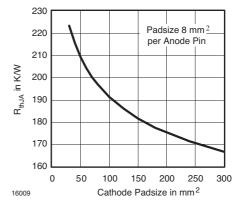
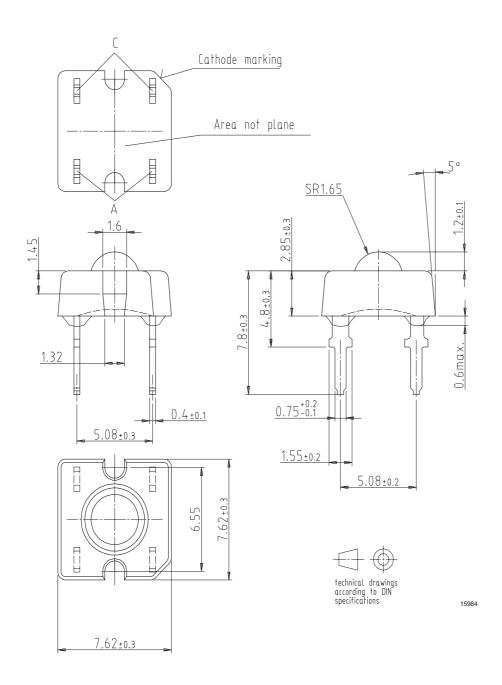


Figure 6. Thermal Resistance Junction Ambient vs. Cathode Padsize

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Package Dimensions in mm



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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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