

**MODEL NO : TM104SBH01****MODEL VERSION: 00****SPEC VERSION : 2.3****ISSUED DATE: 2015-12-30**

- Preliminary Specification
- Final Product Specification

**Customer :** \_\_\_\_\_

Approved by	Notes

**TIANMA Confirmed :**

Prepared by	Checked by	Approved by
Junwen Du	Longping Deng	Feng Qin

This technical specification is subjected to change without notice

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## 1 General Specifications

	<b>Feature</b>	<b>Spec</b>
<b>Display Spec.</b>	Size	10.4 inch
	Resolution	800(RGB) x 600
	Technology Type	a-Si
	Pixel Configuration	R.G.B. Vertical Stripe
	Pixel pitch(mm)	0.264x0.264
	Display Mode	TM with Normally White
	Surface Treatment	Anti-Glare(3H)
	Viewing Direction	12 o'clock
	Gray Scale Inversion Direction	6 o'clock
<b>Mechanical Characteristics</b>	LCM (W x H x D) (mm)	236.00x176.90x7.3
	Active Area(mm)	212.2*159.4
	With /Without TSP	With TSP
	Matching Connection Type	CN1:HIROSE DF19K-20P-1H CN2:JST BHSR-02VS-1 CN3:SFW4R-1STAE1-LF
	LED Numbers	36 LED
	Weight (g)	412g
<b>Electrical Characteristics</b>	Interface	LVDS 6-bit
	Color Depth	262K
	Driver IC	NT39411+NT39211

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance:  $\pm 5\%$

## 2 Input/Output Terminals

### 2.1 TFT LCD Panel

Matching connector of Hirose DF19K-20P-1H (56)

Pin No.	Symbol	I/O	Function	Remark
1	VDD	P	Power Supply	
2	VDD	P	Power Supply	
3	GND	P	Ground	
4	GND	I	Ground	Note3
5	IN0-	I	LVDS receiver signal channel 0. LVDS Differential Data Input (R0, R1, R2, R3, R4, R5, G0)	Note2
6	IN0+	I		
7	GND	P	Ground	
8	IN1-	I	LVDS receiver signal channel 1. LVDS Differential Data Input (G1, G2, G3, G4, G5, B0, B1)	Note2
9	IN1+	I		
10	GND	P	Ground	
11	IN2-	I	LVDS receiver signal channel 2 LVDS Differential Data Input (B2, B3, B4, B5, DE)	Note2
12	IN2+	I		
13	GND	P	Ground	
14	CLKIN-	I	LVDS receiver signal clock	Note2
15	CLKIN+	I		
16	GND	P	Ground	
17	NC	-	No connection	Note2
18	NC	-		
19	GND	P	Ground	
20	GND	P	Ground	Note2

**Table 2.1 input terminal pin assignment**

Note1: Please add the FPC connector type and matched one if necessary .

### 2.2 CN2 (BackLight Connector)

Matching connector :JST BHSR-02VS-1

No	Symbol	I/O	Description	Wire Color
1	LEDA	P	LED driving anode (high voltage)	Red
2	LEDK	P	LED driving cathode (low voltage)	White

**2.3 CN3 (TP FPC)**

Matching connector of SFW4R-1STAE1-LF

No	Symbol	I/O	Description	Remark
1	Y2	I	ITO film	
2	X2	I	ITO glass	
3	Y1	I	ITO film	
4	X1	I	ITO glass	

CN3(TSP FPC) Match Connector: SFW4R-1STAE1-LF(for reference);

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### 3 Absolute Maximum Ratings

GND=0V

Item	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VCC	-0.5	5.0	V	Note1
Input voltage	V <sub>IN</sub>	-0.5	5.0	V	
Operating Temperature	Top	-20	70	°C	
Storage Temperature	Tst	-30	80	°C	
Relative Humidity Note2	RH	--	≤95	%	Ta ≤ 40°C
		--	≤85	%	40°C < Ta ≤ 50°C
		--	≤55	%	50°C < Ta ≤ 60°C
		--	≤36	%	60°C < Ta ≤ 70°C
		--	≤24	%	70°C < Ta ≤ 80°C
Absolute Humidity	AH	--	≤70	g/m <sup>3</sup>	Ta > 70°C

**Table 3 Absolute Maximum Ratings**

Note1: Input voltage include RxIN0±, RxIN1±, RxIN2±, RxCLKI±.

Note2: Ta means the ambient temperature.

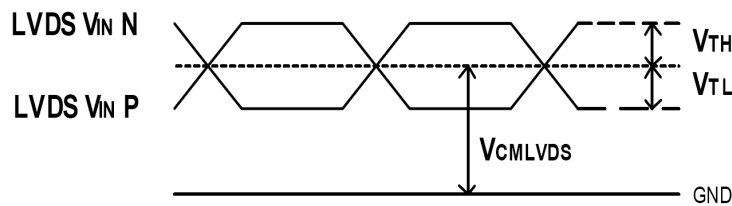
It is necessary to limit the relative humidity to the specified temperature range.  
Condensation on the module is not allowed.

## 4 Electrical Characteristics

### 4.1 Driving TFT LCD Panel

GND=0V, Ta=25°C

Item	Symbol	Min	Typ	Max	Unit	Remark
LVDS Differential input high threshold	$V_{TH}$	-	-	+100	mV	$V_{CMLVDS}=1.2V$
LVDS Differential input low threshold	$V_{TL}$	-100	-	-	mV	$V_{CMLVDS}=1.2V$
Differential input voltage	$ V_{ID} $	0.1	-	0.6	V	
LVDS input common mode voltage	$V_{CMLVDS}$	$ V_{ID} /2$	-	$1.4-( V_{ID} /2)$	V	
Input current	$I_{IN}$	-10		10	μA	
Supply Voltage	VDD	3.0	3.3	3.6	V	
Common Electrode Driving Signal	VCOM	-0.02	4.30	-	V	Note1
Sync Frequency	FVD	-	60	70	Hz	
VDD Power Consumption	$I_{VDD}$	-	260	380	mA	Note2


**Figure 4.1 LVDS DC timing diagram**

DC Timing Diagram 1: LVDS waveform



**4.2 Driving Backlight**

Ta=25°C

Item	Symbol	Min	Typ	Max	Unit	Remark
Forward Current	$I_F$	-	120.0	180	mA	Note 1
Forward Current Voltage	$V_F$	18	19.2	21.6	V	Note 1
Backlight Power Consumption	$W_{BL}$	-	2.304	3.888	W	Note 1
LED Life Time	-	10000	(20000)	-	hrs	Note 2

Note 1: The figure below shows the connection of backlight LED.

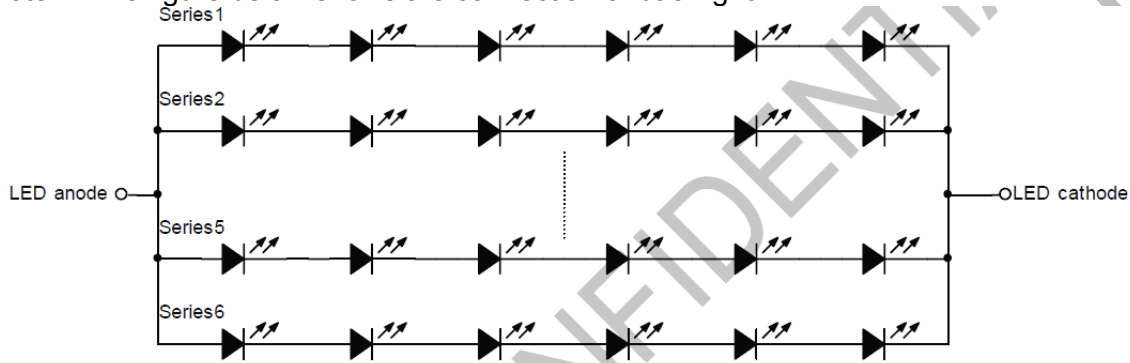


Figure 4.2 LED connection of backlight

 Note 2:  $I_F$  is defined for one channel LED.

Optical performance should be evaluated at Ta=25°C only.

If LED is driven by high current, high ambient temperature &amp; humidity condition.

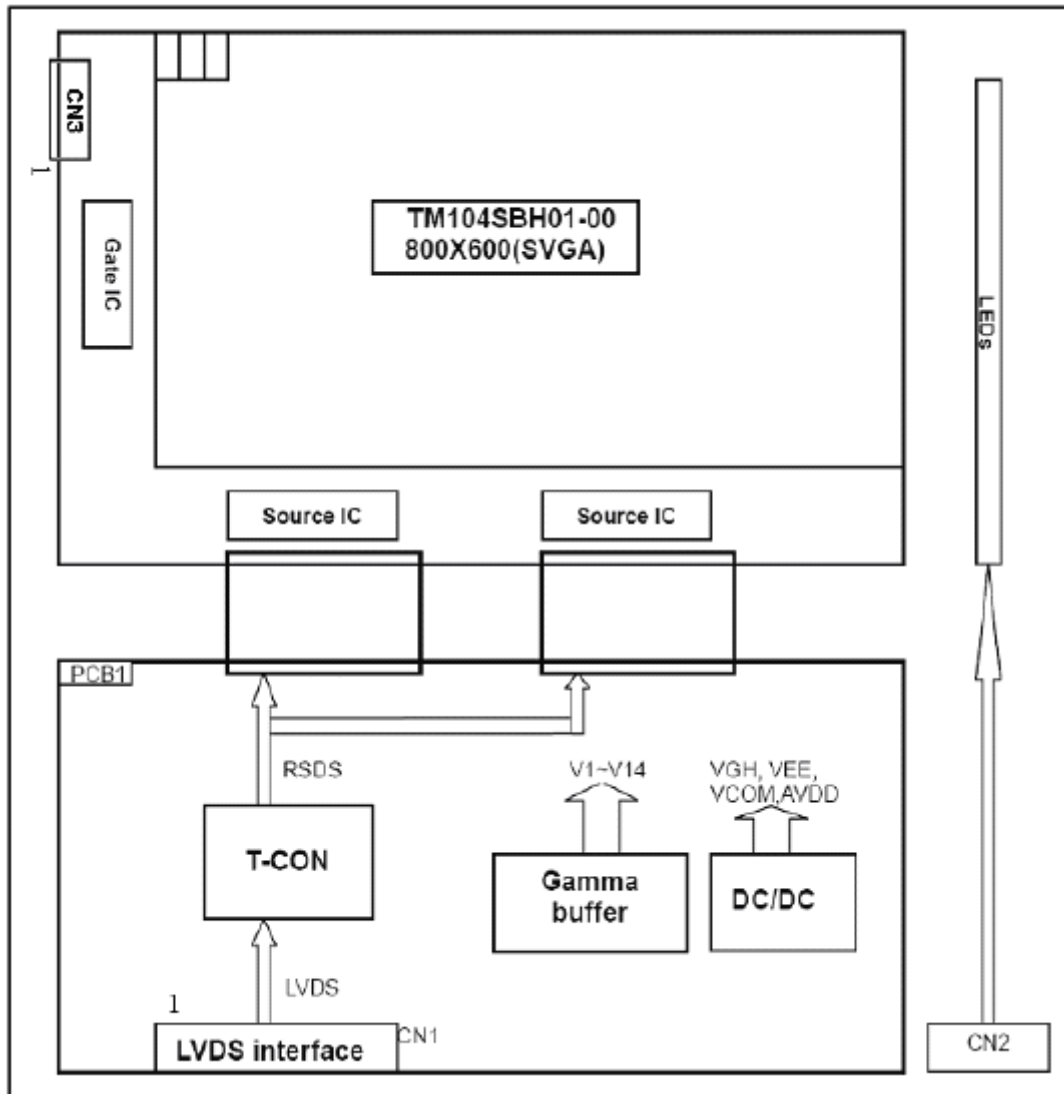
The life time of LED will be reduced.

Operating life means brightness goes down to 50% initial brightness.

Typical operating life time is estimated data.

 Note 3: One LED:  $I_F(1/6) = 120\text{mA}$ .

**4.3 Block Diagram**



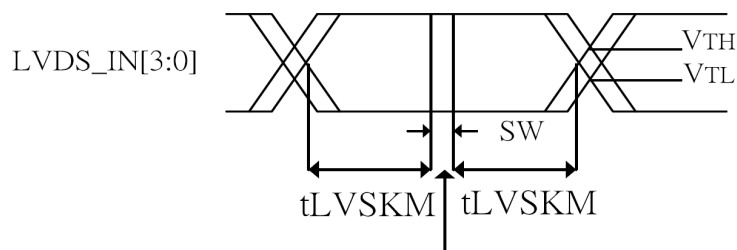
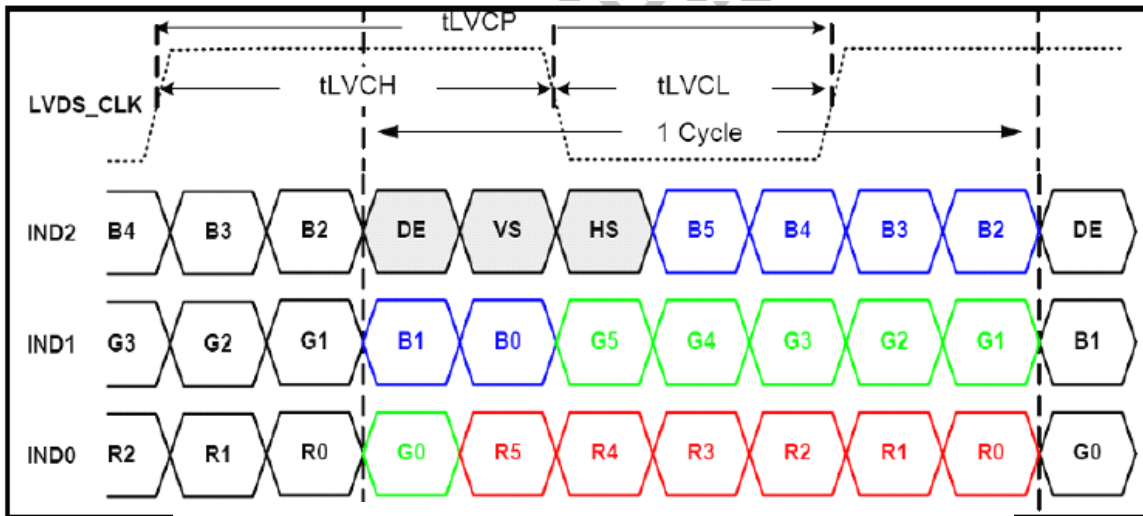
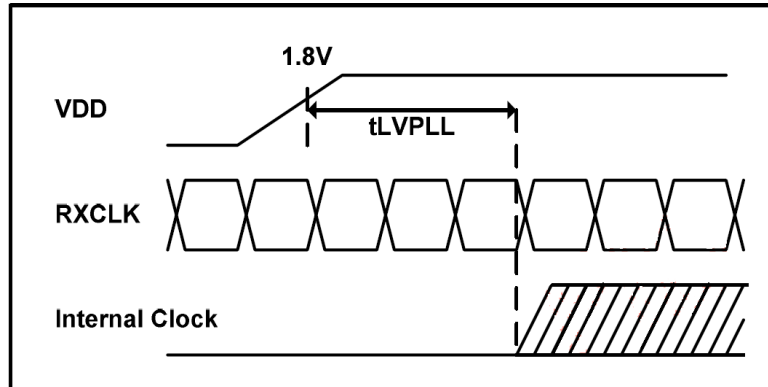
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## 5 Timing Chart

### 5.1 Timing Parameter

Item	Symbol	Min	Typ	Max	Unit	Condition
Clock period	tLVCP	20.0	25	31.25	ns	
Clock high time	tLVCH	-	14.29	-	ns	
Clock low time	tLVCL	-	10.71	-	ns	
PLL wake-up time	tLVPLL	-	-	1	ms	
Input skew margin	tLVSKM	400	-	-	ps	f=85MHz

**Table 5.1 timing parameter**



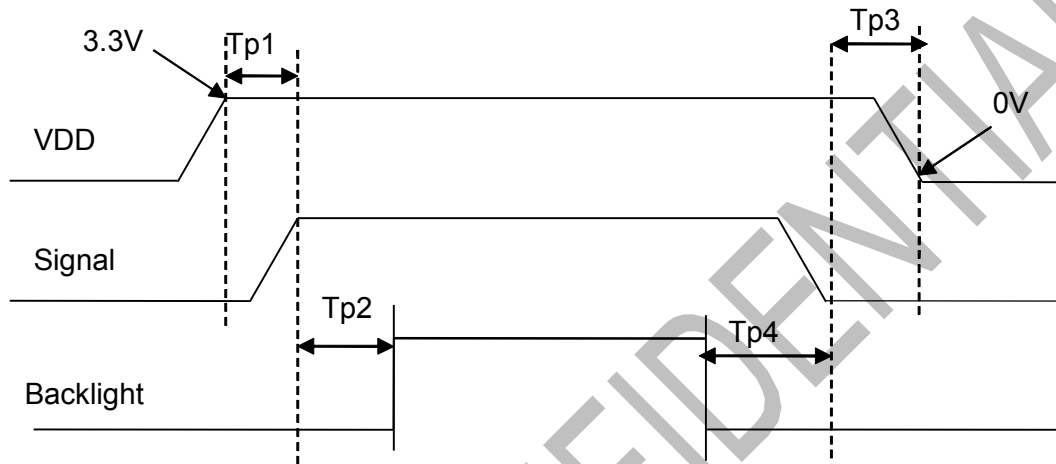
Ideal Strobe Position

SW: Setup and Hold time  
SW: Setup and Hold time

AC Timing Diagram 3: LVDS timing skew margin.

**5.2 Power On/Off Sequence**

Item	Symbol	Min	Typ	Max	Unit	Remark
VDD 3.0V to signal starting	Tp1	0	-	50	ms	
Signal starting to backlight on	Tp2	150	-	-	ms	
Signal off to VDD 3.0V	Tp3	0	-	50	ms	
Backlight off to signal off	Tp4	150	-	-	ms	


**Figure 5.2 Interface power on/off sequence**

## 6 Optical Characteristics

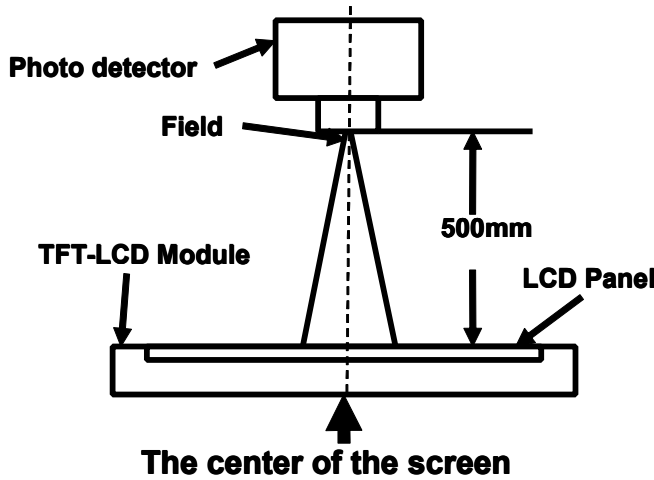
Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
<b>View Angles</b>	$\theta T$	$CR \geq 10$	50	60	-	Degree	Note2,3
	$\theta B$		60	70	-		
	$\theta L$		60	70	-		
	$\theta R$		60	70	-		
<b>Contrast Ratio</b>	CR	$\theta=0^\circ$	300	400	-		Note 3
<b>Response Time</b>	$T_{ON}$	25°C	-	10	15	ms	Note 4
	$T_{OFF}$		-	15	25		
<b>Chromaticity</b>	<b>White</b>	Backlight is on	x	0.271	0.321	0.371	Note 1,5
			y	0.293	0.343	0.393	
	<b>Red</b>		x	0.545	0.595	0.645	Note 1,5
			y	0.308	0.358	0.408	
	<b>Green</b>		x	0.286	0.336	0.386	Note 1,5
			y	0.510	0.560	0.610	
	<b>Blue</b>		x	0.098	0.148	0.198	Note 1,5
			y	0.075	0.125	0.175	
<b>Uniformity</b>	U		70	80	-	%	Note 6
<b>NTSC</b>			-	50	-	%	Note 5
<b>Luminance</b>	L		250	300	-	cd/m <sup>2</sup>	Note 7

Test Conditions:

1.  $I_F = 120$  mA, and the ambient temperature is 25°C.
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

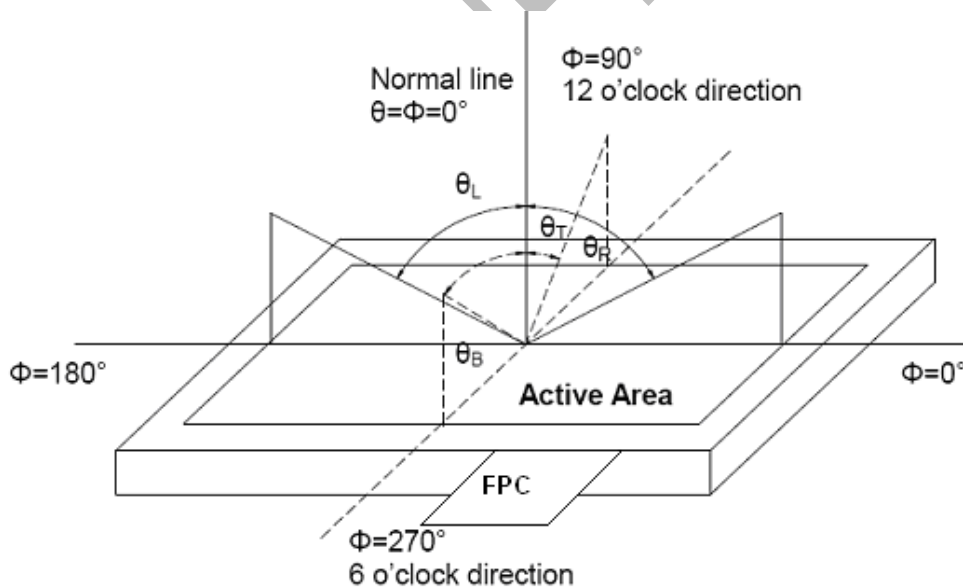
The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity	BM-7A	2°
Response Time		

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

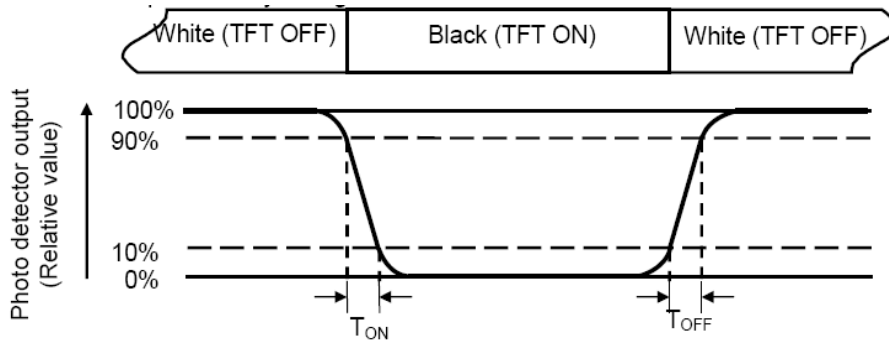
“White state “: The state is that the LCD should drive by Vwhite.

“Black state”: The state is that the LCD should drive by Vblack.

V<sub>white</sub>: To be determined    V<sub>black</sub>: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T<sub>ON</sub>) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T<sub>OFF</sub>) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

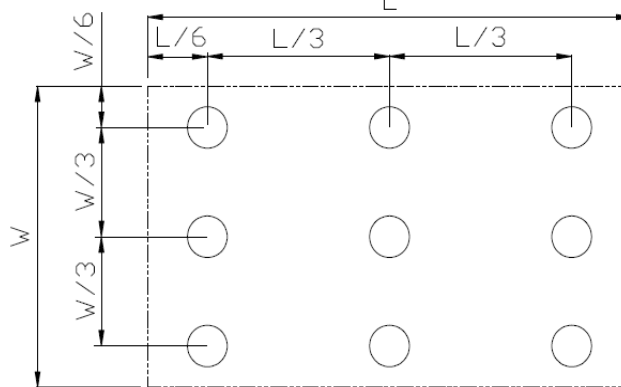
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min} / L_{\max}$$

L-----Active area length    W----- Active area width



L<sub>max</sub>: The measured Maximum luminance of all measurement position.

L<sub>min</sub>: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

## 7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts=+70℃, 240hrs	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Operation	Ta=-20℃, 240hrs	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	Ta=+80℃, 240hrs	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	Ta=-30℃, 240hrs	IEC60068-2-1:2007 GB2423.1-2008
5	Storage at High Temperature and Humidity	Ta = +60℃, 90% RH max,240 hours	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-30℃ 30 min~+80℃ 30 min, Change time:5min, 30 Cycles	Start with cold temperature, End with high temperature, IEC60068-2-14:1984,G B2423.22-2002
7	ESD	C=150pF,R=330Ω, Air:±8Kv, Contact:±4Kv, 10times/terminal	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration Test	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2hours for each direction of x.y.z (6 hours for total)	IEC60068-2-6:1982 GB/T2423.10—1995
9	Mechanical Shock (Non OP)	80G 6ms, ±X,±Y,±Z 3 times for each direction	IEC60068-2-27:1987 GB/T2423.5—1995
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32:1990 GB/T2423.8—1995

Note1: Ts is the temperature of panel's surface.

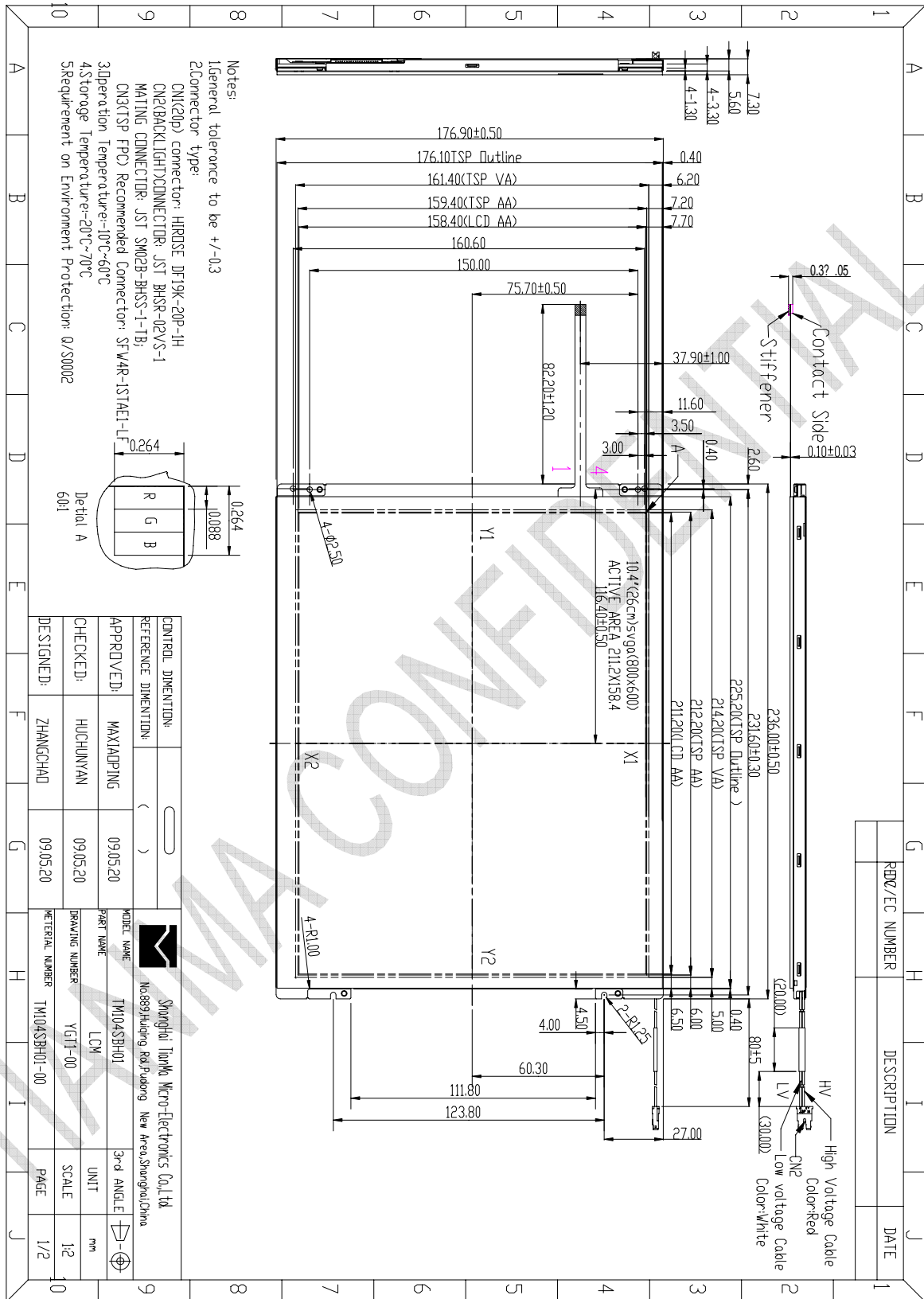
Note2: Ta is the ambient temperature of sample.

Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

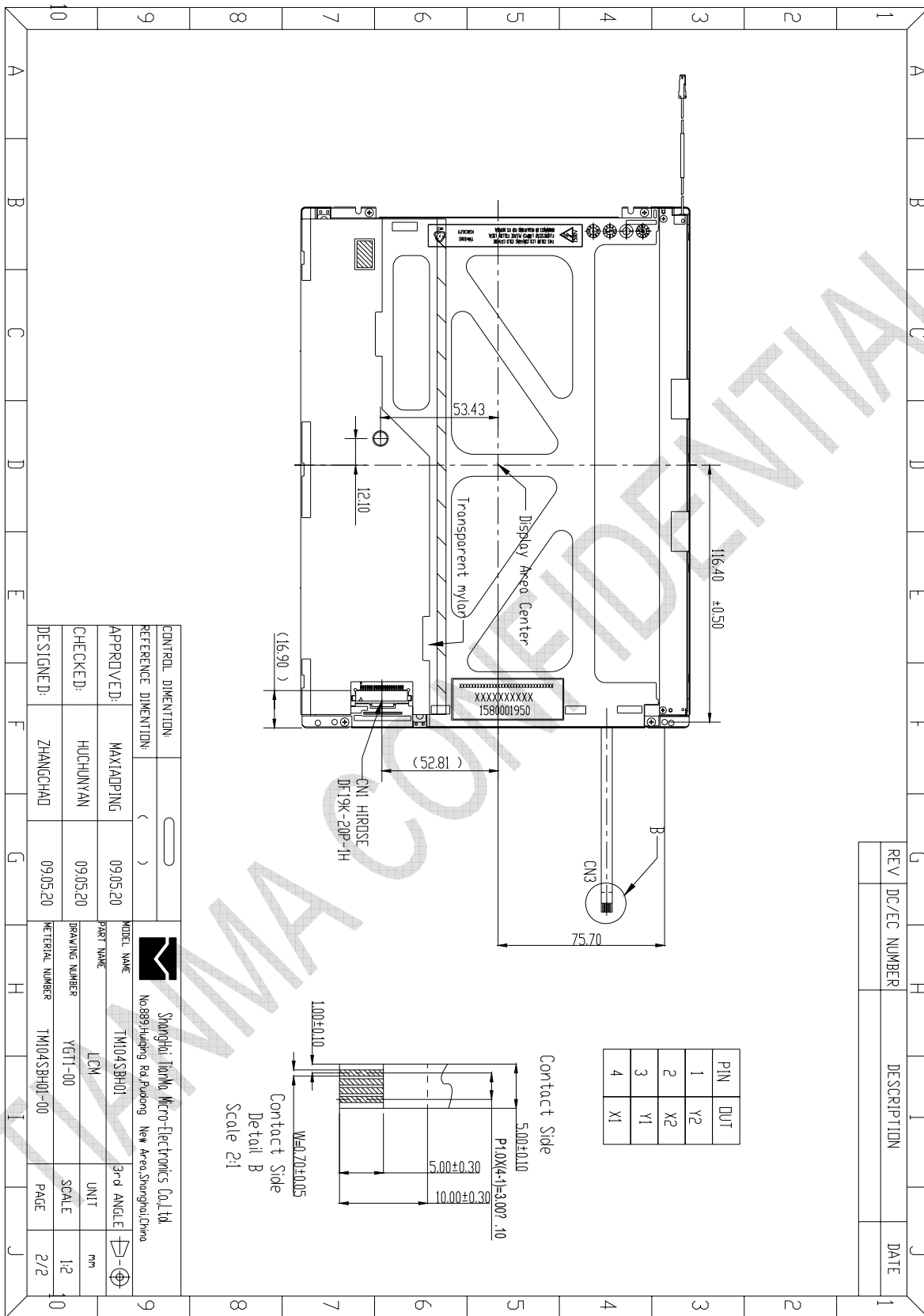
Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.



### 8 Mechanical Drawing



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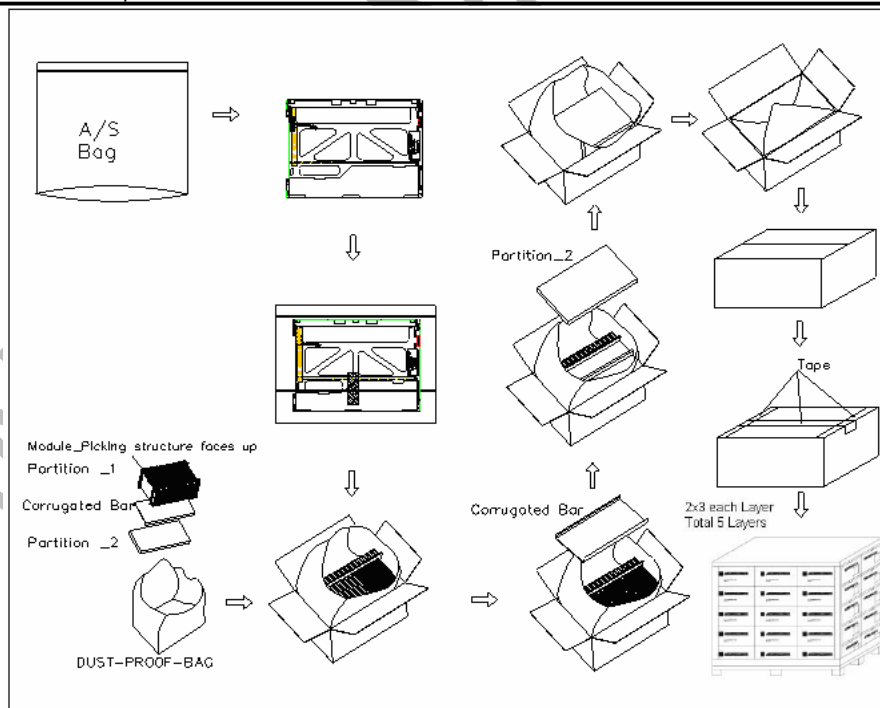


CONTROL DIMENTION:	( )	
REFERENCE DIMENTION:	( )	
APPROVED:	MAXIAPING	09.05.20
CHECKED:	HUCHUNYAN	09.05.20
DESIGNED:	ZHANGCHAD	09.05.20
MODEL NAME	TM104SBH01	3-rd ANGLE
PART NAME	LCM	UNIT
DRAWING NUMBER	YGT1-100	SCALE
SERIAL NUMBER	TM104SBH01-00	PAGE

Shanghai Tianma Micro-Electronics Co., Ltd  
 No.889Jiading Rd, Pudong New Area, Shanghai, China

**9 Packing Drawing**

No	Item	Model (Material)	Dimensions (mm)	Unit Weight(Kg)	Quantity	Remark	
1	LCM module	TM104SBH01	236x176.9x7.3	0.42	20		
2	Partition_1	Corrugated Paper	513x333x217	1.326	1		
3	Anti-static Bag	PE	247×256×0.05	0.004	20		
4	DUST-PROOF BAG	PE	700×530	0.06	1		
5	Partition_2	Corrugated Paper	505x332x4.0	0.1	2		
6	Corrugated Bar	Corrugated Paper	513x248	0.102	2		
7	Carton	Corrugated Paper	530x350x250	1.02	1		
8	Total weight(Kg)	11.28 ± 10%					



## **10 Precautions for Use of LCD Modules**

### **10.1 Handling Precautions**

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1 Be sure to ground the body when handling the LCD Modules.

10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

### **10.2 Storage precautions**

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0°C ~ 40°C Relatively humidity: ≤80%

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

### **10.3 Transportation Precautions**

10.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.