SPECIFICATION FOR LCD MODULE

Model No. **TM162AAA6-1**

Prepared by: Date: Checked by: Date: Verified by: Date: Approved by: Date:

TIANMA MICROELECTRONICS CO., LTD

REVISION RECORD

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

1. General Specifications:

1.1 Display type: TN

1.2 Display color*:

Display color: Blue-Black

Background: White

1.3 Polarizer mode: Reflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/16 Duty 1/5 Bias

1.6 Backlight: None

1.7 Display Fonts: 5 x 7 dots + Cursor (1 Character)

1.8 Controller: HCD66701A00

1.9 Data Transfer: 8 Bit Parallel

1.10 Operating Temperature: $0---+50^{\circ}$ C

Storage Temperature: -20----+60 °C

1.11 Outline Dimensions: Refer to outline drawing on next page

1.12 Dot Matrix: 16 Characters X 2 Lines

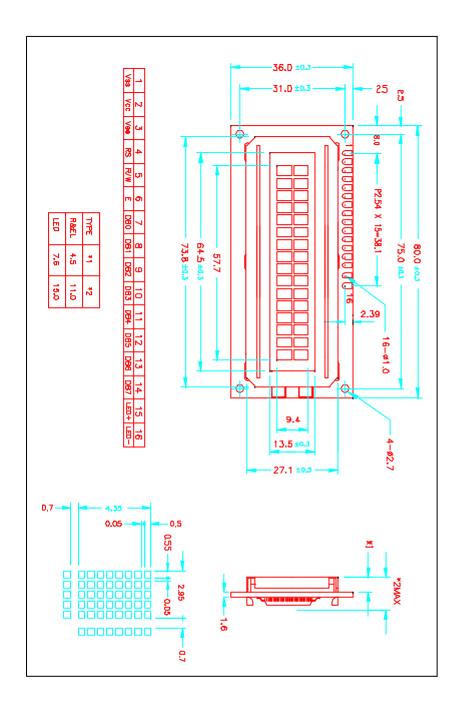
1.13 Dot Size: 0.55X0.50(mm) 1.14 Dot Pitch: 0.60X0.55 (mm)

1.15 Weight: 20g

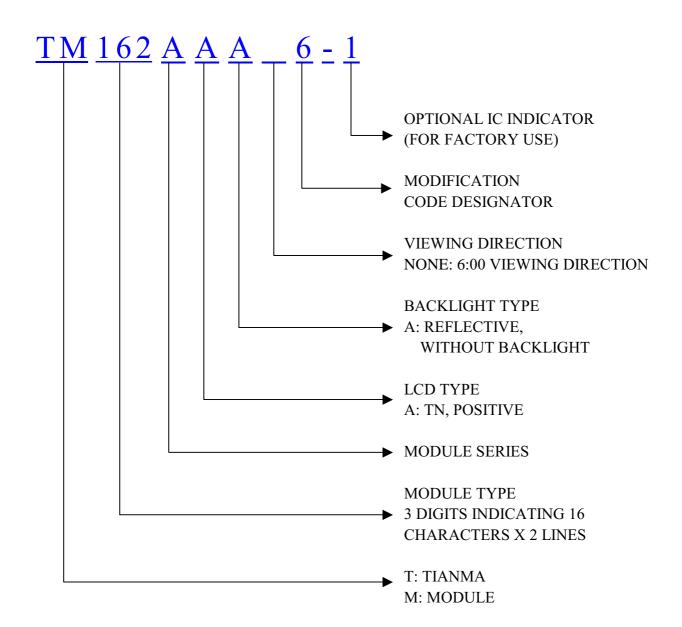
1.16 PCB edtion: TM162AD P1-2

^{*} Color tone is slightly changed by temperature and driving voltage.

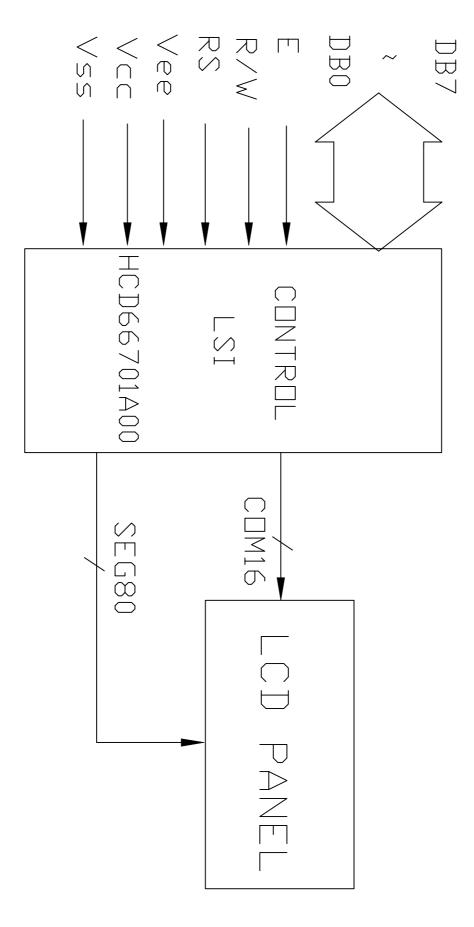
2. Outline Drawing



3. LCD Module Part Numbering System



4. Circuit Block Diagram



5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	V_{DD} – V_{SS}	-0.3	7.0	V	
LCD Driving Voltage	$V_{ m LCD}$	-0.3	13.0	v	
Operating Temperature Range	T_{OP}	0	+50	°C	No
Storage Temperature Range	T_{ST}	-20	+60		Condensation

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics

Iten	n	Symbol	Min.	Тур.	Max.	Unit
Supply V (Log	_	V _{DD} -V _{SS}	4.5	5.0	5.5	V
Supply Voltage (LCD Drive)		V _{LCD}	-	4.7	-	V
Input	High	V _{IH} (V _{DD} =5.0)	$0.7V_{DD}$	-	V _{DD} +0.3	V
Signal Voltage	Low	$V_{\text{\tiny IL}}$ $(V_{\text{DD}}=5.0)$	-0.3	-	0.2 V _{DD}	V
Supply c		$I_{DD} $ $(V_{DD}-V_{SS}=5.0)$	-	1.5	2.0	mA
Supply c		${ m I}_{\scriptscriptstyle m EE}$	-	0.40	0.6	mA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	Vss	0V	Ground
2	Vcc	5.0V	Power supply voltage for logic and LCD(+)
3	Vee	0.3V	Power supply voltage for LCD(-)
4	RS	H/L	Selects registers
5	R/W	H/L	Selects read or write
6	E	H/L	Starts data read/write
7	DB0	H/L	Data bit0
8	DB1	H/L	Data bit1
9	DB2	H/L	Data bit2
10	DB3	H/L	Data bit3
11	DB4	H/L	Data bit4
12	DB5	H/L	Data bit5
13	DB6	H/L	Data bit6
14	DB7	H/L	Data bit7
15	NC	-	No signal
16	NC	-	No signal

6.3 Interface Timing Chart

AC Characteristics (V $_{\rm CC}$ = 2.7 to 5.5V, $T_{\rm a}$ = –30 to +75°C*³)

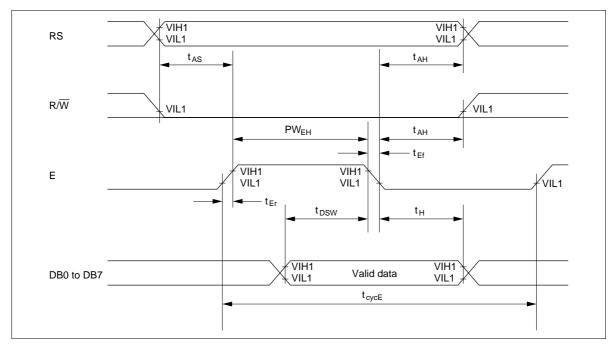
Write Operation

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Enable cycle time	t _{cycE}	1000	_	_	ns	
Enable pulse width (high level)	PW_{EH}	450	_	_		
Enable rise/fall time	t _{Er} , t _{Ef}	_	_	25		
Address set-up time (RS, R/W to E)	t _{AS}	40	_	_		
Address hold time	t _{AH}	20	_	_		
Data set-up time	t _{DSW}	195	_	_		
Data hold time	t _H	10	_	_	_	

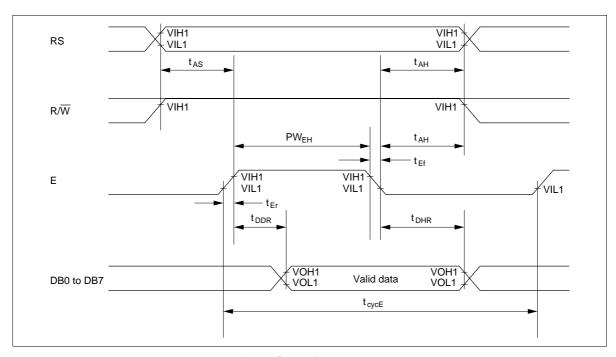
Read Operation

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Enable cycle time	t _{cycE}	1000	_	_	ns	
Enable pulse width (high level)	PW_{EH}	450	_	_		
Enable rise/fall time	t _{Er} , t _{Ef}	_	_	25		
Address set-up time (RS, R/\overline{W} to E)	t _{AS}	40	_	_		
Address hold time	t _{AH}	20	_	_		
Data delay time	t _{DDR}	_	_	350		
Data hold time	t _{DHR}	10	_	_		

Timing Characteristics



Write Operation



Read Operation

6.4 Instruction Code

					Co	ode						Execution Time (max) (when f _{cp} or
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	f _{osc} is 320 kHz)
Clear display	0	0	0	0	0	0	0	0	0	1	Clears entire display and sets DDRAM address 0 in address counter.	1.28 ms
Return home	0	0	0	0	0	0	0	0	1	_	Sets DDRAM address 0 in address counter. Also returns display from being shifted to original position. DDRAM contents remain unchanged.	1.28 ms
Entry mode set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	31 μs
Display on/off control	0	0	0	0	0	0	1	D	С	В	Sets entire display (D) on/off, cursor on/off (C), and blinking of cursor position character (B).	31 μs
Cursor or display shift	0	0	0	0	0	1	S/C	R/L	_	_	Moves cursor and shifts display without changing DDRAM contents.	31 μs
Function set	0	0	0	0	1	DL	N	F	_	_	Sets interface data length (DL), number of display lines (L), and character font (F).	31 μs
Set CGRAM address	0	0	0	1	ACG	ACG	ACG	ACG	ACG	ACG	Sets CGRAM address. CGRAM data is sent and received after this setting.	31 μs
Set DDRAM address	0	0	1	ADD	Sets DDRAM address. DDRAM data is sent and received after this setting.	31 μs						
Read busy flag & address	0	1	BF	AC	Reads busy flag (BF) indicating internal operation is being performed and reads address counter contents.	0 μs						

	Code							-		Execution Time (max) (when f _{cp} or			
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Descr	iption	f _{osc} is 320 kHz)
Write data to CG or DDRAM	1	0	Write	e data							Writes or CG	data into DDRAM RAM.	31 μs $t_{ADD} = 4.7 \ \mu s^*$
Read data from CG or DDRAM	1	1	Read	d data							Reads or CG	s data from DDRAM RAM.	31 μs $t_{ADD} = 4.7 \ \mu s^*$
	S/C R/L R/L	= 1: = 0: = 1: = 0: = 1: = 1: = 1:	Displa Curso Shift t Shift t 8 bits, 2 lines	ement mpanion by shift or move to the to the DL = s, N = O dots ally op	t re right left 0: 4 I 0: 1 I , F = 0	ine : 5×1 ng					CGRA ACG: ADD:	M: Display data RAM M: Character generator RAM CGRAM address DDRAM address (corresponds to cursor address) Address counter used for both DD and CGRAM addresses	Execution time changes when frequency changes Example: When f_{cp} or f_{OSC} is 270 kHz, $31 \mu s \times \frac{320}{270} = 37 \mu s$

Note: — indicates no effect.

* After execution of the CGRAM/DDRAM data write or read instruction, the RAM address counter is incremented or decremented by 1. The RAM address counter is updated after the busy flag turns off. In Figure 10, t_{ADD} is the time elapsed after the busy flag turns off until the address counter is updated.

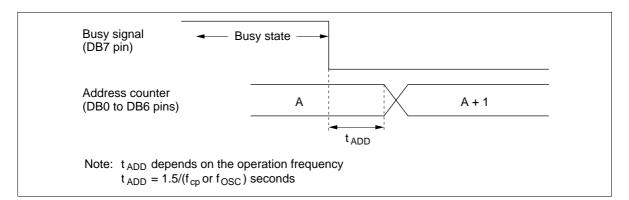


Figure 10 Address Counter Update

6.5 Character generator ROM(HCD66701A00)

Upper 4 Lower Bits 4 Bits	0000	0010	0011	0100	0101	0110	0111	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			<u>a</u>		*	!== -			-53	==_	CC	P
xxxx0001	(2)				Q	3	-==	Li		7-	Ġ	-==	
xxxx0010	(3)		<u> </u>		R	<u> </u> ;	1.**.	ľ	•1	ij	,×.*		Ш
xxxx0011	(4)	##				<u>. </u>	-=-			.	=	=-	28
xxxx0100	(5)					<u></u>	†	•••		P.	†7		
xxxx0101	(6)					豊	i_i						
xxxx0110	(7)				Ų	+	ij	===			=======================================		-
xxxx0111	(8)	7	# 		Į,J	9	iụi			×	-		71
xxxx1000	(1)	ľ.			X	ŀŋ	×	- •••				-,i	×
xxxx1001	(2)	j			Y		* <u>-</u>	!		, i		- [
xxxx1010	(3)	*								! "!	<u> </u> _,-		#:
xxxx1011	(4)		7	K		k	4					×	Ħ,
xxxx1100	(5)	7			#			-	<u>.</u> ,:		-	4	FFI
xxxx1101	(6)					ľi	}			••••		-i-	
xxxx1110	(7)				.**•.	ř"i		==			- <u>-</u> "-	F ⁻ 1	
xxxx1111	(8)		?			O		•11	<u>'-</u> ,;	***			

Note: The user can specify any pattern for character-generator RAM.

7. Optical Characteristics

7.1 Optical Characteristics

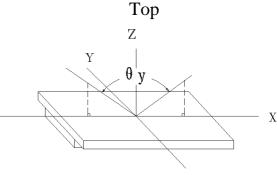
Ta= 25° C

Item		Symbol	Condition		Min.	Тур.	Max.	Unit
Viowing	Anglo	$\theta_{\!\mathbf{x}}$	C >2	θ _y =0°	35		10	
Viewing A	Viewing Angle		Cr≥2	θ _x =0°	-30	-30 30		
Contrast l	Ratio	Cr	$\theta_{x}=20^{\circ}$ $\theta_{y}=0^{\circ}$		3.0	-	-	
Response	Turn on	Ton		20°	-	-	150	ma
Time	Turn off	Toff	θ_{y} =	=0°	-	-	150	ms

7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle

Top Z

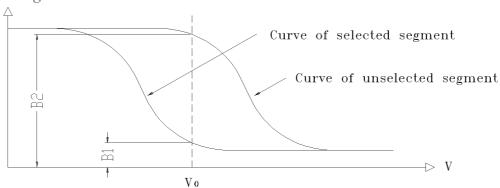


Bottom

Bottom

7.2.2 Definition of Contrast Ratio

Brightness



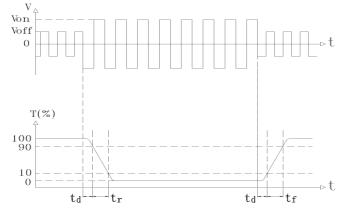
unselected state brightness Contrast Ratio = B2/B1selected state brightness

Measuring Conditions:

1) Ambient Temperature: 25°C;

2) Frame frequency: 64Hz

7.2.3 Definition of Response time



Turn on time: $t_{on} = t_d + t_r$

Turn off time: $t_{off} = t_d + t_f$

Measuring Condition:

1) Operating Voltage: 4.7V

2) Frame frequency: 64Hz

8. Reliability

8.1 Content of Reliability Test

$T_{\alpha-}$	25	$^{\circ}$
ra-	-23	\cup

No.	Test Item	Content of Test	Test condition
1	High Temperature	Endurance test applying the high	60°C
	Storage	storage temperature for a long time	96H
2	Low Temperature	Endurance test applying the low	-20°C
	Storage	storage temperature for a long time	96H
		Endurance test applying the	
3	High Temperature	electric stress (voltage & current)	50℃
	Operation	and the thermal stress to the	96H
		element for a long time	7011
	Low Temperature	Endurance test applying the	$0^{\circ}\!\mathbb{C}$
4	Operation	electric stress under low	96H
	Орегиноп	temperature for a long time	
	High Temperature	Endurance test applying the high	40℃ 90%RH
5	_	Humidity Storage temperature and high numberly	
	7 Training Storage	storage for a long time	96H
		Endurance test applying the low	
	Tommonotumo	and high temperature cycle	20°C /60°C
6	Temperature	-20°C ← 25°C ← 60°C ← 25°C	-20°C/60°C
	Cycle	30min 5min 30min 5min ←———————————————————————————————————	10 cycles
		1 cycle	
	77'1 4' TD 4		10Hz~150Hz,
7	Vibration Test	Endurance test applying the	50m/s^2 ,
	(package state)	vibration during transportation	40min
	Shock Test	Endurance test applying the shock	Half- sine wave,
8	(package state)	during transportation	100m/s^2 ,
	(Package state)		11ms
	Atmospheric	Endurance test applying the	40kPa
9	Pressure Test	atmospheric pressure during	40KFa 16H
		transportation by air	1011

8.2 Failure Judgment Criterion

Criterion	Test Item No.									Failura Judgament Critarian	
Item	1	2	3	4	5	6	7	8	9	Failure Judgement Criterion	
Basic Specification	1	V	1	1	1	1	1	V		Out of the basic Specification	
Electrical specification	V	V	1	V	V					Out of the electrical specification	
Mechanical Specification							1	V		Out of the mechanical specification	
Optical Characteristic	V	V	1	V	V	V			V	Out of the optical specification	
Note	For test item refer to 8.1										
Remark	Basic specification = Optical specification + Mechanical specification										

9. QUALITY LEVEL

Examination	At Ta=25°C	Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL	
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Appendix A			II	Major 1.0 Minor 2.5	
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See A _J	ppendix	В	II	Major 1.0 Minor 2.5	

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828

10. Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature: $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

Appendix AInspection items and criteria for appearance defects

Items	Contents	Criteria					
Leakage		Not permitted					
Rainbow		According to the limit specimen					
	Wrong polarizer attachment	Not permitted					
	Bubble between	Not counted		Max. 3 defects allowed			
Polarizer	polarizer and glass	ф<0.3mm		0.3mm≤¢≤0.5mm			
	Scratches of polarizer	According to	According to the limit specimen				
Black spot (in viewing area)		Not counted	Max. 3 spots allowed		Max. 3		
		X<0.2mm	0.2mm≤X≤0.5mm				
	α	X=(a+b)/2	spots (lines)				
Black line (in viewing area)		Not counted	Max. 3 lines allowed		allowed		
	b	a<0.02mm	0.02mm≤a≤0.05mm b≤2.0mm				
Progressive cracks		Not permitted					

Appendix BInspection items and criteria for display defects

Items Contents		Contents	Criteria				
Open segment or open common		Not permitted					
Short			Not permitted				
Wrong viewing angle			Not permitted				
Contrast radio uneven			According to the limit specimen				
Crosstalk			According to the limit specimen				
	-	 	Not counted	Max.3 dots allowed			
	1		X<0.1mm	0.1mm≤X≤0.2mm			
Pin holes and cracks in segment (DOT)			X=(a+b)/2	Max.3 dots			
	- D	Not counted	Max.2 dots allowed	allowed			
			A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm			
Black spot (in viewing area)			Not counted	Max.3 spots allowed			
			X<0.1mm	0.1mm≤X≤0.2mm			
			X=(a+b)/2	Max.3 spots			
Black line (in viewing area)	1 1		Not counted	Max.3 lines allowed	(lines) allowed		
			a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm			

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Criteria			
	- - a	Not counted	Max. 2 defects allowed		
		x<0.1mm	0.1mm≤x≤0.2mm		
		x=(a+b)/2			
				Max.3	
	D-111-a	Not counted	Max. 1 defects allowed	defects	
Transformation of segment		a<0.1mm	0.1mm≤a≤0.2mm D>0		
		Max.2 defects $0.8W \le a \le 1.2$			
		a=measured value of width W=nominal value of width			