SPECIFICATION FOR LCD MODULE

Model No. ___ **TM204AFFW7** ___

Prepared by:	Date:
Checked by :	Date:
Verified by :	Date:
Approved by:	Date:

TIANMA MICROELECTRONICS CO., LTD

Ver1.0

REVISION RECORD

Date	Ver	Ref. Page	Revision NO.	Revision Item
2003-3-12	Ver1.0			

1. General Specifications:

1.1 Display type: STN/Blue Mode

1.2 Display color*:

     Display color: White

     Background: Blue

1.3 Polarizer mode: Transmissive/Negative

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/16 Duty 1/5 Bias

1.6 Backlight: LED(White,4V±0.2V,Two lamps)

1.7 Controller: S6A0069X01-C0CX（KS0066UP-00CC）

1.8 Data Transfer: 8 Bit Parallel

1.9 Operating Temperature: -20----+70℃

   Storage Temperature: -30----+80℃

1.10 Outline Dimensions: Refer to outline drawing on next page

1.11 Dot Matrix: 20 Characters X 4Lines

1.12 Dot Size: 0.55X0.55(mm)

1.13 Dot Pitch: 0.60X0.60(mm)

1.14 Weight: 20g

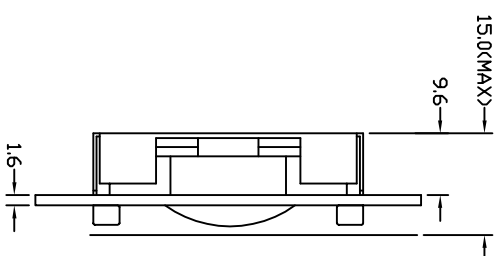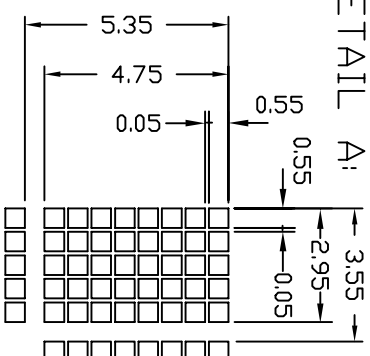   * Color tone is slightly changed by temperature and driving voltage.

2. Outline Drawing

NOTES:
1. DISPLAY TYPE:
2. DRIVE METHOD:
3. VIEWING DIRECTION:
4. POLARIZER MODE:
5. LCD OPERATING VOLTAGE:
6. OPERATING TEMP:
7. STORAGE TEMP:
8. BACKLIGHT TYPE:
9. CONTROLLER:
10. BEZEL IS TO BE PAINTED BLACK
11. ALL UNMARKED TOLERANCE:±0.3mm

STN/BLUE MODE
1/16 DUTY 1/5 BIAS
6:00
TRANSMISSIVE/NEGATIVE
4.7V
-20°C~+70°C
-30°C~+80°C
LED(WHITE,4.0V±0.2,TWO LAMPS)
S6A0069X01-C0CX(KS0066UP-00CC)
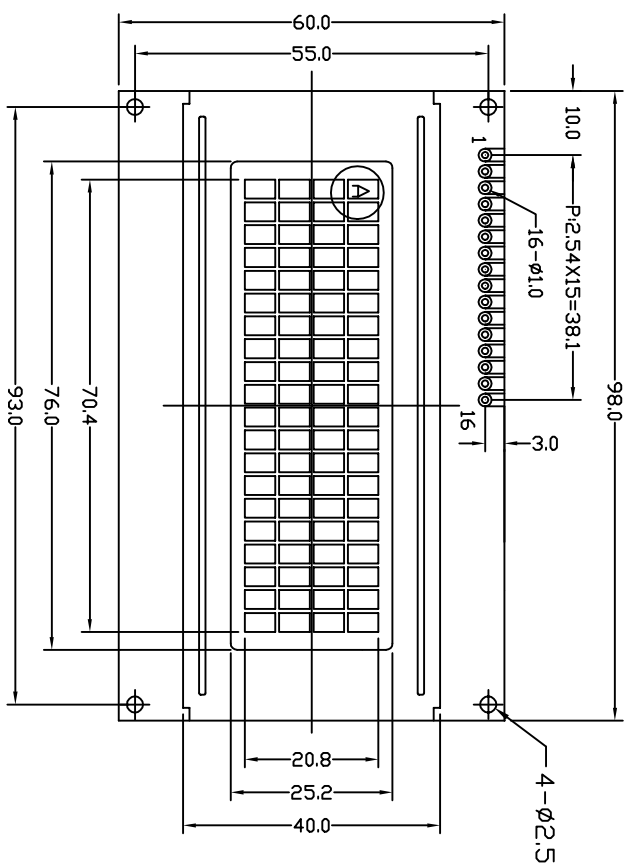
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Vss	Vcc	Vee	RS	R/W	E	DB0	DB1	DB2	DB3	DB4	DB5	DB6	DB7	LED+	LED-

DETAIL A:

TIAN-MA MICROELECTRONICS CO.

6/F., CASTIC Building, Shennan Road, Central, Shenzhen, China

DRAWN BY:	TITLE: TM204AFFW7
CHECKED BY:	
APPROVED BY:	DWG NO: G-1
CONFIRMED BY:	DWG NAME:TM204AFFW7G-1 SHEET NO: 1 OF 1

SCALE
UNIT: mm

3. LCD Module Part Numbering System

TM 204 A F F W 7

MODIFICATION
CODE DESIGNATOR

TEMPERATURE RANGE
W:WIDE TEMPERATURE RANGE

BACKLIGHT TYPE
F: TRANSMISSIVE
LED BACKLIGHT

LCD TYPE
F: BLUE MODE STN, NEGATIVE

MODULE SERIES

MODULE TYPE
3 DIGITS INDICATING
20 CHARACTERS X 4 LINES

T: TIANMA
M: MODULE

4. Circuit Block Diagram

DB7 ~ DB0

E

R/W

RS

Vee

Vcc

Vss

S6A0069X01-C0CX

CONTROL LSI

COM16

SEG40

SEG160

4

SEGMENT DRIVER×2

LCD
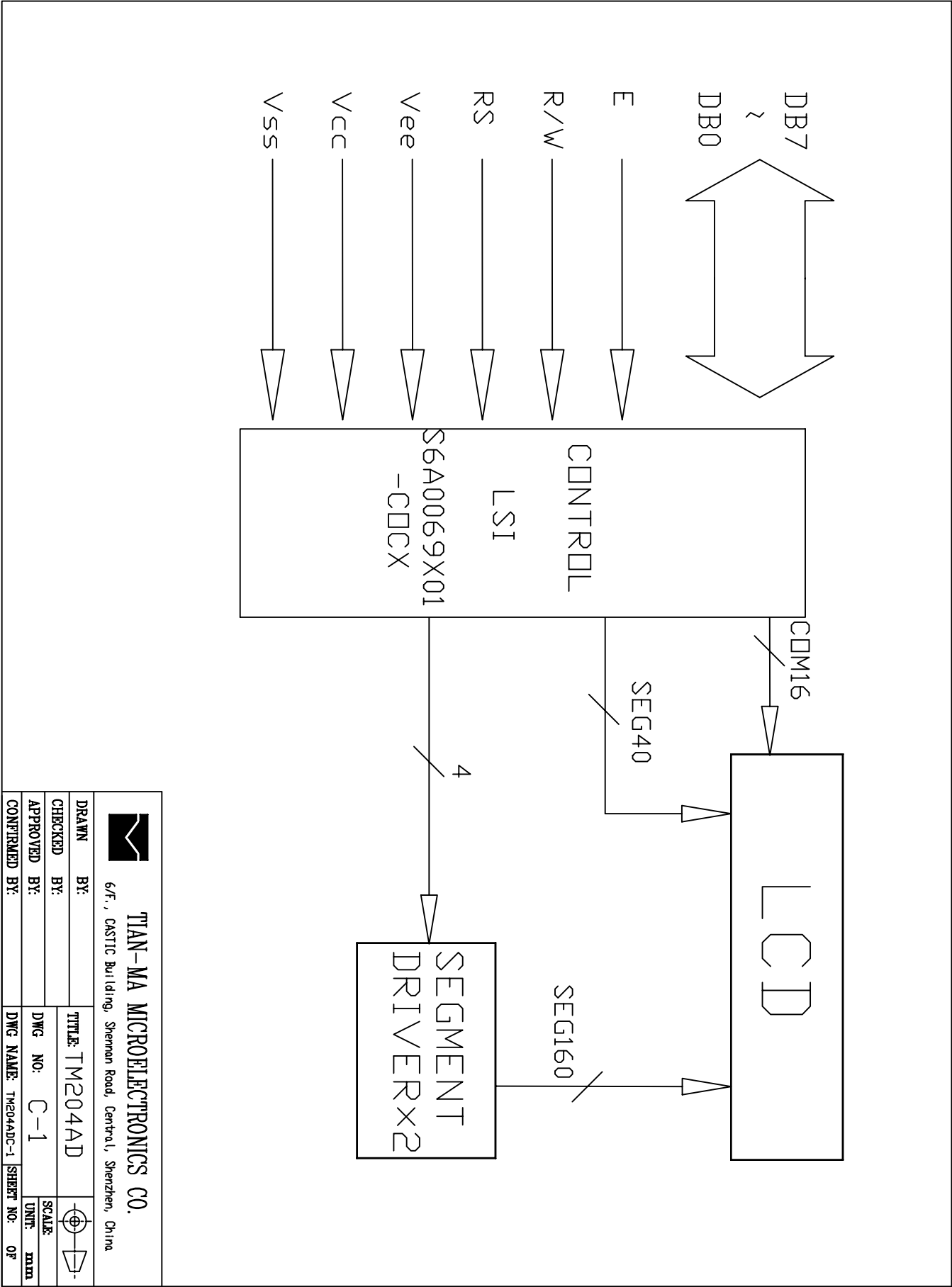
TIAN-MA MICROELECTRONICS CO.

6/F., CASTIC Building, Shennan Road, Central, Shenzhen, China

DRAWN BY:
CHECKED BY:
APPROVED BY:
CONFIRMED BY:

TITLE: TM204AD

DWG NO: C-1

DWG NAME: TM204ADC-1

SCALE:
UNIT: mm
SHEET NO: OF

5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	$V_{DD} - V_{SS}$	-0.3	7.0	V	
LCD Driving Voltage	V_{LCD}	-0.3	13.0		
Operating Temperature Range	T_{OP}	-20	+70	℃	No Condensation
Storage Temperature Range	T_{ST}	-30	+80		

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics

Item		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage (Logic)		$V_{DD}-V_{SS}$	4.5	5.0	5.5	V
Supply Voltage (LCD Drive)		V_{LCD}	-	4.7	-	V
Input Signal Voltage	High	V_{IH} （V_{DD}=5.0）	$0.7V_{DD}$	-	V_{DD}+0.3	V
	Low	V_{IL} （V_{DD}=5.0）	-0.3	-	$0.2 V_{DD}$	V
Supply current (Logic)		I_{DD} （$V_{DD}-V_{SS}$ =5.0）	-	-	0.6	mA
Supply current (LED)		I_{LED}	-	-	40.0	mA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	Vss	0V	Ground
2	Vcc	5.0V	Power supply voltage for logic and LCD(+)
3	Vee	0.3V	Power supply voltage for LCD(-)
4	RS	H/L	Register Select H: Data register is selected L: Instruction register is selected
5	R/W	H/L	Selects read or write H: Read operation L: Write operation
6	E	H/L	Starts data read/write
7	DB0	H/L	Data bit0
8	DB1	H/L	Data bit1
9	DB2	H/L	Data bit2
10	DB3	H/L	Data bit3
11	DB4	H/L	Data bit4
12	DB5	H/L	Data bit5
13	DB6	H/L	Data bit6
14	DB7	H/L	Data bit7
15	LED+	4.0V	Power supply voltage for LED(+)
16	LED-	0	Power supply voltage for LED(-)

6.3 Interface Timing Chart

AC Characteristics(V_{DD}=4.5V~5.5V,Ta=-30~+85℃)

| Mode	Characteristics	Symbol	Min	Typ	Max	Unit
Write Mode (refer to Figure-6)	E Cycle Time	tc	500	-	-	ns
	E Rise / Fall Time	t_R, t_F	-	-	20	
	E Pulse Width (High, Low)	tw	230	-	-	
	R/W and RS Setup Time	tsu1	40	-	-	
	R/W and RS Hold Time	t_{H1}	10	-	-	
	Data Setup Time	tsu2	80	-	-	
	Data Hold Time	t_{H2}	10	-	-	
Read Mode (refer to Figure-7)	E Cycle Time	tc	500	-	-	ns
	E Rise / Fall Time	t_R, t_F	-	-	20	
	E Pulse Width (High, Low)	tw	230	-	-	
	R/W and RS Setup Time	tsu	40	-	-	
	R/W and RS Hold Time	t_H	10	-	-	
	Data Output Delay Time	tD	-	-	120	
	Data Hold Time	tDH	5	-	-	
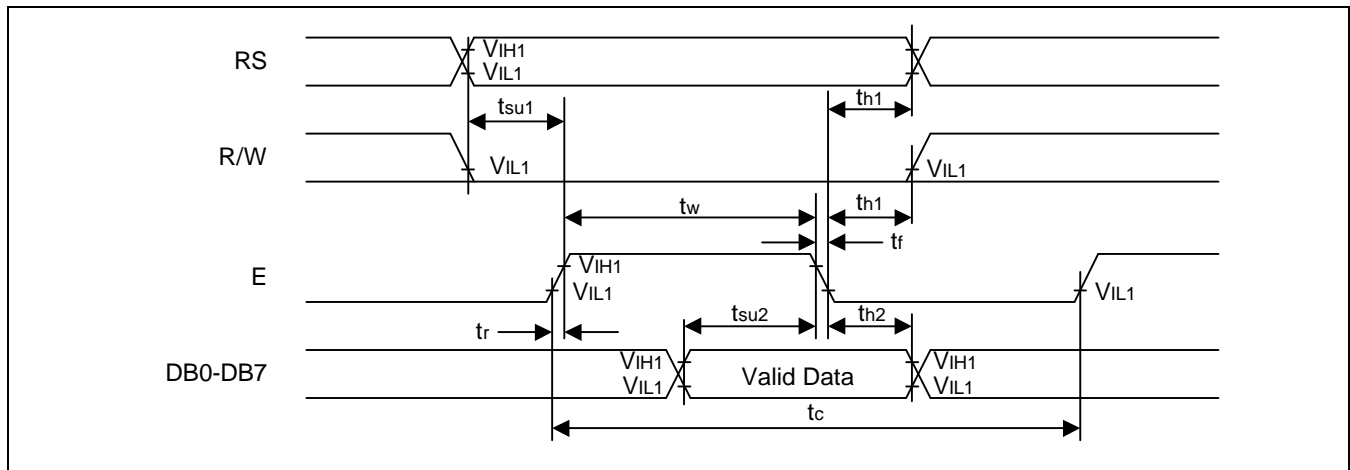
Figure 6. Write Mode Timing Diagram
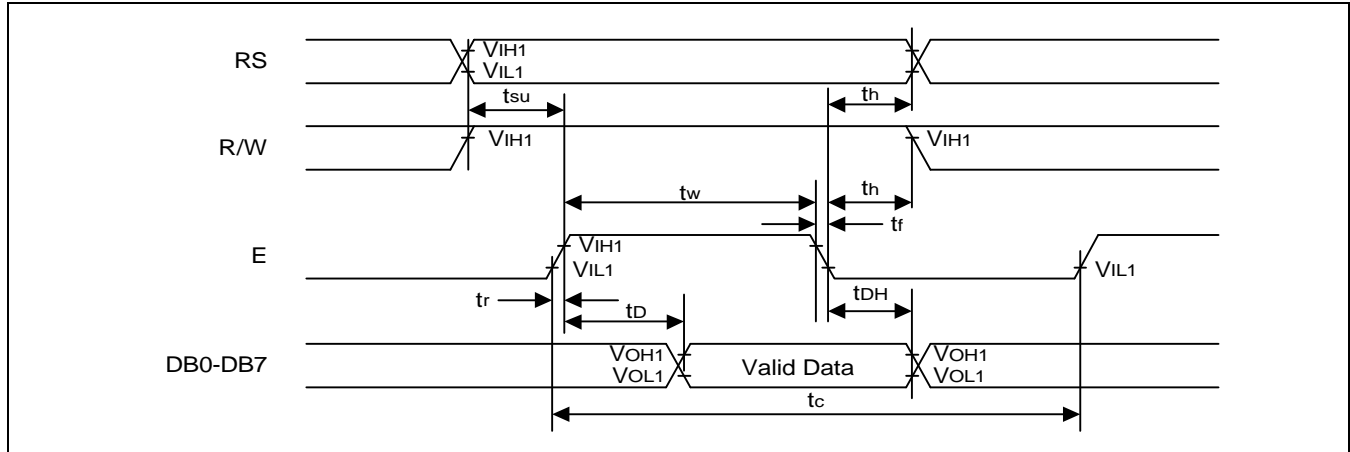
Figure 7. Read Mode Timing Diagram

6.4 Instruction Code

Table 7. Instruction Table

Instruction	Instruction Code										Description Instruction Code	Execution time (fsoc=270kHz)
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.53ms
Return Home	0	0	0	0	0	0	0	0	1	X	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and make shift of entire display enable.	39μs
Display ON/OFF Control	0	0	0	0	0	0	1	D	C	B	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	39μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	X	X	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	39μs
Function Set	0	0	0	0	1	DL	N	F	X	X	Set interface data length (DL : 4-bit/8-bit), numbers of display line (N : 1-line/2-line), display font type(F : 5 X 8 dots/ 5 X 11 dots)	39μs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39μs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39μs
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0μs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43μs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43μs

NOTE: When an MPU program with checking the Busy Flag (DB7) is made, it must be necessary 1/2 fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag (DB7) goes to "LOW".

6.5 Character generator ROM(KS0066U-00)

Upper 4bit / Lower 4bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)			0	@	P	`	p				─	タ	ミ	α	p
LLLH	(2)		!	1	A	Q	a	q			。	ア	チ	ム	ä	q
LLHL	(3)		"	2	B	R	b	r			｢	イ	ツ	メ	β	θ
LLHH	(4)		#	3	C	S	c	s			｣	ウ	テ	モ	ε	∞
LHLL	(5)		$	4	D	T	d	t			、	エ	ト	ヤ	μ	Ω
LHLH	(6)		%	5	E	U	e	u			・	オ	ナ	ユ	σ	ü
LHHL	(7)		&	6	F	V	f	v			ヲ	カ	ニ	ヨ	ρ	Σ
LHHH	(8)		'	7	G	W	g	w			ァ	キ	ヌ	ラ	g	π
HLLL	(1)		(	8	H	X	h	x			ィ	ク	ネ	リ	√	x̄
HLLH	(2)		)	9	I	Y	i	y			ゥ	ケ	ノ	ル	⁻¹	y
HLHL	(3)		*	:	J	Z	j	z			ェ	コ	ハ	レ	j	千
HLHH	(4)		+	;	K	[	k	{			ォ	サ	ヒ	ロ	×	万
HHLL	(5)		,	<	L	¥	l					ャ	シ	フ	ワ	¢	円
HHLH	(6)		-	=	M	]	m	}			ュ	ス	ヘ	ン	Ł	÷
HHHL	(7)		.	>	N	^	n	→			ョ	セ	ホ	゛	ñ	
HHHH	(8)		/	?	O	_	o	←			ッ	ソ	マ	゜	ö	█

7. Optical Characteristics

7.1 Optical Characteristics   Ta=25℃

Item		Symbol	Condition		Min.	Typ.	Max.	Unit
Viewing Angle		θ_x	$Cr \geq 2$	$\theta_y=0°$	-35	--	20	Deg
		θ_y		$\theta_x=0°$	-30	--	30	
Contrast Ratio		C_r	$\theta_x=0°$ $\theta_y=0°$		4.0	-	-	
Response Time	Turn on	T_{on}	$\theta_x=0°$ $\theta_y=0°$		-	-	250	ms
	Turn off	T_{off}			-	-	250	

7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle
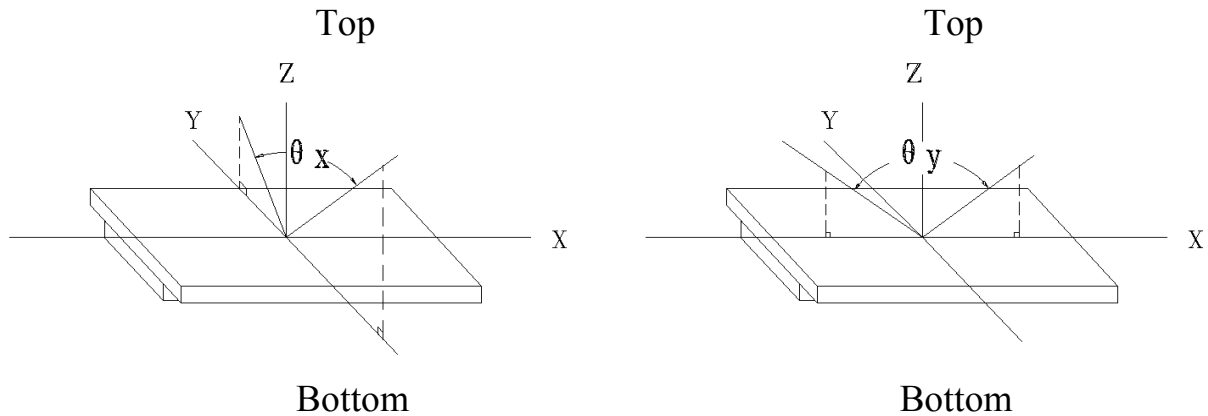
7.2.2 Definition of Contrast Ratio
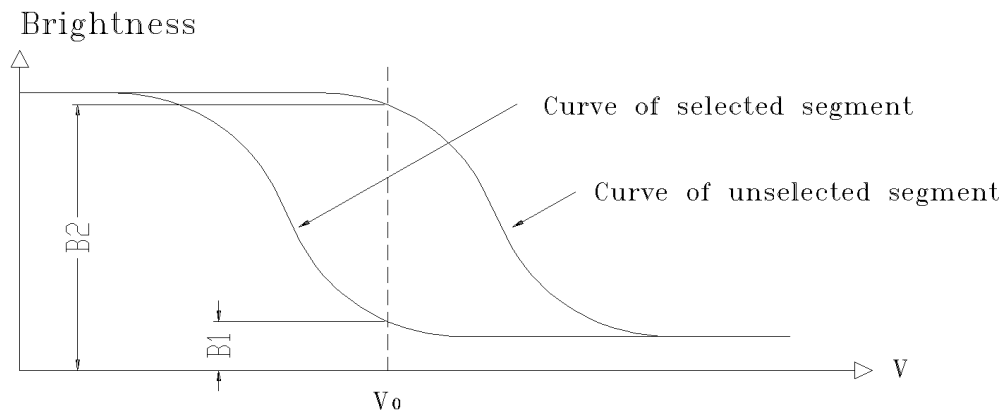
$$\text{Contrast Ratio} = B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

1) Ambient Temperature: 25℃;   2) Frame frequency: 84.3Hz

7.2.3 Definition of Response time
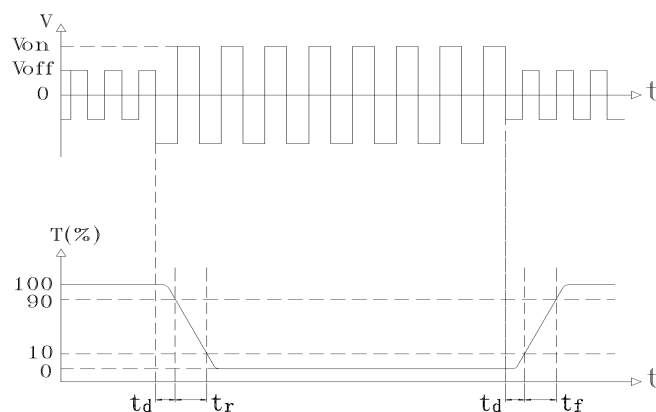
Turn on time: $t_{on} = t_d + t_r$ Turn off time: $t_{off} = t_d + t_f$

Measuring Condition:

1) Operating Voltage: 4.7V   2) Frame frequency: 84.3Hz

8. Reliability

8.1 Content of Reliability Test   Ta=25℃

No.	Test Item	Content of Test	Test condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	80℃ 240H Restore 4H at 25℃
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-30℃ 240H Restore 4H at 25℃
3	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	60℃ 90%RH 240H Restore 4H at 25℃
4	Temperature Cycle	Endurance test applying the low and high temperature cycle -30℃⟷25℃⟷80℃⟷25℃ 30min 5min 30min 5min ⟵------------------------⟶ 1 cycle	-30℃/80℃ 10 cycles
5	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 100m/s^2, 120min
6	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 300m/s^2, 18ms
7	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	25kPa 16H

8.2 Failure Judgment Criterion

Criterion Item	Test Item No.							Failure Judgement Criterion
	1	2	3	4	5	6	7	
Basic Specification	√	√	√	√	√	√	√	Out of the basic Specification
Electrical specification	√	√	√					Out of the electrical specification
Mechanical Specification					√	√		Out of the mechanical specification
Optical Characteristic	√	√	√	√			√	Out of the optical specification
Note	For test item refer to 8.1							
Remark	Basic specification = Optical specification + Mechanical specification							

9. QUALITY LEVEL

Examination or Test	At Ta=25℃ (unless otherwise stated)	Inspection				
		Min.	Max.	Unit	IL	AQL
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Appendix A			II	Major 1.0 Minor 2.5
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See Appendix B			II	Major 1.0 Minor 2.5
Note: Major defects: Open segment or common, Short, Serious damages, Leakage Miner defects: Others Sampling standard conforms to GB2828						

10. Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

  — Isopropyl alcohol
  — Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

  — Water
  — Ketone
  — Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

  a. Be sure to ground the body when handling the LCD Modules.

  b. Tools required for assembly, such as soldering irons, must be properly ground.

  c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

  d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:
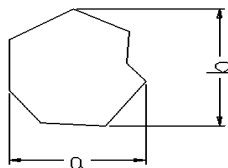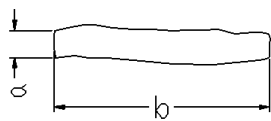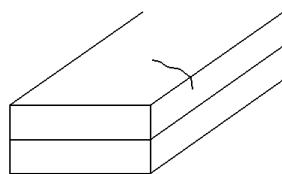
Temperature :   0℃ ～ 40℃

Relatively humidity: ≤80%

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.
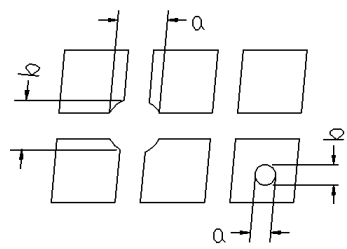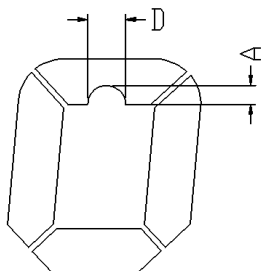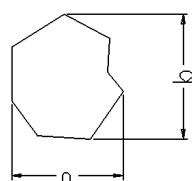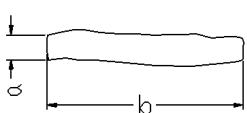
Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria			
Leakage		Not permitted			
Rainbow		According to the limit specimen			
Polarizer	Wrong polarizer attachment	Not permitted			
	Bubble between polarizer and glass	Not counted		Max. 3 defects allowed	
		φ<0.3mm		0.3mm≤φ≤0.5mm	
	Scratches of polarizer	According to the limit specimen			
Black spot (in viewing area)		Not counted	Max. 3 spots allowed		Max. 3 spots (lines) allowed
		X<0.2mm	0.2mm≤X≤0.5mm		
		X=(a+b)/2			
Black line (in viewing area)		Not counted	Max. 3 lines allowed		
		a<0.02mm	0.02mm≤a≤0.05mm b≤2.0mm		
Progressive cracks		Not permitted			
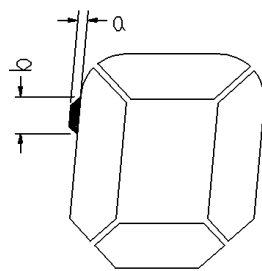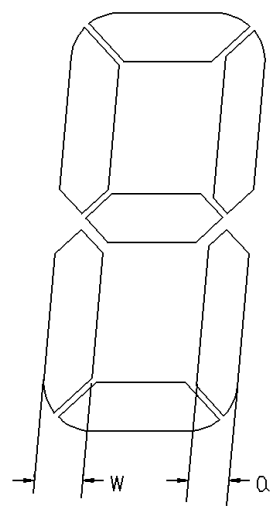
Appendix B

Inspection items and criteria for display defects

Items		Contents	Criteria		
Open segment or open common			Not permitted		
Short			Not permitted		
Wrong viewing angle			Not permitted		
Contrast radio uneven			According to the limit specimen		
Crosstalk			According to the limit specimen		
Pin holes and cracks in segment (DOT)		Not counted	Max.3 dots allowed	Max.3 dots allowed	
		X<0.1mm	0.1mm≤X≤0.2mm		
		X=(a+b)/2			
		Not counted	Max.2 dots allowed		
		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm		
Black spot (in viewing area)		Not counted	Max.3 spots allowed	Max.3 spots (lines) allowed	
		X<0.1mm	0.1mm≤X≤0.2mm		
		X=(a+b)/2			
Black line (in viewing area)		Not counted	Max.3 lines allowed		
		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm		

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Criteria		
Transfor-mation of segment		Not counted	Max. 2 defects allowed	Max.3 defects allowed
		x＜0.1mm	0.1mm≤x≤0.2mm	
		x=(a+b)/2		
		Not counted	Max. 1 defects allowed	
		a＜0.1mm	0.1mm≤a≤0.2mm D>0	
		Max.2 defects allowed 0.8W≤a≤1.2W a=measured value of width W=nominal value of width		