SPECIFICATION FOR LCD MODULE

Prepared by: Date: Checked by: Date: Verified by: Date: Approved by: Date:

TIANMA MICROELECTRONICS CO., LTP



## **REVISION RECORD**

Date	Rev.No.	Revision Items	Prepared	Checked	Approved
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# 1.General Specifications

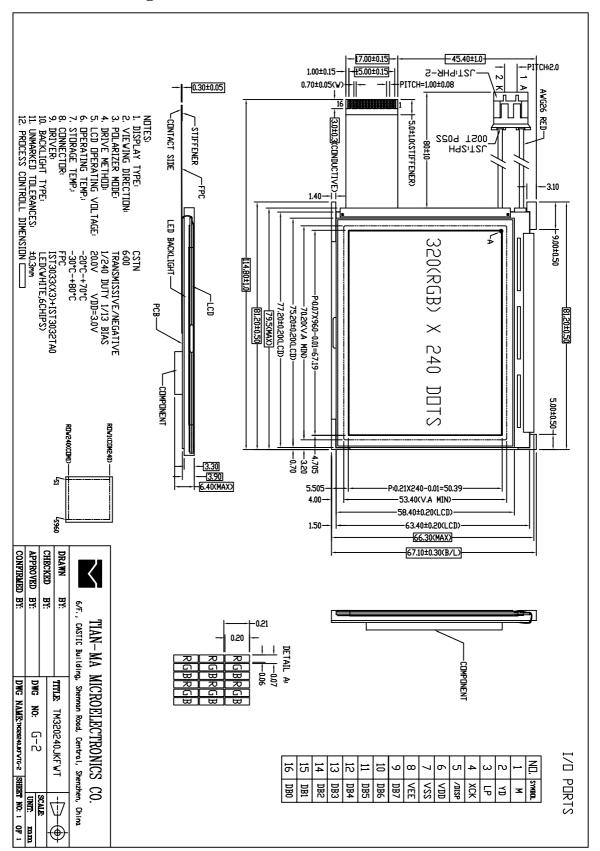
Item	Contents	Unit	Note
LCD Type	Color STN	-	
Display color			1
LCD Duty	1/240	-	
LCD Bias	1/13	-	
Viewing Direction	6:00	O'Clock	
Viewing Area(W×H)	70.2×53.4	mm	
Active Area(W×H)	67.19×50.39	mm	
Number of Dots	320(RGB)×240	mm	
Dote Size(W×H)	0.20×0.20	mm	
Dot Pitch(W×H)	0.21×0.21	mm	
Controller	None	-	
$V_{ m DD}$	3.0	V	
Vop=?	20.0	V	
Outline Dimensions	Refer to outline drawing on next page		
Backlight	LED(white)	-	
Operating Temperature	-20 ~ +70	-	
Storage Temperature	-30 ~ +80	-	
Weight	TBD	g	2
Data Transfer	8 bits parallel	-	
Polarizer Mode	Transmissive/Negative	-	

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: TBD- To Be Determined.



#### 2. Outline Drawing





# 3. Circuit Block Diagram



## **4.** Absolute Maximum Ratings(Ta=25)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	$V_{ m DD}$	-0.3	7.0	V	
Power Supply Voltage for LCD	Vop= VEE-VSS	-0.3	25	V	
Logic Signal Input Voltage	$V_{\rm I}$	-0.3	V <sub>DD</sub> +0.3	V	
Operating Temperature	Тор	-20	+70		
Storage Temperature	Tst	-30	+80		

#### Notes:

- 1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
- 2.  $V_{DD} > V_{SS}$  must be maintained.



# **5. Electrical Specifications and Instruction Code**

## **5.1 Electrical characteristics (Ta=25** )

Paran	neter	Symbol	Condition	Min	Тур	Max	Unit	Note
Supply	voltage	$V_{ m DD}$	Ta=25	2.7	3.0	3.3	V	
Operation voltage		$V_{\mathrm{OP}}$	Ta=25	19.0	20.0	21.0	V	1
Input	'H'	$V_{ m IH}$	V <sub>DD</sub> =3.0V	$0.8V_{DD}$	-	$V_{ m DD}$	V	
voltage	'L'	$V_{\rm IL}$	V <sub>DD</sub> =3.0V	Vss	-	$0.2V_{\mathrm{DD}}$	V	
Output	'H'	$V_{ m OH}$	-	$0.8V_{DD}$	-	$V_{ m DD}$	V	
voltage	'L'	$V_{OL}$	-	Vss	-	$0.2V_{DD}$	V	
	,		Normal mode	-	TBD.		mA	2
Curr Consur		I <sub>CC2</sub>	Stand-by mode	-	TBD.	-	mA	3
	1		dimming mode	-	TBD.	-	mA	4

#### Note:

- 1: At the condition at  $V_{DD}$ =3.0V.
- 2: Display full white. Backlight on state.
- 3: IC on standby mode.
- 4: Display full white. Backlight dimming state



## 5.2 LED backlight specification

Item		Symbol	Condition	Min	Тур	Max	Unit	Note
Forward voltage		$V_{\mathrm{f}}$	I <sub>f</sub> =30mA	9.0	9.9	10.8	V	
Reverse voltage		V <sub>r</sub>	-	-	-	-	V	
Forward	Normal	$I_{pn}$	3-chip	20	20	40	A	
current	Dimming	$I_{pd}$	serial (x2)	20	30	40	mA	
Reverse Current		$I_{\rm r}$	V <sub>r</sub> =-V	-	-	-	μΑ	
Uniformity			I <sub>f</sub> =30mA	70%	-	-		

#### **5.3 Indicator LED Specification**

Color	Condition	VF(V)			coordinate		
		Min	Тур	Max	X	y	
white	vhite If=30mA	9.0	9.9	10.8	TBD.	TBD.	



## **5.4 Interface Signals**

Pin No.	Symbol	Level	Description
1	M	H/L	Input of signal to AC electrify the liquid crystal drive output
2	YD	H/L	Scan start pulse
3	LP	H/L	Display data latch pulse input
4	XCK	H/L	Display data shift clock input
5	DISPOFF	H/L	H: Display on, L: Display off
6	VDD	+3.0V	Supply voltage for logic
7	VSS	0V	Ground
8	VEE	+20V	LCD voltage output
9	D7	H/L	Data bit 7
10	D6	H/L	Data bit 6
11	D5	H/L	Data bit 5
12	D4	H/L	Data bit 4
13	D3	H/L	Data bit 3
14	D2	H/L	Data bit 2
15	D1	H/L	Data bit 1
16	D0	H/L	Data bit 0
_	Α	9.9V	Power supply for LED backlight anode
-	K	0V	Power supply for LED backlight cathode



## **5.5 Interface Timing Chart**

Note: Please refer to Integrated Solutions Technology's driver specification. IST3032 and IST3033









## **5.6 INSTRUCTION**

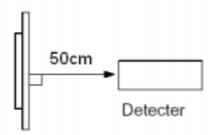


# 6. Optical Characteristics

Item	Sy	ymbol	Condition	Min.	Тур.	Max.	Unit	Note	
Brightness		Bp	Ф1=0°	80			Cd/m <sup>2</sup>	1	
Uniformity		Вр	Ф2=0°	70%				1,2	
Viewing	$\Phi_1(u)$	p down)	Cr≥2	_	40 ~ +35		Dog	2	
Angle	Ф2(16	eft right)	C1 <u>2</u> 2	-	30 ~ +30		Deg	3	
Contrast Ratio		Cr	Φ -00	30	40	60	1	4	
Response	Tr		$\Phi_1=0^{\circ}$ $\Phi_2=0^{\circ}$	•	-	180	ms	5	
Time	$T_{\mathrm{f}}$			70	-	90		3	
	W	X		•	0.30	1	ı		
		у		•	0.36	1	ı		
	n	X		•	0.53	1	ı		
Color of CIE	R	у		•	0.37	1	ı		
Coordinate	G	X	$\Phi_1 = 0^{\circ}$ $\Phi_2 = 0^{\circ}$	•	0.31	1	ı	1,6	
	U	у	¥2 0	•	0.51	-	-		
	D	X		_	0.16	-	-		
	В	у		-	0.18	1	ı		
NTSC Ratio		S			27%				



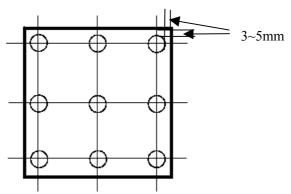
Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment PR-705 ( $\Phi$ 10mm)



Note 2:  $Bp = Bp (Min.) / Bp (Max.) \times 100 (\%)$ 

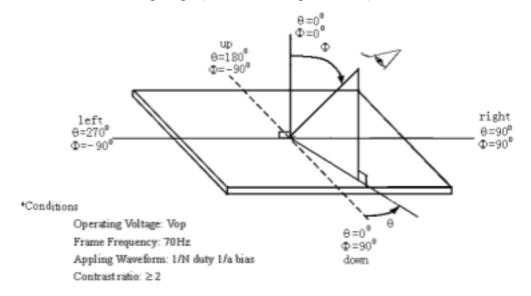
Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = Minimum brightness in 9 measured spots.



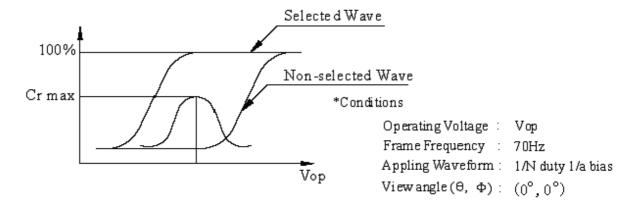
Measurement equipment PR-705 (Φ10mm)

Note 3: Definition of Viewing Angle(Test LCD using DMS501)



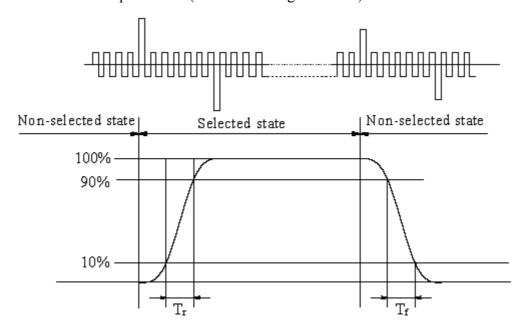


Note 4: Definition of contrast ratio.( Test LCD using DMS501)



Contrast  $ratio(Cr) = \frac{Brightness\ of\ selected\ dots}{Brightness\ of\ non-selected\ dots}$ 

Note 5: Definition of Response time(Test LCD using DMS501)

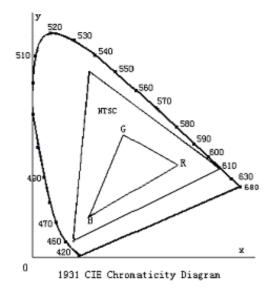


Operating Voltage: Vop Frame Frequency: 70Hz

Appling Waveform: 1/N duty 1/a bias

View angle  $(\theta, \Phi)$ :  $(0^0, 0^0)$ 

#### Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.



## Color gamut:

$$S = \frac{area\ of\ RGB\ triangle}{area\ of\ NTSC\ triangle} \times 100\%$$



# 7. Reliability

No.	Test Item	Test condition	Criterion
1	High Temperature Storage	80 ±2 96H Restore 4H at 25	
2	Low Temperature Storage	-30 ±2 96H Restore 4H at 25	
3	High Temperature Operation	70 ±2 48H Restore 4H at 25	
4	Low Temperature Operation	-20 ±2 48H Restore 4H at 25	1. After testing, cosmetic
5	High Temperature /Humidity Storage	40 ±2 90%RH 48H	defects should not happen.  2.Total current consumption should not be
6	Temperature Cycle	-30 ←→25 ←→80 5min 30min ←→25 , 5min after 10cycle, Restore 4H at 25	over 10% of initial value.
7	Vibration Test (package state)	10Hz~150Hz, 100m/s2, 120min	
8	Shock Test (package state)	Half- sine wave, 300m/s2, 18ms	Not allowed cosmetic and electrical defects.
9	Atmospheric Pressure Test	25kPa 16H Restore 2H	
10	Cable Bending Test	Bending area and angle follow design document requirement	More than 50000 times



# 8 Quality level

# 8.1 Notes for quality standard

	Note						
General	<ol> <li>Should any defects which are not specified in this standard happen, addi standard shall be determined by mutual agreement between customer Tianma.</li> <li>Viewing Area should be the area which Tianma guarantees.</li> <li>Limited sample should be prior to this Inspection standard.</li> <li>Viewing Judgement should be under static pattern.</li> <li>Inspection conditions         <ul> <li>Inspection distance</li> <li>250 mm (from the sample)</li> <li>Temperature</li> <li>25±5</li> <li>Inspection angle</li> <li>45degrees in LCD view direction</li> </ul> </li> </ol>						
Definitions of Inspection items	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble  The color of a small area is different from the remainder. The phenomenon dose not change with voltage.  The color of a small area is different from the remainder.  The phenomenon dose not change with voltage.						
	remainder. The phenomenon changes with voltage.  Polarizer defect Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass.						
Definitions of Inspection ranges	Glass defect  X1  X2  Dividing A zone and B zone proceed to make a judgment.  A zone: Inside Viewing area  B zone: Outside Viewing area  X1(A.A~V.A): mm  X2(A.A~V.A): mm  Y1(A.A~V.A): mm  Y2(A.A~V.A): mm						
Outgoing Inspection standard	Rank   Inspection Item   AQL(Number of defective LCMs counted)						



## 8.2Standards of inspection items

				Judgement stan	dard	
Ins	spection item			vaagement stan	Acceptabl	le number
1113	spection tem			Category	A zone	B zone
1	Black spot, White spot Bright Spot, Pinhole Foreign P Bubble and Particle Between polarizer Scratch on polarizer	= (a+b)/2 (mm) and glass,	A B C D	0.15 0.15< 0.20 0.20< 0.30 0.30< Total defective point(B,C)	Neglecte 2 1 0 3	Neglected
2	Black line, White line, Bubble and Particle Between Polarizer and glass, Scratch on polarizer	W:Width, L:Length(mm)	A B C D	W 0.10 0.01 <w 0.03="" 3.0<br="" l="">0.03<w 0.05="" 3.0<br="" l="">0.05<w Total defective point(B,C)</w </w></w>	Neglected 2 1 0 2 2	Neglected
3	Contrast variation	$ \begin{array}{c c}  & b \\ \hline  & a \\  & \Phi = (a+b)/2 \text{(mm)} \end{array} $	A B C D	$\begin{array}{c cccc} \Phi & 0.2 \\ \hline & 0.2 < \Phi & 0.3 \\ \hline & 0.3 < \Phi & 0.4 \\ \hline & 0.4 < \Phi \\ \hline & Total defective point(B,C) \\ \end{array}$	Neglected 2 1 0 3	Neglected
4	Bubble inside cell		any	size	none	none
5	Polarizer defect (if Polarizer is used)	Scratch and damage on polarizer, Particle on polarizer or between polarizer and glass.  Bubble, dent and convex	A B C	er to item 1 and item 2. $ \frac{\Phi}{0.3} = \frac{0.3}{0.7} = \frac{0.7}{0.7} $ Total defective point(B,C)	Neglected 2 0 2	Neglected
6	Surplus glass	Stage surplus glass  b  Surrounding surplus glass	b	0.3mm  uld not influence outline dime	nsion and as	sembling.



MODEL No.:TM320240JKFWT

Inspection item			Judgment standard
			Category(application: B zone)
7	Glass defect	The front of lead terminals	A If a t and b 1.0, c is not limited
	crack		B a t, 1 b 2mm, c 3mm
		b	C If glass crack cover alignment mark, b 0.5mm.
		w t a c	D Crack at two sids of lead terminals should not cover patterns and alignment mark
		Surrounding crack—non-contact side  seal  c b a t  Inner border line of the seal  Outer border line of the seal	b < Inner borderline of the seal
		Surrounding crack— contact side  seal  c h a  Inner border line of the seal  Outer border line of the seal	b < Outer borderline of the seal
		Corner	A a t, b 3.0, c 3.0  *Glass crack should not cover patterns used for



Inspection item			Judgement standard
8	PCB defect	Component soldering: No cold soldering, short, open circuit, burr, tin ball The flat encapsulation component position deviation must be less than 1/2 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2)	Component  Soldering pad  Lead  L1>0
		lead defect: The lead lack must be less than 1/2of its width; The lead burr must be less than 1/2 of the seam; Impurities connect with the near leads is not permitted	
		Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	Soldering tin is not permit in this area  Soldering tin is not permit in this area
			socket Base Board



#### 9. Precautions for Use of LCD Modules

#### 9.1 Handling Precautions

- 9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 9.1.6 Do not attempt to disassemble the LCD Module.
- 9.1.7 If the logic circuit power is off, do not apply the input signals.
- 9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and



other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

#### 9.2 Storage precautions

- 9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature:  $0 \sim 40$ 

Relatively humidity: ≤80%

- 9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- **9.3** The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.