

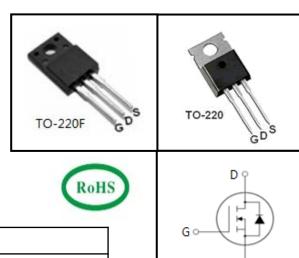
## 200V N-Channel MOSFET

#### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information			
Device	Marking		
TMA18N20H	TO-220F	A18N20H	
TMP18N20H	TO-220	P18N20H	

<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted					
Parameter		Comple at	Value		
		Symbol	TO-220F	TO-220	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)		V <sub>DSS</sub>	200		V
Continuous Drain Current		I <sub>D</sub>	18		А
Pulsed Drain Current (	(note1)	I <sub>DM</sub>	72		А
Gate-Source Voltage		V <sub>GSS</sub>	±20		V
Single Pulse Avalanche Energy (	(note2)	E <sub>AS</sub>	340		mJ
Avalanche Current (	(note1)	I <sub>AR</sub>	15		А
Repetitive Avalanche Energy (	(note1)	E <sub>AR</sub>	8.3		mJ
Power Dissipation (T <sub>C</sub> = 25°C)		$P_{D}$	63.7	104	W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+150		°C

Thermal Resistance				
Borometer	Symbol	Value		Unit
Parameter	Symbol TO-220F		TO-220	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.96	1.2	00.00
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5	60	°C/W



_			Value			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	200			V
Zoro Coto Voltago Drain Current	I <sub>DSS</sub>	$V_{DS} = 200V, V_{GS} = 0V, T_{J} = 25^{\circ}C$	5			
Zero Gate Voltage Drain Current		$V_{DS} = 160 \text{V}, V_{GS} = 0 \text{V}, T_{J} = 125 ^{\circ}\text{C}$			100	μA
Gate-Source Leakage	$I_{\mathrm{GSS}}$	$V_{GS} = \pm 20V$			±100	nA
Gate-Source Threshold Voltage	$V_{\rm GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 9A		0.12	0.15	Ω
Dynamic						
Input Capacitance	C <sub>iss</sub>	V - 0V		1318		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 25V,$		180		
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		75		
Total Gate Charge	$Q_g$			41		nC
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 160V, I_{D} = 18A,$ $V_{GS} = 10V$		5.5		
Gate-Drain Charge	$Q_{gd}$	93		19.5		
Turn-on Delay Time	t <sub>d(on)</sub>			24		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 100V, I_{D} = 18A,$		45		
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		101		ns
Turn-off Fall Time	t <sub>f</sub>			95		
Drain-Source Body Diode Character	istics					
Continuous Body Diode Current	I <sub>s</sub>	T 05.00			18	۸
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			72	Α
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = 18\text{A}, V_{GS} = 0\text{V}$			1.4	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> = 0V,I <sub>S</sub> = 18A,		230		ns
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt =100A /μs		1.8		μC

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = 15A,  $V_{DD}$  = 50V,  $R_{G}$  = 25  $\Omega$ , Starting  $T_{J}$  = 25  $^{o}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics ( $T_J = 25^{\circ}C$ )

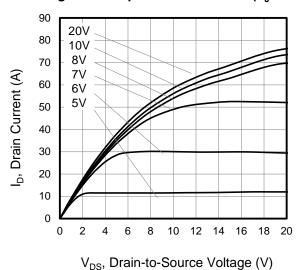


Figure 3. Drain Current vs. Temperature

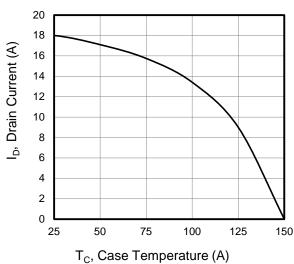


Figure 5. Transfer Characteristics

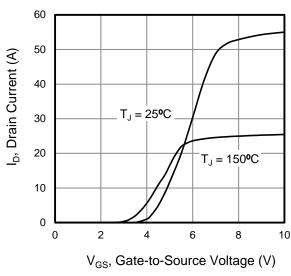


Figure 2. Body Diode Forward Voltage

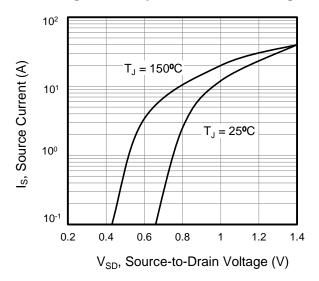


Figure 4.  $BV_{DSS}$  Variation vs. Temperature

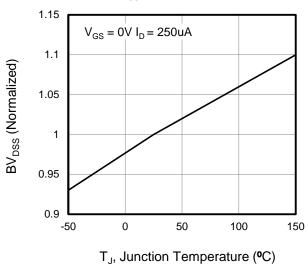
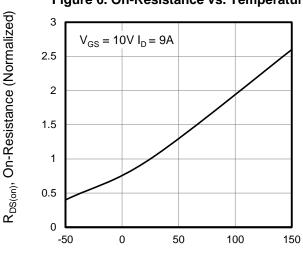


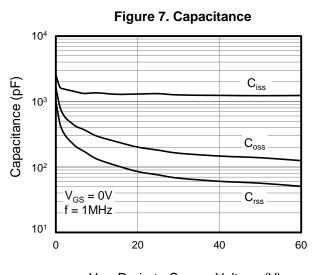
Figure 6. On-Resistance vs. Temperature



T<sub>.I</sub>, Junction Temperature (°C)

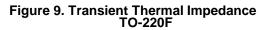


## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted



 $V_{DS}$ , Drain-to-Source Voltage (V)

 $Q_g$ , Total Gate Charge (nC)



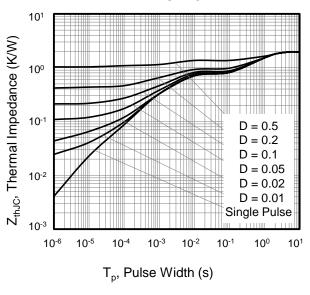


Figure 10. Transient Thermal Impedance TO-220

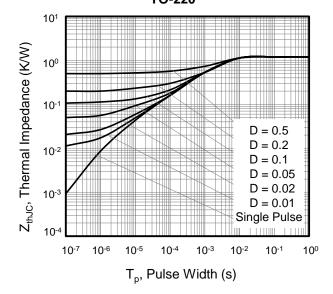




Figure A: Gate Charge Test Circuit and Waveform

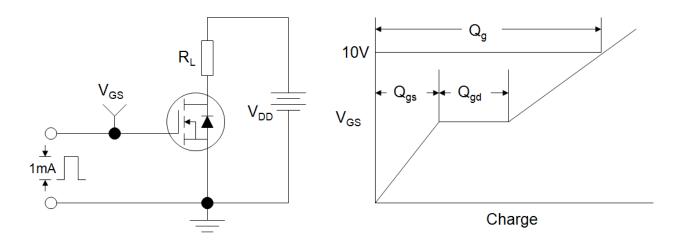


Figure B: Resistive Switching Test Circuit and Waveform

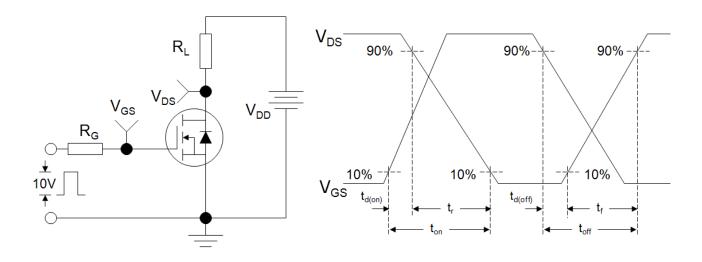
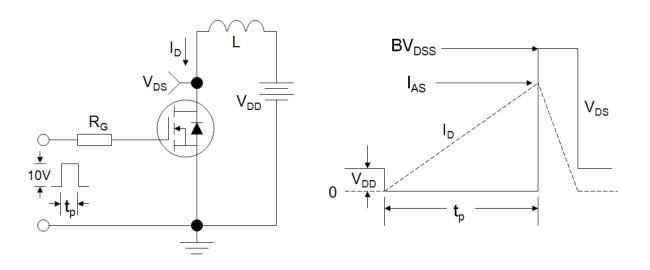


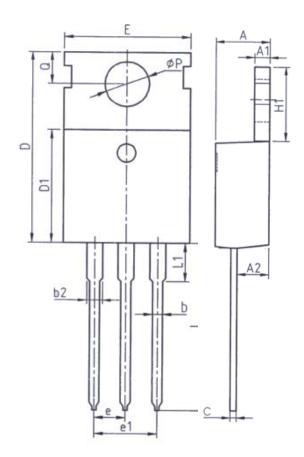
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

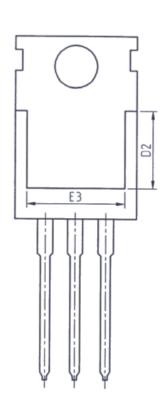


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# **TO-220**

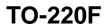


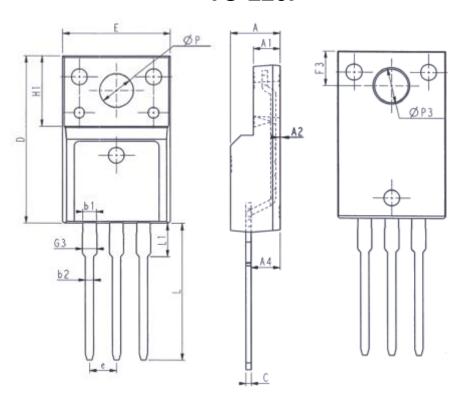


Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
A1	1. 25	1. 45		
A2	2. 20	2. 60		
b	0. 70	0. 95		
b2	1. 17	1. 47		
С	0. 40	0. 65		
D	15. 10	16. 10		
D1	8. 80	9. 40		
D2	5. 50	_		

Unit: mm				
Symbol	Min.	Max.		
E	9. 70	10. 30		
E3	7. 00 -			
е	2. 54BSC			
e1	5. 08BSC			
H1	6. 25	6. 85		
L	12. 75	13.80		
L1	_	3. 40		
Р	3. 40	3. 80		
Q	2. 60 3. 00			







Unit: mm		Unit: mm			
Symbol	Min.	Max.	Symbol Min. Max.		
E	9. 96	10.36	L	12. 68	13. 28
Α	4. 50	4. 90	L1	2. 93	3. 13
A1	2. 34	2. 74	Р	3. 03	3. 38
A2	0. 30	0. 60	Р3	3. 15	3. 65
A4	2. 56	2. 96	F3	3. 15	3. 45
С	0. 40	0. 65	G3	1. 25	1. 55
D	15. 57	16. 17	b1	1. 18	1. 43
H1	6. 70REF		b2	0. 70	0. 95
e	2. 54BSC				



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