

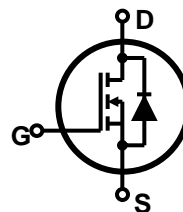
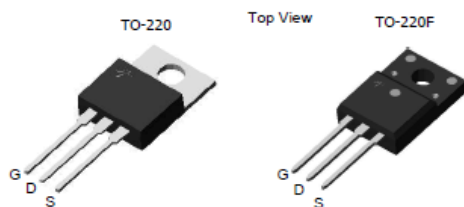
Features

- Low gate charge
- 100% avalanche tested
- Improved dv/dt capability
- RoHS compliant
- Halogen free package
- JEDEC Qualification
- Fast reverse recovery

$$V_{DSS} = 550 \text{ V @ } T_{jmax}$$

$$I_D = 4 \text{ A}$$

$$R_{DS(ON)} = 1.85 \Omega(\text{max}) @ V_{GS} = 10 \text{ V}$$



Device	Package	Marking	Remark
TMP5N50SG / TMPF5N50SG	TO-220 / TO-220F	TMP5N50SG / TMPF5N50SG	Halogen Free

Absolute Maximum Ratings

Parameter	Symbol	TMP5N50SG	TMPF5N50SG	Unit	
Drain-Source Voltage	V_{DSS}	500		V	
Gate-Source Voltage	V_{GS}	±30		V	
Continuous Drain Current	I_D	$T_C = 25 \text{ }^\circ\text{C}$	4 *	A	
		$T_C = 100 \text{ }^\circ\text{C}$	2.8 *	A	
Pulsed Drain Current (Note 1)	I_{DM}	16	16*	A	
Single Pulse Avalanche Energy (Note 2)	E_{AS}	240		mJ	
Repetitive Avalanche Current (Note 1)	I_{AR}	4		A	
Repetitive Avalanche Energy (Note 1)	E_{AR}	9.25		mJ	
Power Dissipation	P_D	$T_C = 25 \text{ }^\circ\text{C}$	92.5	32	W
		Derate above 25 $^\circ\text{C}$	0.74	0.25	W/ $^\circ\text{C}$
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5		V/ns	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150		$^\circ\text{C}$	
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	300		$^\circ\text{C}$	

* Limited only by maximum junction temperature

Thermal Characteristics

Parameter	Symbol	TMP5N50SG	TMPF5N50SG	Unit
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$	1.35	3.9	$^\circ\text{C}/\text{W}$
Maximum Thermal resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics : $T_C=25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test condition	Min	Typ	Max	Units
OFF						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	500	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 500\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 400\text{ V}, T_C = 125^\circ\text{C}$	--	--	10	μA
Forward Gate-Source Leakage Current	I_{GSSF}	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
Reverse Gate-Source Leakage Current	I_{GSSR}	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA

ON

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	1.5	--	3.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 2\text{ A}$	--	1.5	1.85	Ω
Forward Transconductance ^(Note 4)	g_{FS}	$V_{DS} = 30\text{ V}, I_D = 2\text{ A}$	--	8	--	S

DYNAMIC

Input Capacitance	C_{iss}	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	602	--	pF
Output Capacitance	C_{oss}		--	63	--	pF
Reverse Transfer Capacitance	C_{rss}		--	7.1	--	pF

SWITCHING

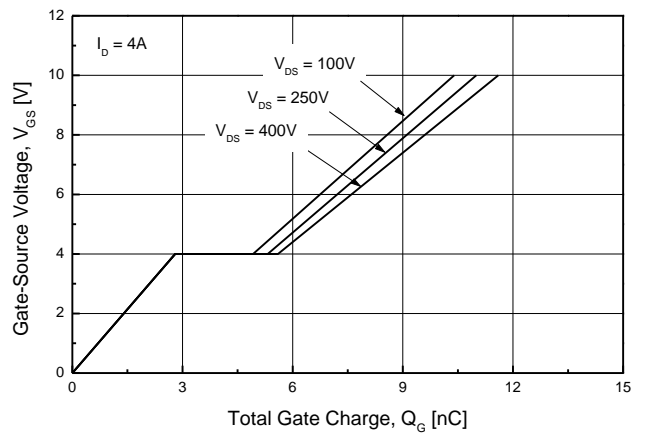
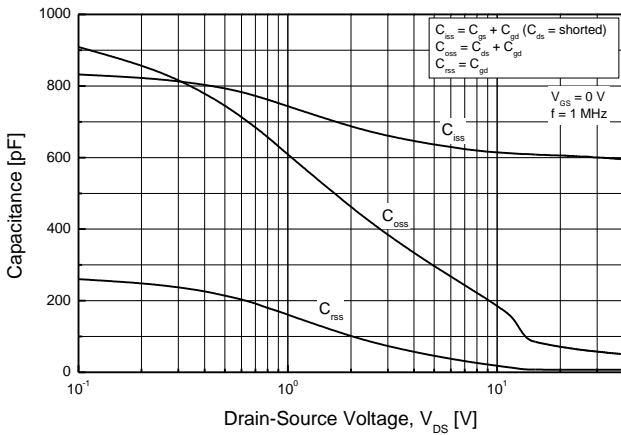
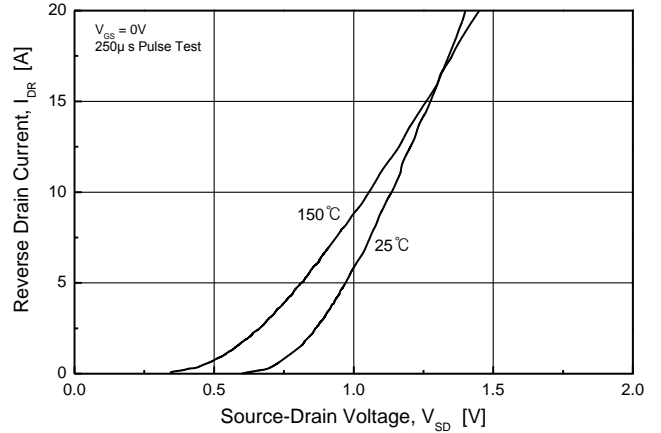
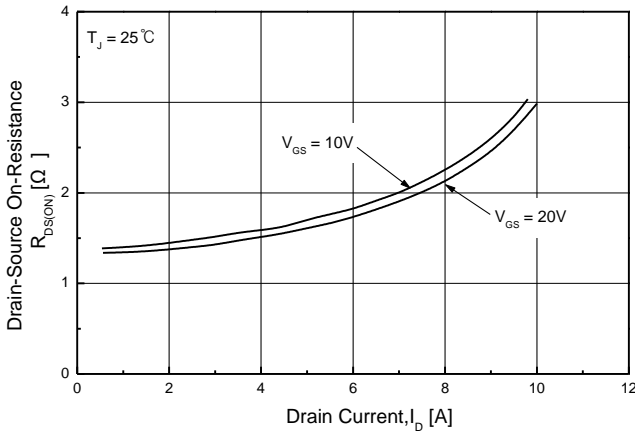
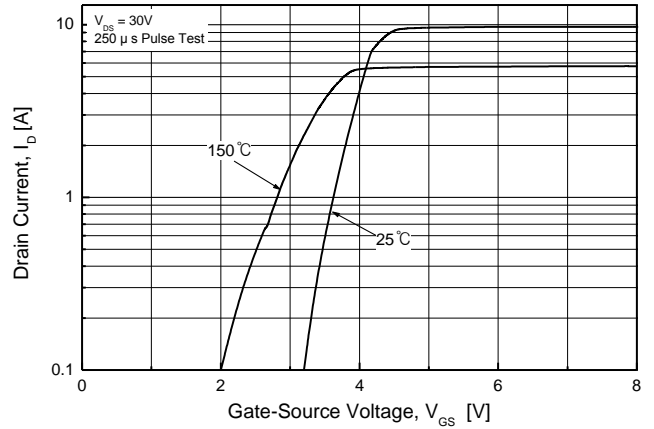
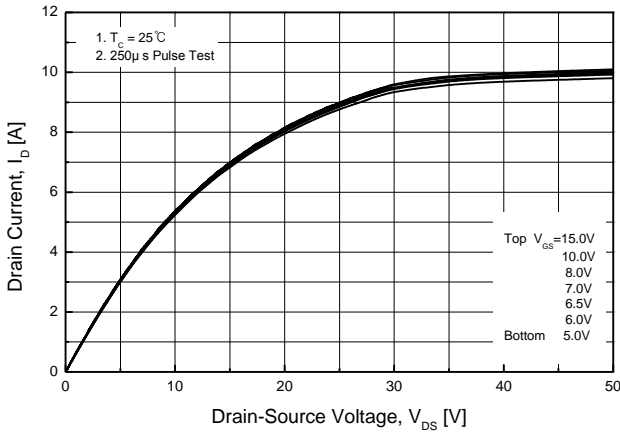
Turn-On Delay Time ^(Note 4,5)	$t_{d(on)}$	$V_{DD} = 250\text{ V}, I_D = 4\text{ A},$ $R_G = 25\ \Omega$	--	40	--	ns
Turn-On Rise Time ^(Note 4,5)	t_r		--	25	--	ns
Turn-Off Delay Time ^(Note 4,5)	$t_{d(off)}$		--	92	--	ns
Turn-Off Fall Time ^(Note 4,5)	t_f		--	35	--	ns
Total Gate Charge ^(Note 4,5)	Q_g	$V_{DS} = 400\text{ V}, I_D = 4\text{ A},$ $V_{GS} = 10\text{ V}$	--	11	--	nC
Gate-Source Charge ^(Note 4,5)	Q_{gs}		--	2.8	--	nC
Gate-Drain Charge ^(Note 4,5)	Q_{gd}		--	2.8	--	nC

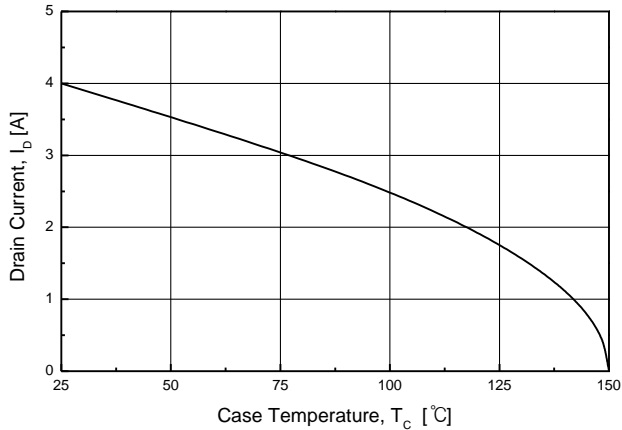
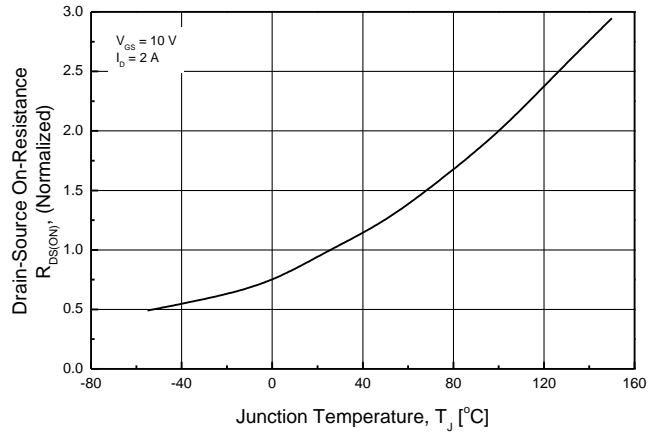
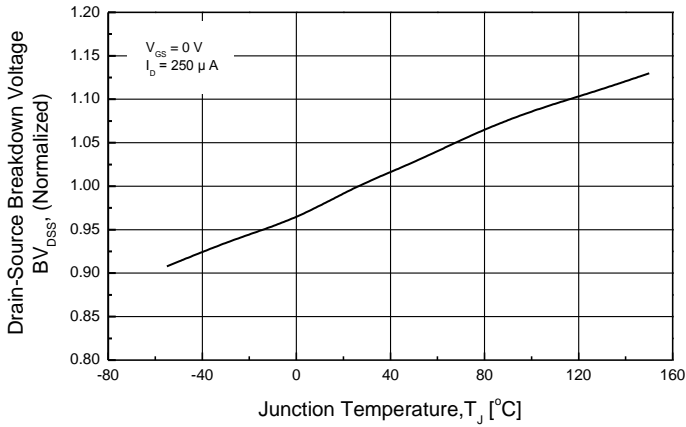
SOURCE DRAIN DIODE

Maximum Continuous Drain-Source Diode Forward Current	I_S	----	--	--	4	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}	----	--	--	16	A
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = 4\text{ A}$	--	--	1.5	V
Reverse Recovery Time ^(Note 4)	t_{rr}	$V_{GS} = 0\text{ V}, I_S = 4\text{ A}$ $dI_f / dt = 100\text{ A}/\mu\text{s}$	--	93	--	ns
Reverse Recovery Charge ^(Note 4)	Q_{rr}		--	192	--	nC

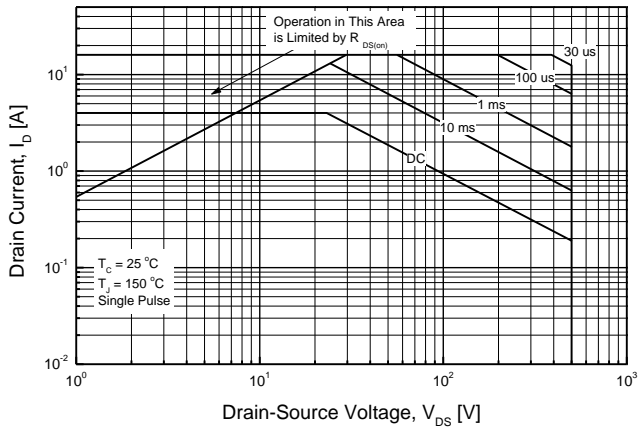
Note :

1. Repeated rating : Pulse width limited by safe operating area
2. $L=27\text{mH}, I_{AS} = 4\text{ A}, V_{DD} = 50\text{ V}, R_G = 25\ \Omega,$ Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 4\text{ A}, di/dt \leq 200\text{ A}/\mu\text{s}, V_{DD} \leq BV_{DS},$ Starting $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse width $\leq 300\ \mu\text{s},$ Duty Cycle $\leq 2\%$
5. Essentially Independent of Operating Temperature Typical Characteristics

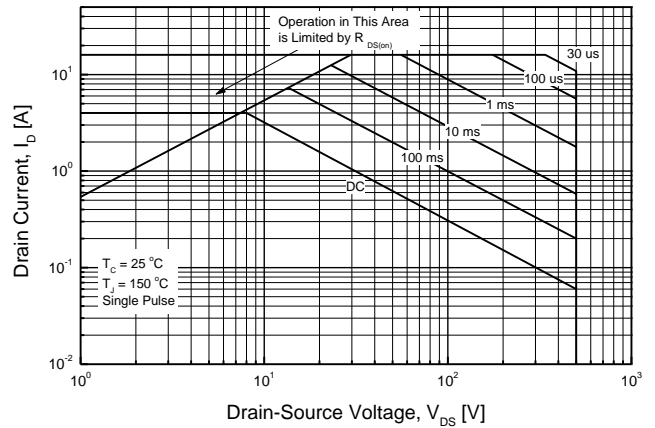




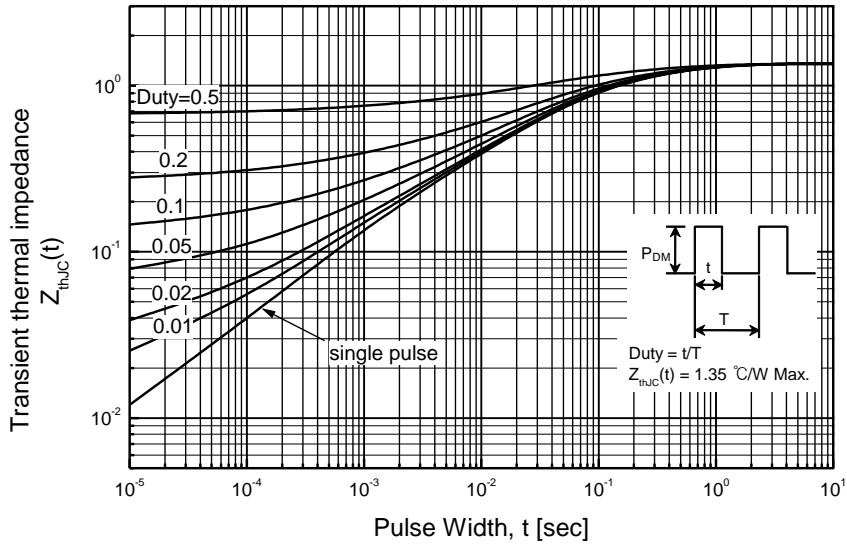
TMP5N50SG



TMPF5N50SG



TMP5N50SG



TMPF5N50SG

