



P-Channel Enhancement-Mode Vertical DMOS Power FETs Quad Array

Ordering Information

$BV_{DSS} /$ BV_{DGS}	$R_{DS(ON)} \text{ Max}$	Order Number / Package
		SOW-20*
-40V	2.0 Ω	TP0604WG

*Same as SO-20 with 300 mil wide body.

Features

- ☐ 4 independent channels
- ☐ 4 electrically isolated die
- ☐ Commercial and Military versions available
- ☐ Freedom from secondary breakdown
- ☐ Low power drive requirement
- ☐ Low C_{iss} and fast switching speeds
- ☐ High input impedance and high gain

Applications

- ☐ Motor control
- ☐ Convertors
- ☐ Amplifiers
- ☐ Switches
- ☐ Power supply circuits
- ☐ Driver (Relays, Hammers, Solenoids, Lamps, Memories, Displays, Bipolar Transistors, etc.)

Thermal Characteristics

Package	Plastic SOW-20
I_D continuous & I_{DR} (single die)	2.0A
I_D pulsed* & I_{DRM} †	0.6A
Power Dissipation @ $T_C = 25^\circ\text{C}$ †	1.5W
θ_{ja} ($^\circ\text{C}/\text{W}$)	85
θ_{jc} ($^\circ\text{C}/\text{W}$)	—

* Pulse test 300 μS pulse, 2% duty cycle.

† Total for package.

Advanced DMOS Technology

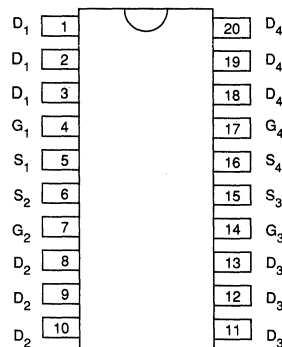
These enhancement-mode (normally-off) power transistors utilize a vertical DMOS structure and Supertex's well-proven silicon-gate manufacturing process. This combination produces devices with the power handling capabilities of bipolar transistors and with the high input impedance and negative temperature coefficient inherent in MOS devices. Characteristic of all MOS structures, these devices are free from thermal runaway and thermally-induced secondary breakdown.

Supertex Vertical DMOS Power FETs are ideally suited to a wide range of switching and amplifying applications where high breakdown voltage, high input impedance, low input capacitance, and fast switching speeds are desired.

Electrical Characteristics

Refer to TP06L Data Sheet for detailed characteristics.

Pin Configuration



top view

SOW-20