

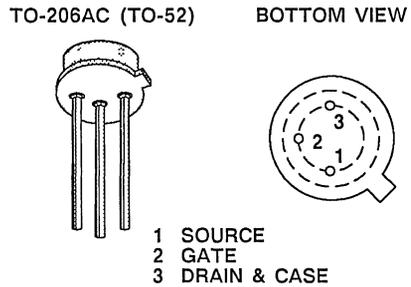
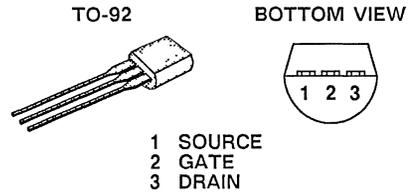
# TP0610 SERIES



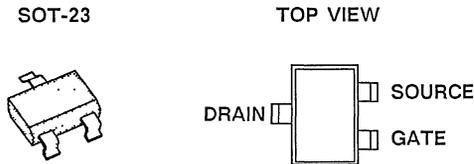
P-Channel Enhancement-Mode MOS Transistors

## PRODUCT SUMMARY

PART NUMBER	$V_{(BR)DSS}$ (V)	$r_{DS(ON)}$ ( $\Omega$ )	$I_D$ (A)	PACKAGE
TP0610E	-60	10	-0.25	TO-206AC
TP0610L	-60	10	-0.18	TO-92
TP0610T	-60	10	-0.12	SOT-23



Performance Curves: VPDS06 (See Section 7)



## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	TP0610E <sup>2</sup>	TP0610L	TP0610T	UNITS	
Drain-Source Voltage	$V_{DS}$	-60	-60	-60	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 30$	$\pm 30$		
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	-0.25	-0.18	-0.12	A
	$T_A = 100^\circ\text{C}$		-0.15	-0.11	-0.07	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	-1	-0.8	-0.4	
Power Dissipation	$T_A = 25^\circ\text{C}$	$P_D$	1.5	0.80	0.36	W
	$T_A = 100^\circ\text{C}$		0.60	0.32	0.14	
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55 to 150			$^\circ\text{C}$	
Lead Temperature (1/16" from case for 10 seconds)	$T_L$	300				

## THERMAL RESISTANCE

THERMAL RESISTANCE	SYMBOL	TP0610E	TP0610L	TP0610T	UNITS
Junction-to-Ambient	$R_{thJA}$	400	156	350	$^\circ\text{C}/\text{W}$

<sup>1</sup>Pulse width limited by maximum junction temperature

<sup>2</sup>Reference  $T_C$  for all temperature testing

ELECTRICAL CHARACTERISTICS <sup>1</sup>				LIMITS				
PARAMETER	SYMBOL	TEST CONDITIONS	TYP <sup>2</sup>	TP0610E		TP0610L		UNIT
				MIN	MAX	MIN	MAX	
<b>STATIC</b>								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = -10 \mu\text{A}$	-70	-60		-60		V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -1 \text{ mA}$	-1.7	-1	-2.4	-1	-2.4	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ $V_{GS} = \pm 20 \text{ V}$ $T_J = 125^\circ\text{C}$	$\pm 1$ $\pm 5$		$\pm 10$ $\pm 50$		$\pm 10$ $\pm 50$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -48 \text{ V}$ $V_{GS} = 0 \text{ V}$ $T_J = 125^\circ\text{C}$	-0.02 -0.2		-1		-1	$\mu\text{A}$
On-State Drain Current <sup>3</sup>	$I_{D(ON)}$	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}$	-80	-50		-50		mA
Drain-Source On-Resistance <sup>3</sup>	$r_{DS(ON)}$	$V_{GS} = -4.5 \text{ V}, I_D = -25 \text{ mA}$	11		25		25	$\Omega$
		$V_{GS} = -10 \text{ V}$ $I_D = -0.5 \text{ A}$ $T_J = 125^\circ\text{C}$	8		10		10	
			15		20		20	
Forward Transconductance <sup>3</sup>	$g_{FS}$	$V_{DS} = -10 \text{ V}, I_D = -0.5 \text{ A}$	135	80		80		mS
Common Source Output Conductance <sup>3</sup>	$g_{OS}$	$V_{DS} = -10 \text{ V}, I_D = -0.1 \text{ A}$	400					$\mu\text{S}$
<b>DYNAMIC</b>								
Input Capacitance	$C_{iss}$	$V_{DS} = -25 \text{ V}$ $V_{GS} = 0 \text{ V}$ $f = 1 \text{ MHz}$	15		60		60	$\mu\text{F}$
Output Capacitance	$C_{oss}$		10		25		25	
Reverse Transfer Capacitance	$C_{rss}$		3		5		5	
<b>SWITCHING</b>								
Turn-On Time	$t_{d(ON)}$	$V_{DD} = -25 \text{ V}, R_L = 133 \Omega$ $I_D = -0.18 \text{ A}, V_{GEN} = -10 \text{ V}$ $R_G = 25 \Omega$  (Switching time is essentially independent of operating temperature)	6		10		10	ns
	$t_r$		10		15		15	
Turn-Off Time	$t_{d(OFF)}$		7		15		15	
	$t_f$		8		20		20	

- NOTES: 1.  $T_A = 25^\circ\text{C}$  unless otherwise noted,  $T_C = 25^\circ\text{C}$  for TP0610E.  
2. For design aid only, not subject to production testing.  
3. Pulse test;  $PW = 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .

# TP0610 SERIES



ELECTRICAL CHARACTERISTICS <sup>1</sup>				LIMITS		
PARAMETER	SYMBOL	TEST CONDITIONS	TYP <sup>2</sup>	TP0610T		UNIT
				MIN	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = -10\ \mu\text{A}$	-70	-60		V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -1\text{ mA}$	-1.7	-1	-2.4	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}$ $V_{GS} = \pm 20\text{ V}$ $T_J = 125^\circ\text{C}$	$\pm 1$		$\pm 10$	nA
			$\pm 5$		$\pm 50$	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -48\text{ V}$ $V_{GS} = 0\text{ V}$ $T_J = 125^\circ\text{C}$	-0.02		-1	$\mu\text{A}$
			-0.2		-200	
On-State Drain Current <sup>3</sup>	$I_{D(ON)}$	$V_{DS} = -10\text{ V}, V_{GS} = -4.5\text{ V}$	-80	-50		mA
Drain-Source On-Resistance <sup>3</sup>	$r_{DS(ON)}$	$V_{GS} = -4.5\text{ V}, I_D = -25\text{ mA}$ $V_{GS} = -10\text{ V}$ $I_D = -0.2\text{ A}$ $T_J = 125^\circ\text{C}$	11		25	$\Omega$
			6		10	
			12		20	
Forward Transconductance <sup>3</sup>	$g_{FS}$	$V_{DS} = -10\text{ V}, I_D = -0.1\text{ A}$	90	60		mS
Common Source Output Conductance <sup>3</sup>	$g_{OS}$		400			$\mu\text{S}$
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = -25\text{ V}$ $V_{GS} = 0\text{ V}$ $f = 1\text{ MHz}$	15		60	pF
Output Capacitance	$C_{oss}$		10		25	
Reverse Transfer Capacitance	$C_{rss}$		3		5	
<b>SWITCHING</b>						
Turn-On Delay Time	$t_{d(ON)}$	$V_{DD} = -25\text{ V}, R_L = 133\ \Omega$ $I_D = -0.18\text{ A}, V_{GEN} = -10\text{ V}$ $R_G = 25\ \Omega$  (Switching time is essentially independent of operating temperature)	6		10	ns
	$t_r$		10		15	
Turn-Off Delay Time	$t_{d(OFF)}$		7		15	
	$t_f$		8		20	

- NOTES: 1.  $T_A = 25^\circ\text{C}$  unless otherwise noted.  
 2. For design aid only, not subject to production testing.  
 3. Pulse test;  $PW = 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ .