



**TP909FC-900-25 Reverse-conducting Thyristor**

**2500 V<sub>DRM</sub>; 1418A rms**

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**RCT FOR INVERTER AND CHOPPER APPLICATIONS**

**Features:**

- . All Diffused Structure
- . Interdigitated Amplifying Gate Configuration
- . Blocking capability up to 2500 volts
- . Guaranteed Maximum Turn-Off Time
- . High dV/dt Capability
- . Pressure Assembled Device

**ELECTRICAL CHARACTERISTICS AND RATINGS**

**Blocking - Off State**

Device Type	V <sub>DRM</sub> (1)	V <sub>DSM</sub> (1)
TP909FC-900-20	2000	2000
TP909FC-900-22	2200	2200
TP909FC-900-25	2500	2500

V<sub>DRM</sub> = Repetitive peak off state voltage

Notes:

All ratings are specified for T<sub>j</sub>=25 °C unless otherwise stated.

- (1) All voltage ratings are specified for an applied 50Hz/60Hz sinusoidal waveform over the temperature range -40 to +125 °C.
- (2) 10 msec. max. pulse width
- (3) Maximum value for T<sub>j</sub> = 125 °C.
- (4) Minimum value for linear and exponential waveshape to 80% rated V<sub>DRM</sub>. Gate open. T<sub>j</sub> = 125 °C.
- (5) Non-repetitive value.

Repetitive peak off state leakage	I <sub>DRM</sub>	20 mA 80mA (3)
Critical rate of voltage rise	dV/dt (4)	1000 V/μsec

**Conducting - on state**

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
RMS value of on-state current	I <sub>TRMS</sub>		1418		A	Nominal value
Average on-state current	I <sub>T(AV)</sub>		903		A	Continuous single-phase, half sine wave, 180° conduction
Peak one cycle surge (non repetitive) current	I <sub>TSM</sub>		14000		A	10 msec (60Hz), sinusoidal wave-shape, 180° conduction, T <sub>j</sub> = 125 °C
I square t	I <sup>2</sup> t		9.8.x10 <sup>5</sup>		A <sup>2</sup> s	10.0 msec
Peak on-state voltage	V <sub>TM</sub>		3.3		V	I <sub>TM</sub> =3000A
Critical rate of rise of on-state current	di/dt		400		A/μs	V <sub>D</sub> =1/2V <sub>DRM</sub> , I <sub>TM</sub> =800A f=60Hz I <sub>GM</sub> =1.5A, di <sub>G</sub> /dt=1.0A/us, T <sub>j</sub> =125 °C
RMS reverse current	I <sub>R(RMS)</sub>		630		A	Nominal value
Average reverse current	I <sub>R(AV)</sub>		400		A	Continuous single-phase, half sine wave, 180° conduction
Peak one cycle surge (non repetitive) current	I <sub>FSM</sub>			6.5	KA	10 msec, sinusoidal wave-shape, T <sub>j</sub> = 125 °C
I <sub>R</sub> square t	I <sub>R</sub> <sup>2</sup> t		1.8.x10 <sup>5</sup>		A <sup>2</sup> s	10.0 msec
Peak reverse voltage	V <sub>RM</sub>		2.6		V	I <sub>RM</sub> =1000A

## ELECTRICAL CHARACTERISTICS AND RATINGS (cont.)

### Gating

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Peak gate power dissipation	$P_{GM}$		16		W	$t_p = 40 \text{ us}$
Average gate power dissipation	$P_{G(AV)}$		5		W	
Peak gate current	$I_{GM}$		10		A	
Gate current required to trigger all units	$I_{GT}$		350		mA	$V_D = 6 \text{ V}; R_L = 2 \text{ ohms}; T_j = +25 \text{ }^\circ\text{C}$
Gate voltage required to trigger all units	$V_{GT}$		2.5		V	$V_D = 6 \text{ V}; R_L = 2 \text{ ohms}; T_j = 25^\circ\text{C}$
Peak non- trigger voltage	$V_{GD}$		0.2		V	$T_j = 125 \text{ }^\circ\text{C}; V_D = 1/2 V_{DRM}$

### Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Turn-off time	$t_q$	32	40	<b>40</b>	$\mu\text{s}$	$I_{TM} = 900 \text{ A}; di_1/dt = -50 \text{ A}/\mu\text{s}; di_2/dt = 50 \text{ A}/\mu\text{s}; I_{RM} = 500 \text{ A}; dV/dt = 50 \text{ V}/\mu\text{s}; V_{DR} = 2/3 V_{DRM}; T_j = 125 \text{ }^\circ\text{C}; t_w = 60 \mu\text{s}$

\* For guaranteed max. value, contact factory.

## THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

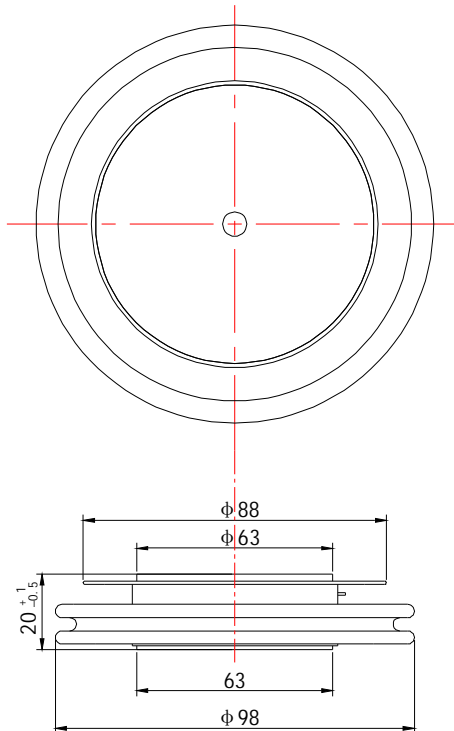
Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	$T_j$	-40	+125		$^\circ\text{C}$	
Storage temperature	$T_{stg}$	-40	+125		$^\circ\text{C}$	
Thyristor part thermal resistance - junction to case	$R_{\Theta I(j-c)}$		0.022		$^\circ\text{C}/\text{W}$	Double sided cooled
Diode part thermal resistance – junction to case	$R_{\Theta II(j-c)}$		0.050		$^\circ\text{C}/\text{W}$	Double sided cooled
Mounting force	F		30		kN	
Weight	W		700		g	

\* Mounting surfaces smooth, flat and greased

Note : for case outline and dimensions, see case outline drawing in page 4 of this Technical Data

CASE OUTLINE AND DIMENSIONS.

Reverse-conducting Thyristor



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