



600V Super-Junction Power MOSFET

DESCRIPTION

600V super-junction Power MOSFET

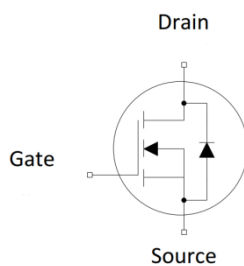
Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The SJ MOSFET is a price-performance optimized product enabling to target cost sensitive applications in Consumer and Lighting markets, designed by Wuxi Unigroup Microelectronics Company.

FEATURES

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- RoHS compliant

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information

Device	Package	Marking
TPA60R330M	TO-220F	60R330M

Key Performance Parameters

Parameter	Value	Unit
$V_{DS} @ T_{j,max}$	600	V
$R_{DS(on),max}$	0.33	Ω
I_D	11	A
$Q_{g,typ}$	19	nC
I_{DM}	33	A



Absolute Maximum Ratings $T_C = 25^{\circ}\text{C}$, unless otherwise noted			
Parameter	Symbol	Value	Unit
		TO-220F	
Drain-Source Voltage ($V_{GS} = 0\text{V}$)	V_{DSS}	600	V
Continuous Drain Current	I_D	$T_C = 25^{\circ}\text{C}$ 11	A
		$T_C = 100^{\circ}\text{C}$ 6.6	
Pulsed Drain Current (note1)	I_{DM}	33	A
Gate-Source Voltage	V_{GSS}	± 30	V
Single Pulse Avalanche Energy (note2)	E_{AS}	210	mJ
Repetitive Avalanche Energy (note2)	E_{AR}	0.32	mJ
Avalanche Current	I_{AR}	1.8	A
MOSFET dv/dt ruggedness, $V_{DS} = 0 \dots 480\text{V}$	dv/dt	50	V/ns
Power Dissipation	P_D	31	W
Continuous Body Diode Current	I_S	9.4	A
Pulsed Diode Forward Current (note1)	I_{SM}	33	
Reverse diode dv/dt (note3)	dv/dt	15	V/ns
Maximum diode commutation speed (note3)	di/dt	500	A/us
Operating Junction and Storage Temperature Range	T_J, T_{stg}	$-55 \sim +150$	$^{\circ}\text{C}$

Thermal Resistance			
Parameter	Symbol	Value	Unit
		TO-220F	
Thermal Resistance, Junction-to-Case	R_{thJC}	4	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	80	



Specifications T _J = 25°C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	600	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 600V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
		V _{DS} = 600V, V _{GS} = 0V, T _J = 150°C	--	--	100	
Gate-Source Leakage	I _{GSS}	V _{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.5	--	4.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 5.5A	--	0.29	0.33	Ω
Gate resistance	R _G	f = 1.0MHz open drain	--	18	--	Ω
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 100V, f = 1.0MHz	--	1021	--	pF
Output Capacitance	C _{oss}		--	43	--	
Reverse Transfer Capacitance	C _{rss}		--	6	--	
Total Gate Charge	Q _g	V _{DD} = 480V, I _D = 11A, V _{GS} = 10V	--	19.0	--	nC
Gate-Source Charge	Q _{gs}		--	4.8	--	
Gate-Drain Charge	Q _{gd}		--	7.2	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 400V, I _D = 11A, R _G = 25Ω	--	28	--	ns
Turn-on Rise Time	t _r		--	61	--	
Turn-off Delay Time	t _{d(off)}		--	89	--	
Turn-off Fall Time	t _f		--	41	--	
Drain-Source Body Diode Characteristics						
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 5.5A, V _{GS} = 0V	--	0.9	1.2	V
Reverse Recovery Time	t _{rr}	V _R = 400V, I _F = I _S , di _F /dt = 100A/μs	--	377	--	ns
Reverse Recovery Charge	Q _{rr}		--	3.4	--	μC
Peak Reverse Recovery Current	I _{rrm}		--	17.8	--	A

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = 1.8A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$
3. Identical low side and high side switch with identical R_G



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

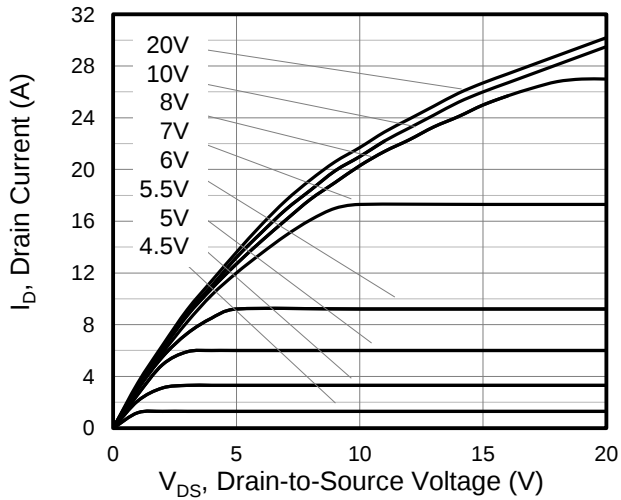


Figure 2. Transfer Characteristics

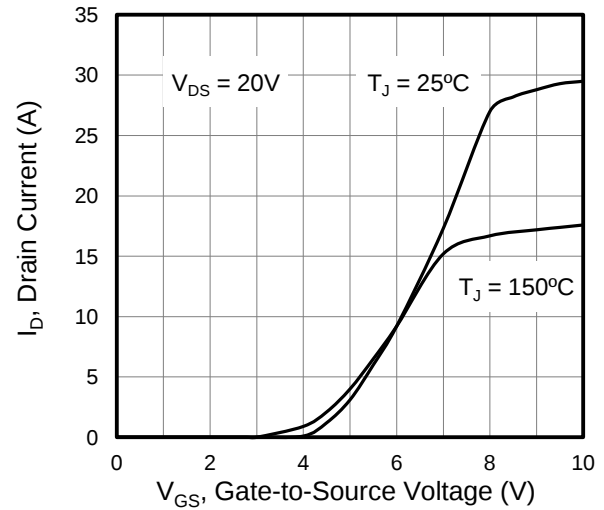


Figure 3. On-Resistance vs. Drain Current

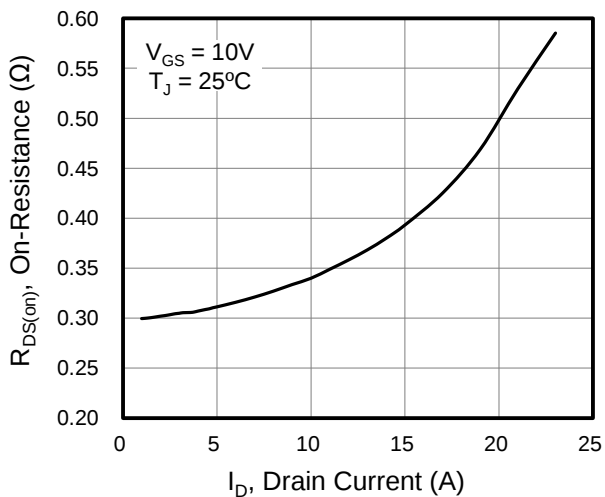


Figure 4. Capacitance

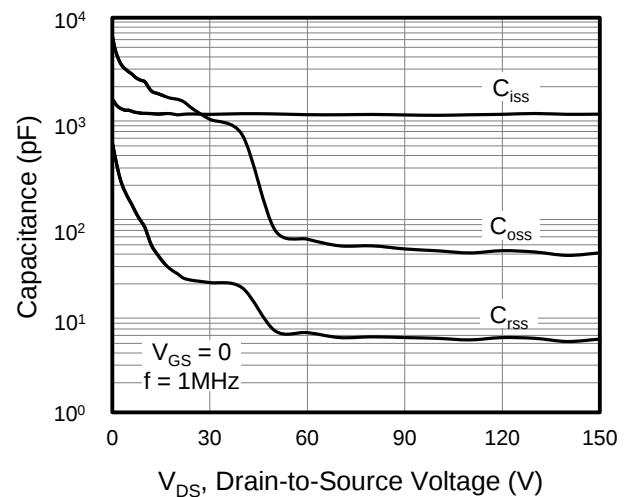


Figure 5. Gate Charge

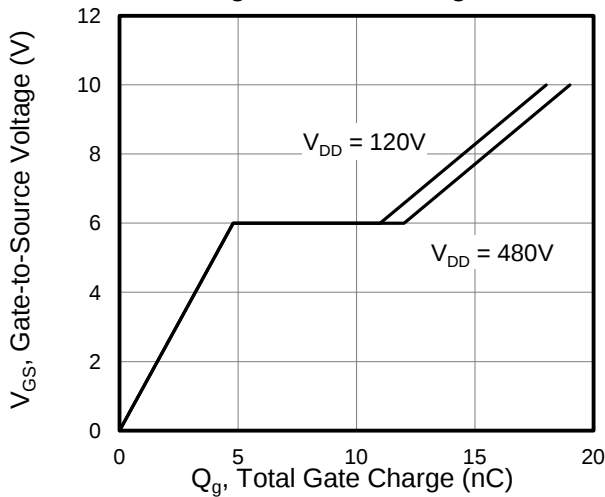
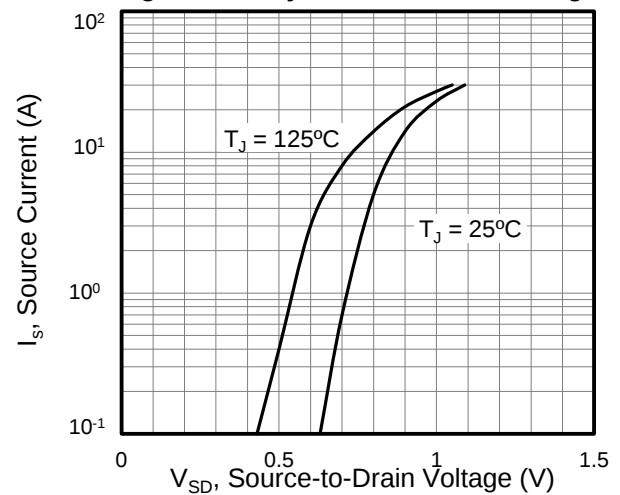


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. On-Resistance vs. Junction Temperature

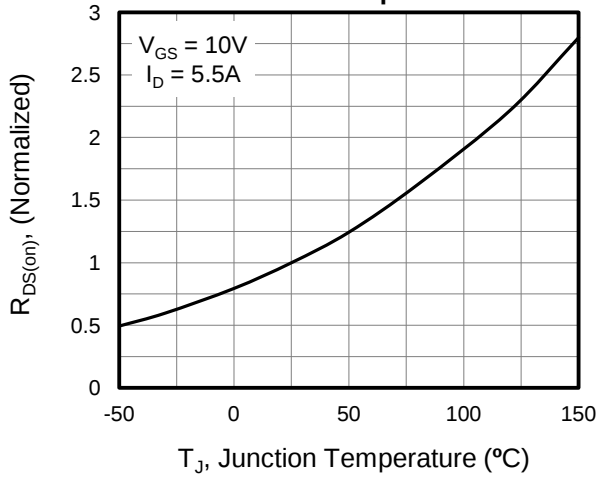


Figure 8. Breakdown voltage vs. Junction Temperature

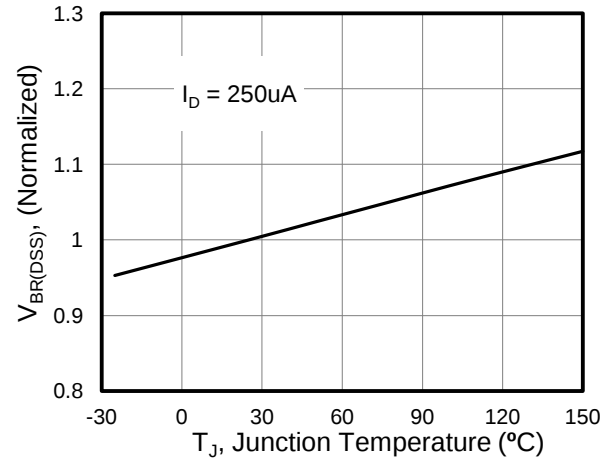


Figure 9. Transient Thermal Impedance TO-220F

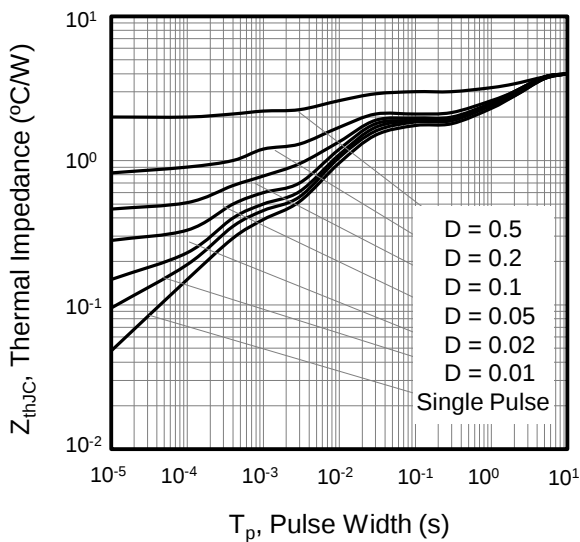


Figure 10. Safe operation area for TO-220F

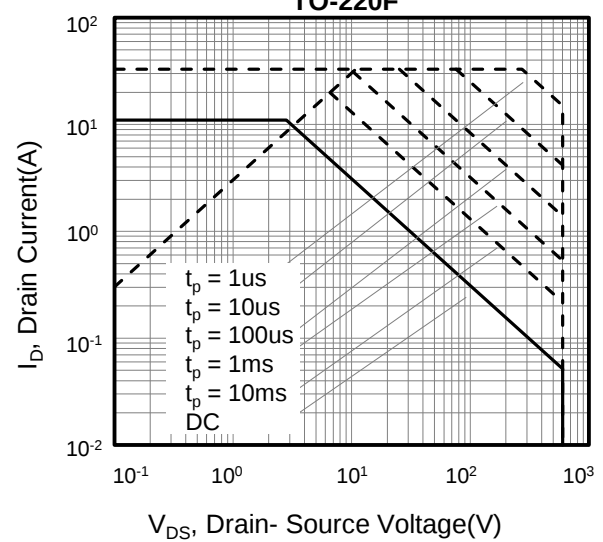




Figure A: Gate Charge Test Circuit and Waveform

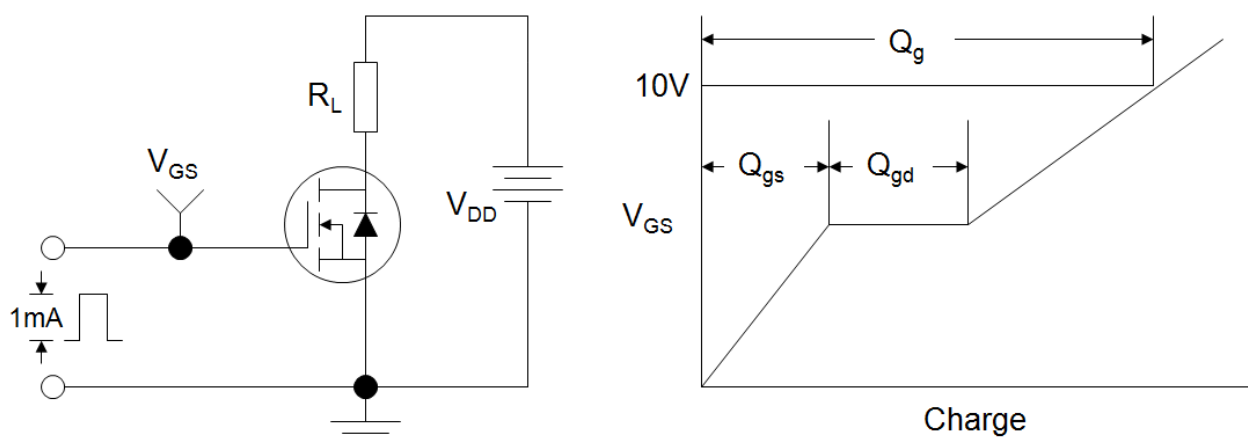


Figure B: Resistive Switching Test Circuit and Waveform

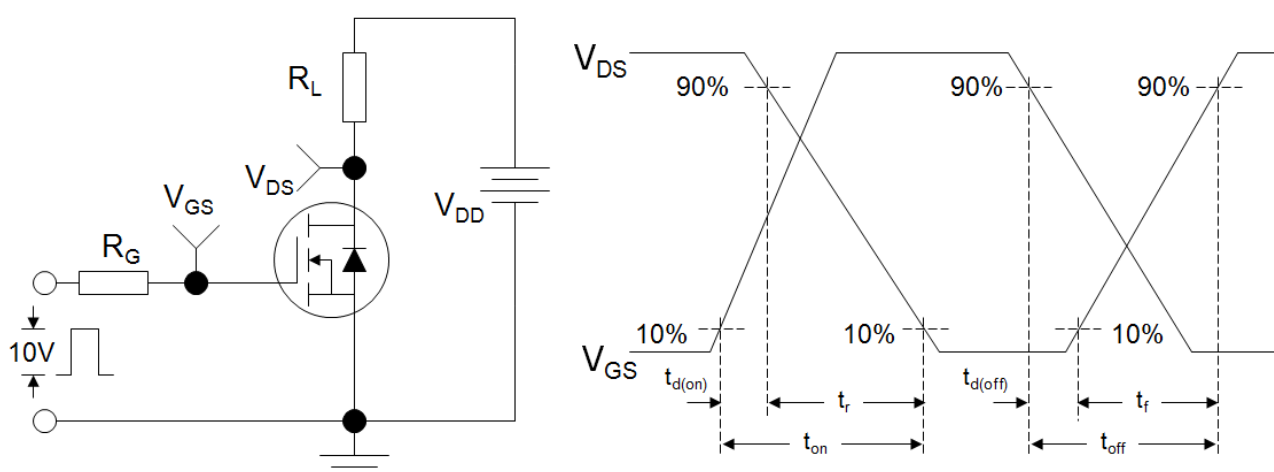
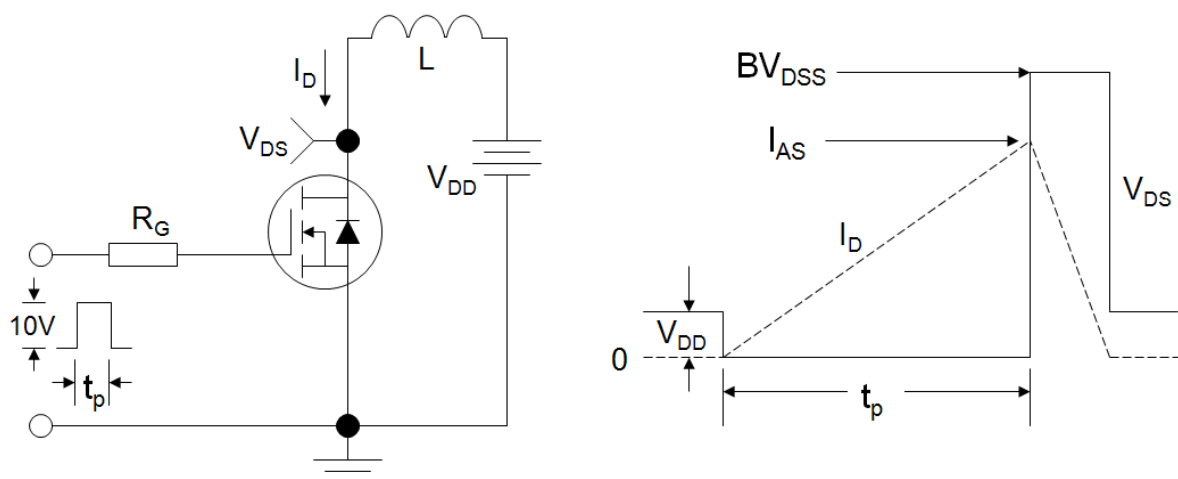
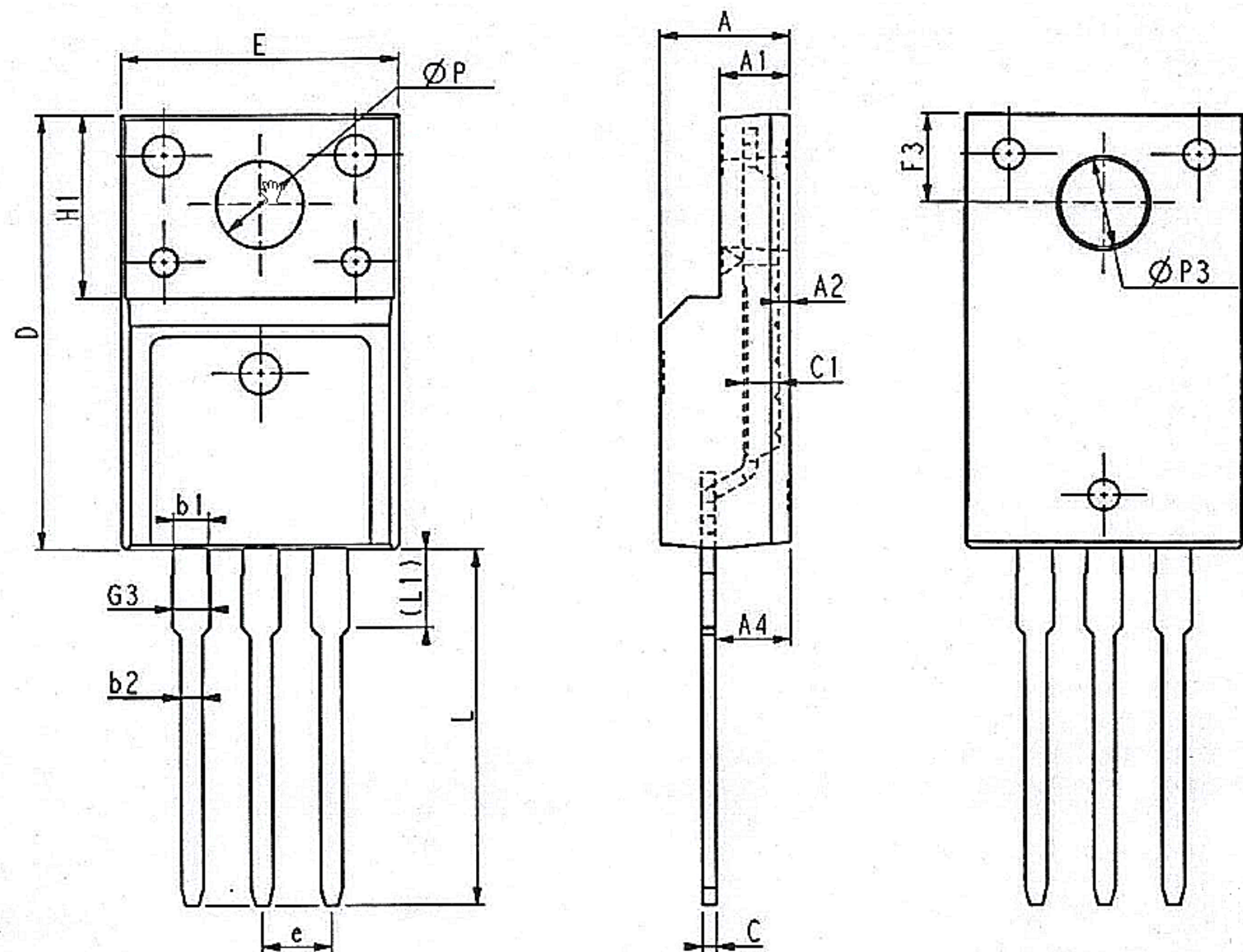


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F



Unit:mm			
Symbol	Min.	Nom	Max.
E	9.96	10.16	10.36
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A2	0.30	0.45	0.60
A4	2.56	2.76	2.96
c	0.40	0.50	0.65
c1	1.20	1.30	1.35
D	15.57	15.87	16.17
H1	6.70REF		

Unit:mm			
Symbol	Min.	Nom	Max.
e	2.54BSC		
L	12.68	12.98	13.28
L1	2.88	3.03	3.18
ΦP	3.03	3.18	3.38
ΦP3	3.15	3.45	3.65
F3	3.15	3.30	3.45
G3	1.25	1.35	1.55
b1	1.18	1.28	1.43
b2	0.70	0.80	0.95



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