TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSII)

TPC6001

Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: RDS (ON) = 22 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 15 S$ (typ.)
- Low leakage current: $IDSS = 10 \mu A (max) (VDS = 20 V)$
- Enhancement mode: V_{th} = 0.5 to 1.2 V (V_{DS} = 10 V, I_{D} = 200 μA)

Maximum Ratings (Ta = 25°C)

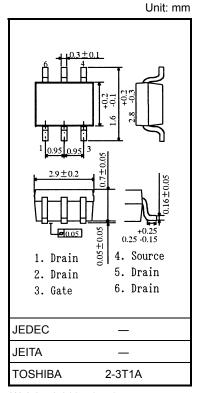
Characteris	tics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	20	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	20	٧	
Gate-source voltage		V _{GSS}	±12	V	
Drain current	DC (Note 1)	I _D	6	Α	
Dialii Cuiteiit	Pulse (Note 1)	I _{DP}	24		
Drain power dissipation	(t = 5 s) (Note 2a)	P_{D}	2.2	W	
Drain power dissipation (t = 5 s) (Note 2b)		P _D	0.7	W	
Single pulse avalanche	energy (Note 3)	E _{AS}	5.8	mJ	
Avalanche current		I _{AR}	3	Α	
Repetitive avalanche e	nergy (Note 4)	E _{AR}	0.22	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	inge	T _{stg}	-55 to 150	°C	

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2a)	R _{th (ch-a)}	56.8	°C/W
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2b)	R _{th (ch-a)}	178.5	°C/W

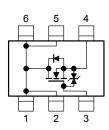
Note 1, Note 2, Note 3, Note 4 and Note 5: See the next page.

This transistor is an electrostatic-sensitive device. Please handle it with caution.

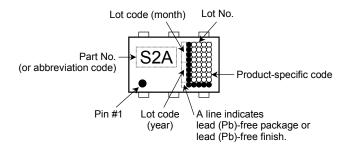


Weight: 0.011 g (typ.)

Circuit Configuration



Marking (Note 5)



Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА	
Drain cut-OFF cu	Orain cut-OFF current		V _{DS} = 20 V, V _{GS} = 0 V	_	_	10	μА	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	20	_	_	· V	
		V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -12 \text{ V}$	8	_	_		
Gate threshold vo	Gate threshold voltage		$V_{DS}=10~V,~I_D=200~\mu A$	0.5	_	1.2	V	
Drain-source ON resistance		R _{DS (ON)}	$V_{GS} = 2.0 \text{ V}, I_D = 3 \text{ A}$	_	35	60		
		R _{DS (ON)}	$V_{GS} = 2.5 \text{ V}, I_D = 3 \text{ A}$	_	28	45	mΩ	
		R _{DS (ON)}	$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$	_	22	30		
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 3 A	7.5	15	_	S	
Input capacitance		C _{iss}		_	755	_	pF	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	172	_		
Output capacitance		Coss]	_	222	_		
Switching time	Rise time	t _r	5 V □ I _D = 3 A	_	6	_	- ns	
	Turn-ON time	t _{on}	V _{GS} 5 V I _D = 3 A C C C	_	11	_		
	Fall time	t _f	R = 3.35	_	32	_		
	Turn-OFF time	t _{off}	$V_{DD} \simeq 10 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	64	_		
Total gate charge (gate-source plus gate-drain)		Qg		_	15		nC	
Gate-source charge		Q _{gs}	$V_{DD} \simeq 16 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 6 \text{ A}$		10			
Gate-drain ("miller") charge		Q _{gd}]	_	5	_		

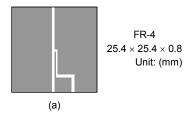
Source-Drain Ratings and Characteristics (Ta = 25°C)

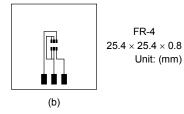
Characteristics		ymbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current (Not	։ 1) հլ	DRP		_		24	Α
Forward voltage (diode)	V	_{DSF}	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V}$	_		-1.2	V

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

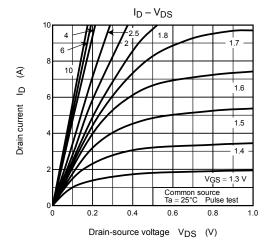


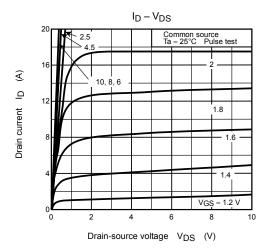


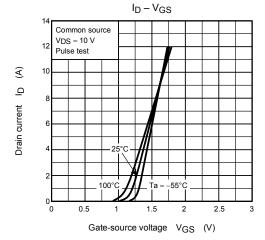
Note 3: V_{DD} = 16 V, T_{ch} = 25°C (initial), L = 0.5 mH, R_G = 25 Ω , I_{AR} = 3.0 A

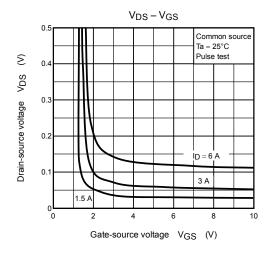
Note 4: Repetitive rating: pulse width limited by maximum channel temperature

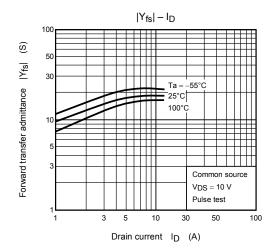
Note 5: . • on lower left of the marking indicates Pin 1.

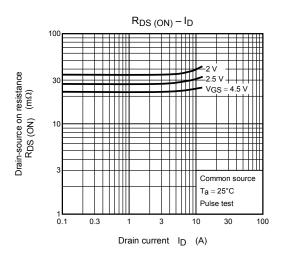


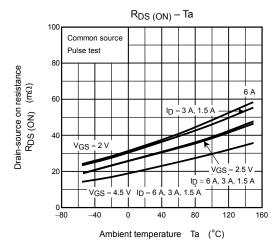


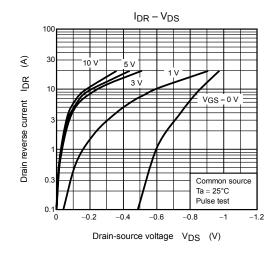


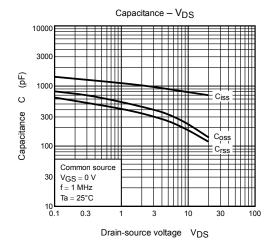


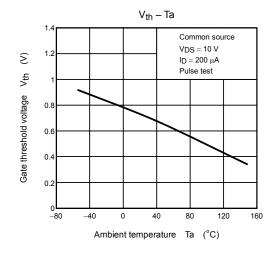


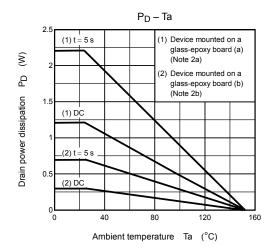


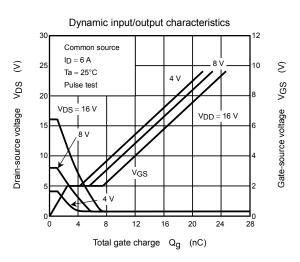


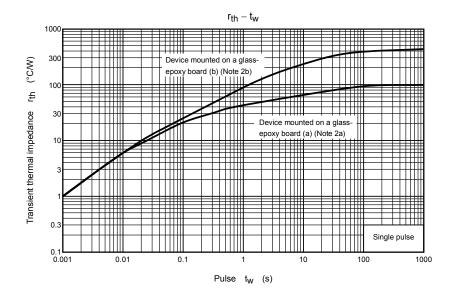


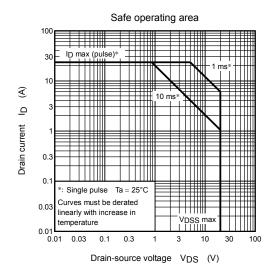












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Handbook" etc..

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