TOSHIBA Transistor Silicon NPN Epitaxial Type

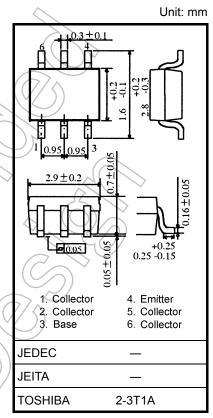
TPC6502

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: h_{FE} = 400 to 1000 (I_{C} = 0.3 A)
- Low collector-emitter saturation voltage: V_{CE (sat)} = 0.14 V (max)
- High-speed switching: t_f = 120 ns (typ.)

Absolute Maximum Ratings (Ta = 25°C)

			IIII	
teristic	Symbol	Rating	Unit	
age	V_{CBO}	100	À	
oltage	V _{CEX}	80	> v	
oltage	V _{CEO}	50	V	
e	V_{EBO}	7	٧	
DC	lc <	3.0	A	
Pulse	ICP	5.0		
	I _B (300	mA	
DC	D (Not64)	0.8	∖ w	
t = 10 s	PC (Note 1)	1.6	///	
re	7)\1	150	°C	
e range	T _{stg}	-55 to 150	°C	
	age DC Pulse DC t = 10 s	DC	VCBO	



Weight: 0.01 g (typ.)

- Note 1: Mounted on an FR4 board (glass-epoxy; 1.6 mm thick; Cu area, 645 mm²)
- Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff curre	nt	I _{CBO}	V _{CB} = 100 V, I _E = 0	_	_	100	nA
Emitter cutoff current		I _{EBO}	$V_{EB} = 7 \text{ V, } I_{C} = 0$	_	_	100	nA
Collector-emitter brea	akdown voltage	V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	50	_	_	V
DC current gain —		h _{FE} (1)	$V_{CE} = 2 \text{ V}, I_{C} = 0.3 \text{ A}$	400		1000	
		h _{FE} (2)	V _{CE} = 2 V, I _C = 1 A	200) /_	_	
Collector-emitter satu	ıration voltage	V _{CE} (sat)	I _C = 1 A, I _B = 20 mA		_	0.14	V
Base-emitter saturati	on voltage	V _{BE (sat)}	I _C = 1 A, I _B = 20 mA	\rightarrow	_	1.10	V
Collector output capacitance		C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz		13	_	pF
Switching time	Rise time	t _r	See Figure 1 circuit diagram.	_	40	_	
	Storage time	t _{stg}	$V_{CC} \approx 30 \text{ V}, R_L = 30 \Omega$	_	500	_	ns
	Fall time	t _f	$I_{B1} = 33.3 \text{ mA}, I_{B2} = 33.3 \text{ mA}$		120	\searrow	

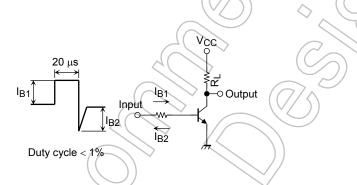
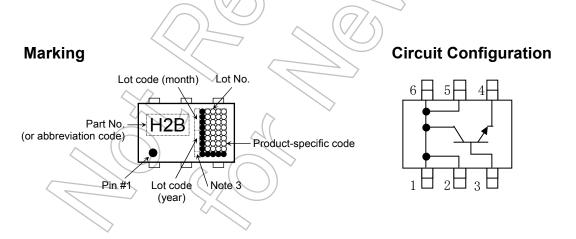


Figure 1. Switching Time Test Circuit & Timing Chart



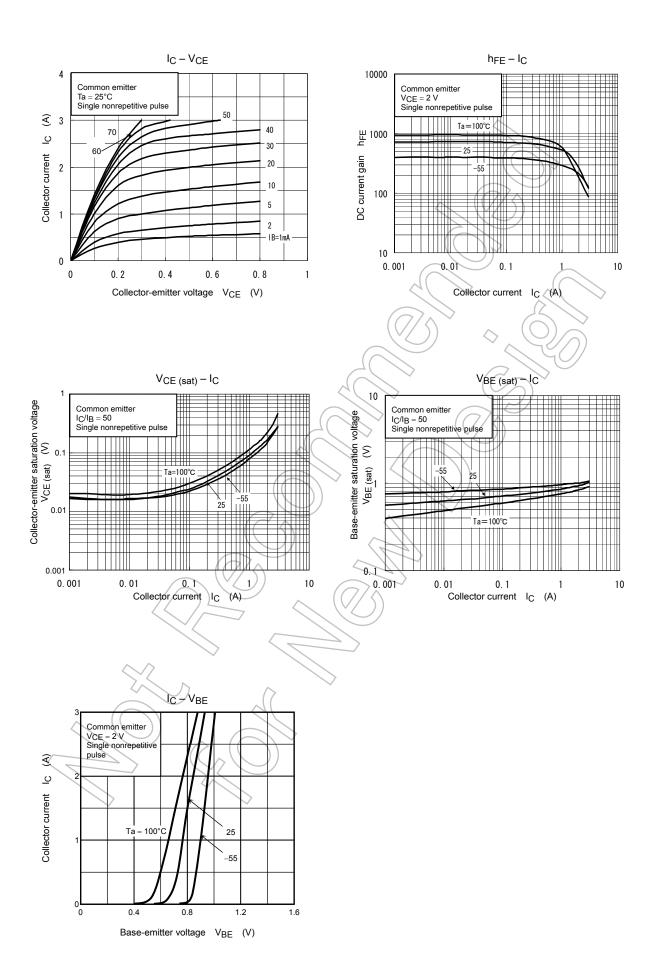
Note 3: A dot marking for identifying the indication of product Labels.

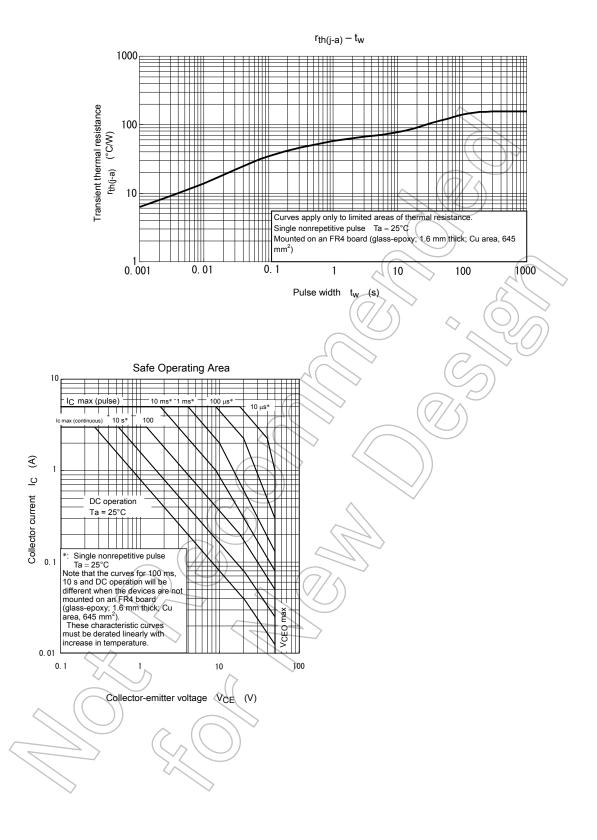
Without a dot: [[Pb]]/INCLUDES > MCV

With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

2 2013-11-01





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