

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOS IV)

TPC8030

Lithium Ion Battery Applications
Portable Equipment Applications
Notebook PC Applications

Unit: mm

- Small footprint due to small and thin package
- Low drain-source ON-resistance: $R_{DS(ON)} = 7.5 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 26 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \text{ }\mu\text{A}$ (max) ($V_{DS} = 30 \text{ V}$)
- Enhancement mode: $V_{th} = 1.3 \text{ to } 2.5 \text{ V}$ ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

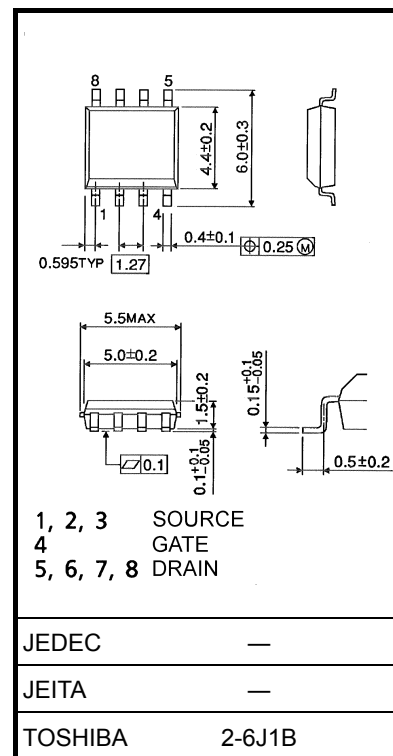
Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	30	V
Gate-source voltage	V_{GSS}	± 25	V
Drain current	DC (Note 1)	I_D	A
	Pulse (Note 1)	I_{DP}	
Drain power dissipation ($t = 10 \text{ s}$) (Note 2a)	P_D	1.9	W
Drain power dissipation ($t = 10 \text{ s}$) (Note 2b)	P_D	1.0	W
Single pulse avalanche energy (Note 3)	E_{AS}	31	mJ
Avalanche current	I_{AR}	11	A
Repetitive avalanche energy (Note 2a) (Note 4)	E_{AR}	0.053	mJ
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55 to 150	$^\circ\text{C}$

Note: Note 1, Note 2, Note 3 and Note 4: See the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

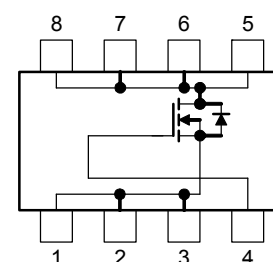
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.080 g (typ.)

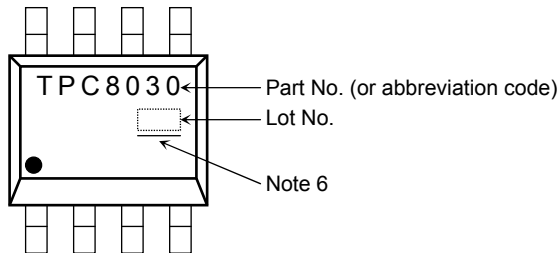
Circuit Configuration



Thermal Characteristics

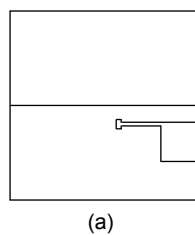
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	$R_{th(ch-a)}$	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	$R_{th(ch-a)}$	125	°C/W

Marking (Note 5)

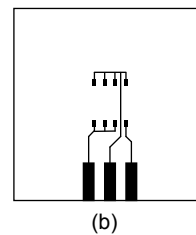


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



FR-4
25.4 × 25.4 × 0.8
(unit: mm)



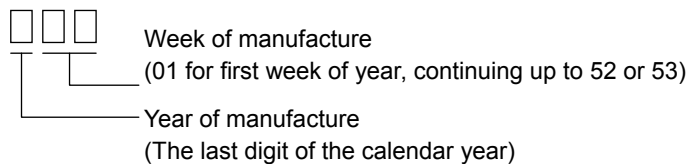
FR-4
25.4 × 25.4 × 0.8
(unit: mm)

Note 3: $V_{DD} = 24\text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 0.2\text{ mH}$, $I_{AR} = 11\text{ A}$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: • on the lower left of the marking indicates Pin 1.

* Weekly code: (Three digits)



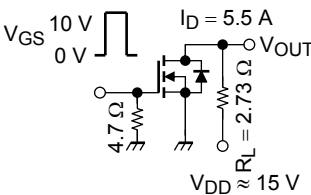
Note 6: A line under a Lot No. identifies the indication of product Labels.

Not underlined: $[[Pb]]/INCLUDES > MCV$

Underlined: $[[G]]/RoHS\ COMPATIBLE$ or $[[G]]/RoHS\ [[Pb]]$

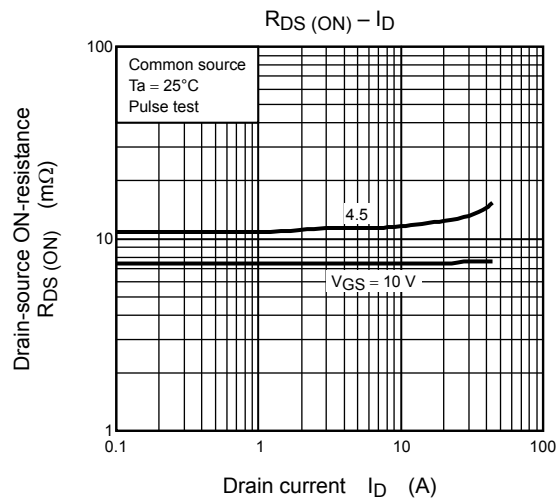
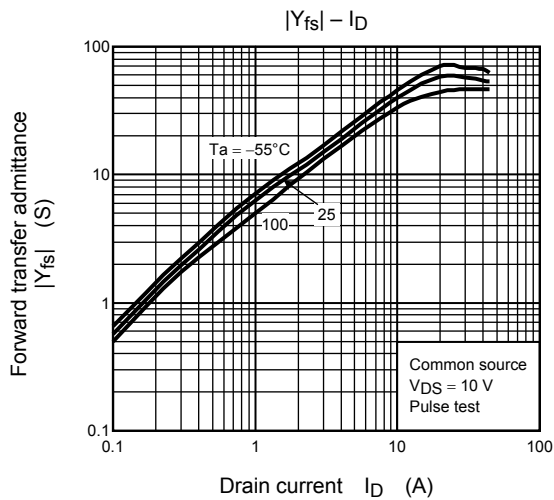
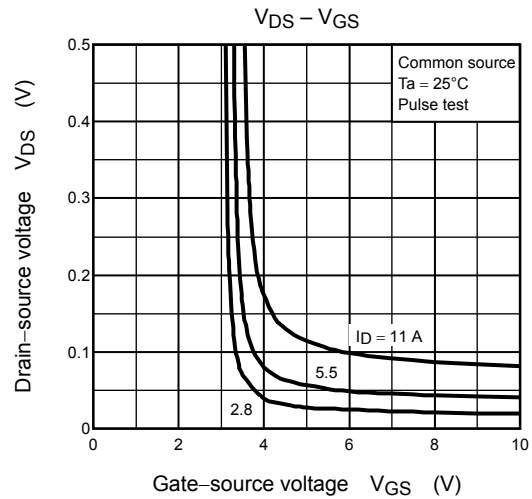
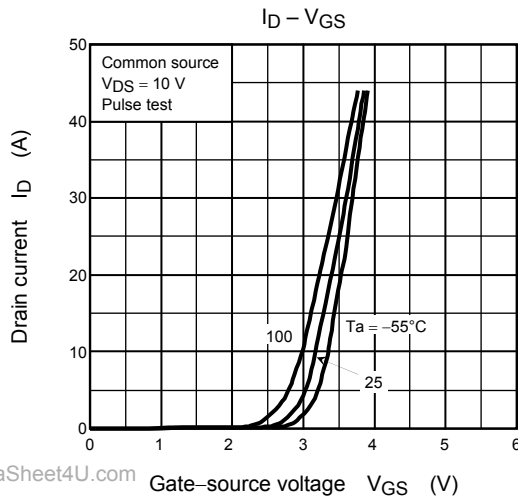
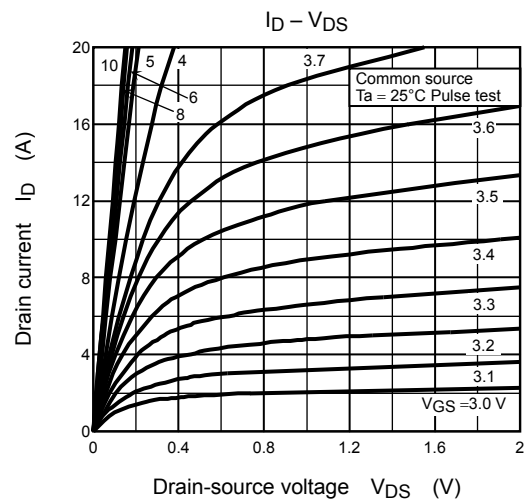
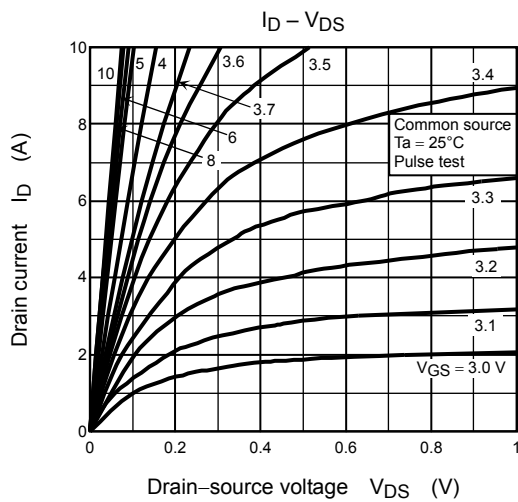
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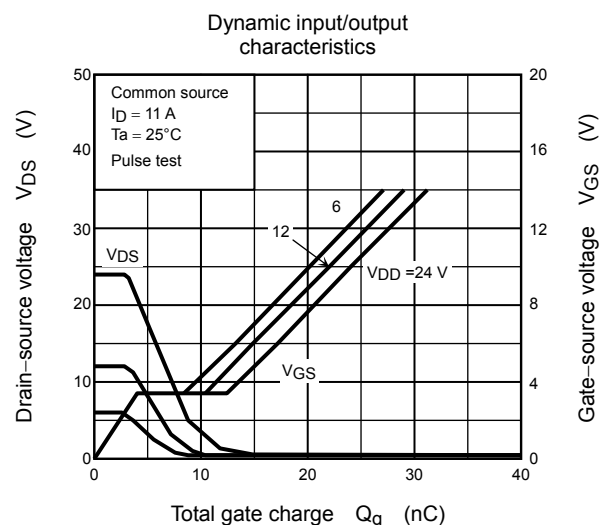
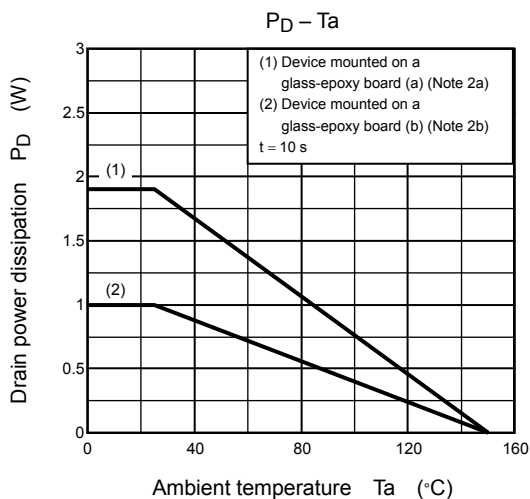
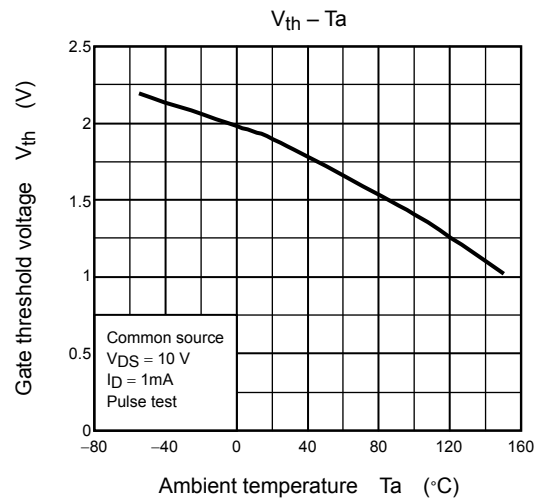
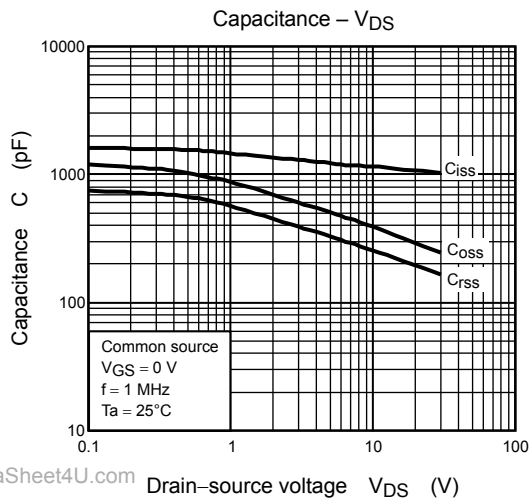
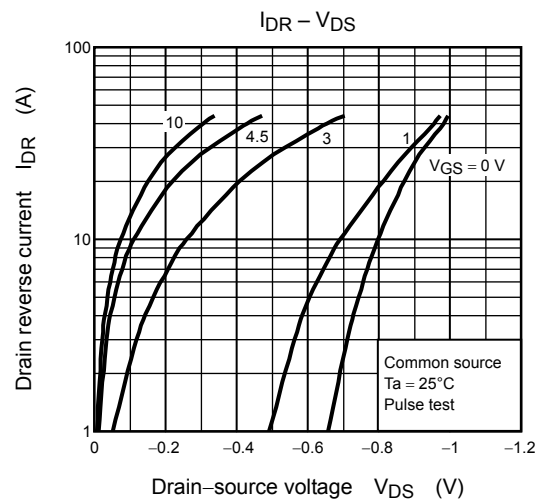
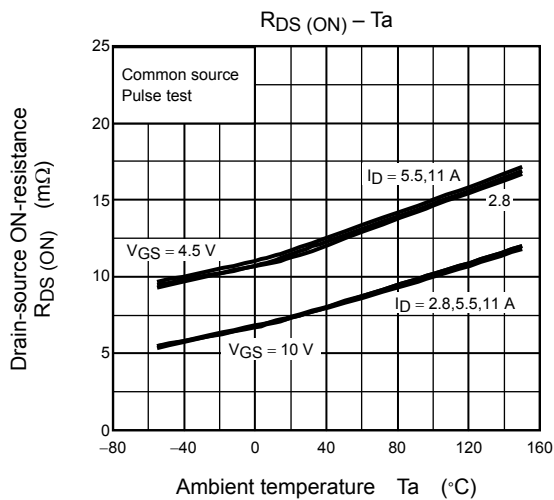
Electrical Characteristics (Ta = 25°C)

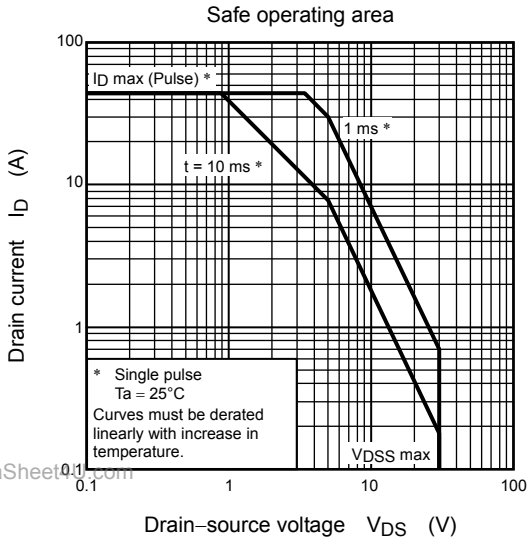
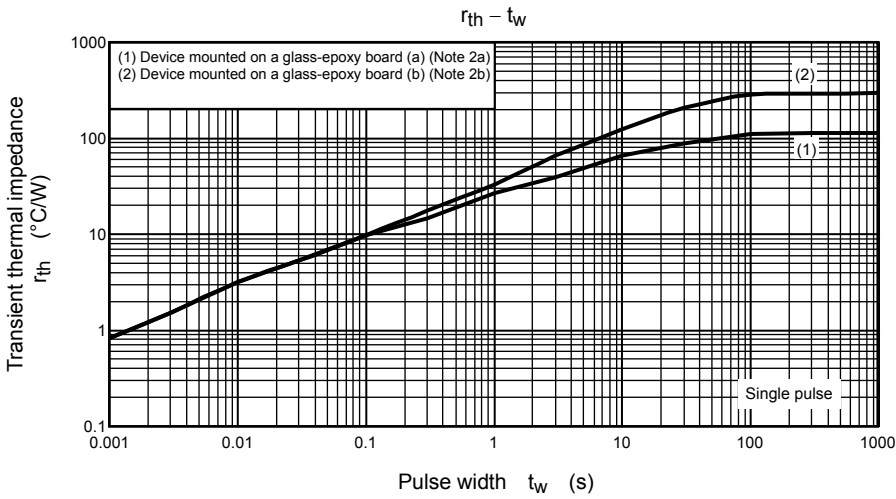
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	—	—	±100	nA
Drain cut-OFF current		I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	—	—	10	μA
Drain-source breakdown voltage		V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	30	—	—	V
		V _{(BR) DSX}	I _D = 10 mA, V _{GS} = −25 V	5	—	—	
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.3	—	2.5	V
Drain-source ON-resistance		R _{DS (ON)}	V _{GS} = 4.5 V, I _D = 5.5 A	—	11.5	17	mΩ
			V _{GS} = 10 V, I _D = 5.5 A	—	7.5	9	
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 5.5 A	13	26	—	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	—	1140	—	pF
Reverse transfer capacitance		C _{rss}		—	255	—	
Output capacitance		C _{oss}		—	390	—	
Switching time	Rise time	t _r	 <p>Duty ≤ 1%, t_w = 10 μs</p>	—	14	—	ns
	Turn-ON time	t _{on}		—	25	—	
	Fall time	t _f		—	9	—	
	Turn-OFF time	t _{off}		—	33	—	
Total gate charge (gate-source plus gate-drain)		Q _g	V _{DD} ≈ 24 V, V _{GS} = 10 V, I _D = 11 A	—	24	—	nC
Gate-source charge 1		Q _{gs1}		—	4	—	
Gate-drain (“miller”) charge		Q _{gd}		—	8.4	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	—	—	—	44	A
Forward voltage (diode)		V _{DSF}	I _{DR} = 11 A, V _{GS} = 0 V	—	—	-1.2	V







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