TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

# **TPCA8050-H**

Switching Regulator Applications Motor Drive Applications DC-DC Converter Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: Q<sub>SW</sub> = 10 nC (typ.)
- Low drain-source ON-resistance:  $R_{DS (ON)} = 9.0 \text{ m}\Omega$  (typ.)
- High forward transfer admittance: |Yfs| = 70 S (typ.)
- Low leakage current: I<sub>DSS</sub> = 10 μA (max) (V<sub>DS</sub> = 60 V)
- Enhancement mode: V<sub>th</sub> = 1.3 to 2.3 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 0.5 mA)

### Absolute Maximum Ratings (Ta = 25°C)

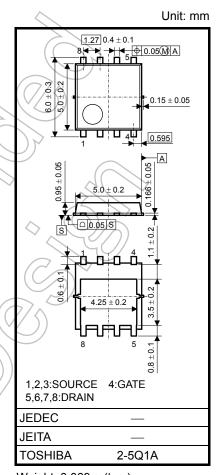
Characteristic		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	60	V
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub> (	60	V
Gate-source voltage		V <sub>GSS</sub>	±20	<\v
Drain current	DC (Note 1)	ID((	24	A
	Pulsed (Note 1)	IDP	72	^
Drain power dissipation	on (Tc = 25°C)	(PD	45	\/w
Drain power dissipation	on (t = 10 s) (Note 2a)	PD	2.8	W
Drain power dissipation	on (t = 10 s) (Note 2b)	PD	1.6	w
Single-pulse avalanche energy (Note 3)		EAS	42	mJ
Avalanche current		I <sub>AR</sub>	24	Α
Repetitive avalanche energy (Tc = 25°C) (Note 4)		EAR	2.68	mJ
Channel temperature		Tch	150	°C
Storage temperature range		Tstg	-55 to 150	°C

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

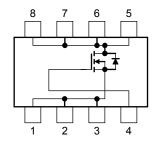
reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.069 g (typ.)

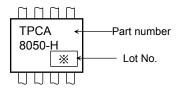
#### **Circuit Configuration**



#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R <sub>th (ch-a)</sub>	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R <sub>th (ch-a)</sub>	78.1	°C/W

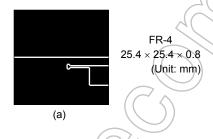
### Marking (Note 5)

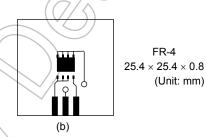


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3:  $V_{DD} = 24 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial),  $L = 100 \mu\text{H}$ ,  $R_G = 25 \Omega$ ,  $I_{AR} = 24 \text{ A}$ 

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)

Week of manufacture (01 for the first week of the year, continuing up to 52 or 53)

Year of manufacture

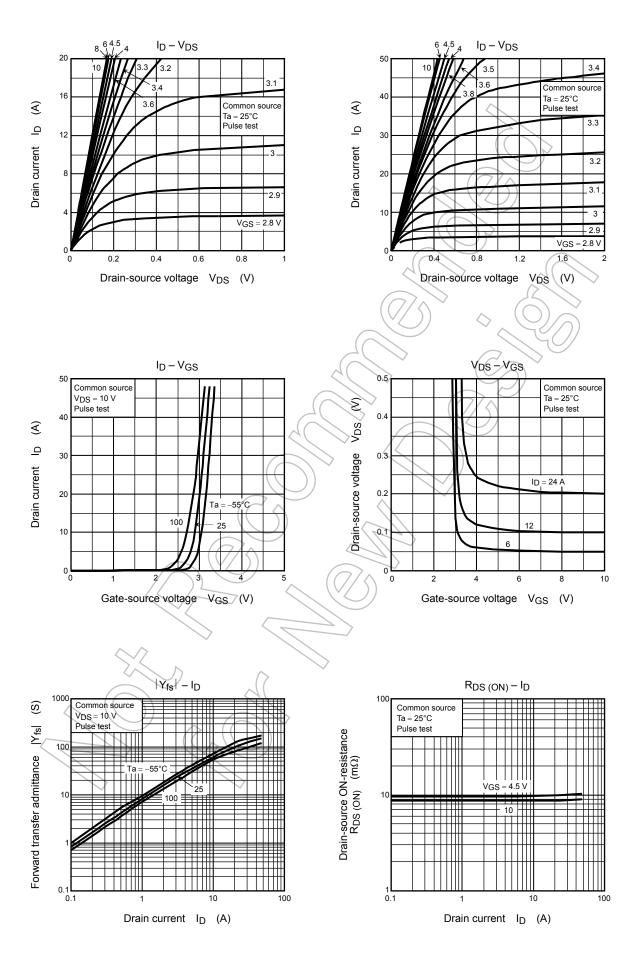
(The last digit of the year)

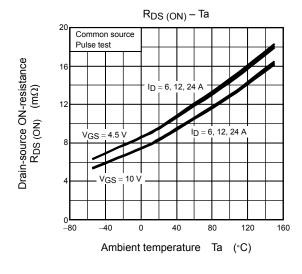
## **Electrical Characteristics (Ta = 25°C)**

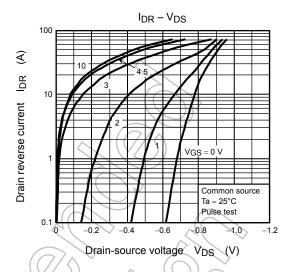
Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA	
Drain cutoff curre	nt	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	10	μА	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	V	
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	45	_	_	V	
Gate threshold vo	oltage	$V_{th}$	$V_{DS} = 10 \text{ V}, I_D = 0.5 \text{ mA}$	1.3	) /_	2.3	V	
Drain-source ON-resistance		R <sub>DS (ON)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 12 \text{ A}$	) / />	10.1	15.3	- mΩ	
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A	$\rightarrow$	9.0	14.2		
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 12 A	35	70	_	S	
Input capacitance		C <sub>iss</sub>			2590	3365		
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	95	140	pF	
Output capacitane	ce	Coss		_ /	300	$\searrow$		
Gate resistance		rg	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	1.0	1.5	Ω	
Switching time	Rise time	t <sub>r</sub>	ACS O A D S T S T S T S T S T S T S T S T S T S	M	2.5	) _	ns	
	Turn-on time	t <sub>on</sub>			11			
	Fall time	t <sub>f</sub>		)	5.7	_		
	Turn-off time	t <sub>off</sub>	V <sub>DD</sub> ≈ 30 V Duty ≤ 1%, t <sub>W</sub> = 10 μs	_	39	_		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 24 \text{ A}$	_	41	_	-	
			$V_{DD} \approx 48 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 24 \text{ A}$		21			
Gate-source char	ge 1	Q <sub>gs1</sub>		_	7.8		nC	
Gate-drain ("Miller") charge		Qgd	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 24 \text{ A}$	_	6.1	_		
Gate switch charge		Q <sub>SW</sub>		_	10	_		

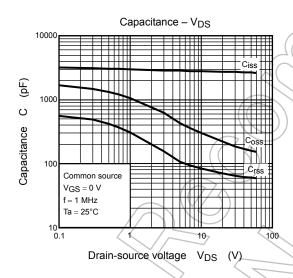
## Source-Drain Ratings and Characteristics (Ta = 25°C)

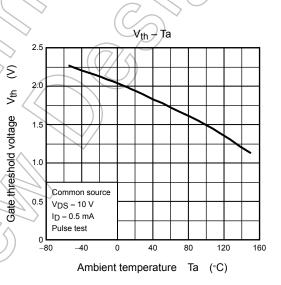
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I <sub>DRP</sub>	> -	_	_	72	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 24 A, V <sub>GS</sub> = 0 V		_	-1.2	V

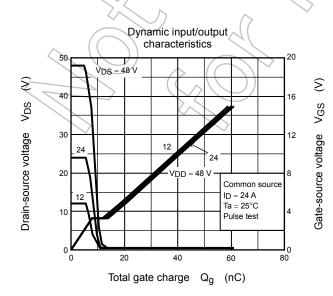






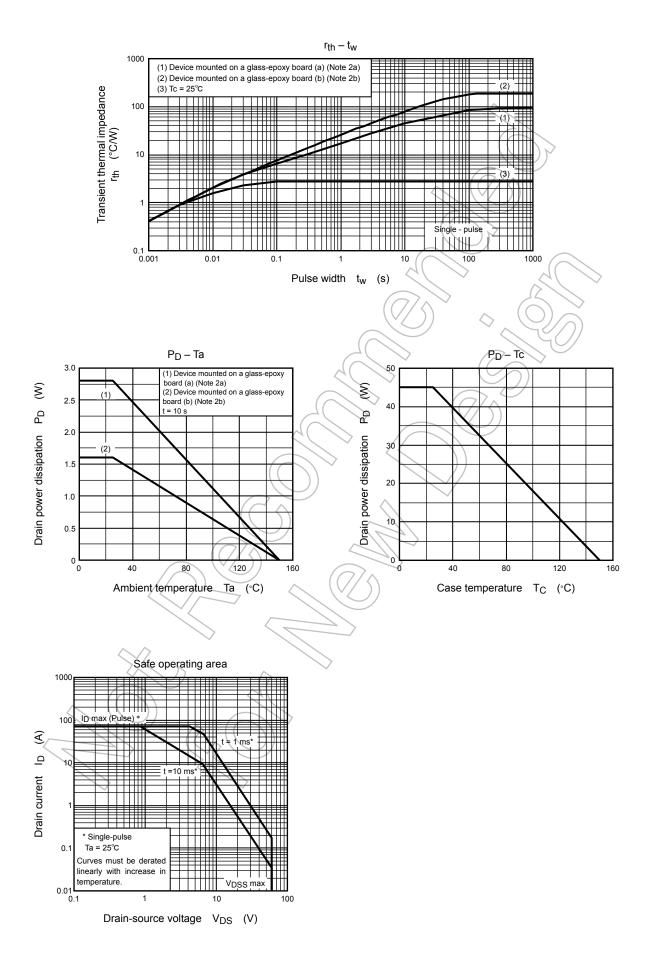






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