TOSHIBA Field Effect Transistor with Built-in Schottky Barrier Diode Silicon N-Channel MOS Type (U-MOS V-H)

TPCA8A04-H

High Efficiency DC-DC Converter Applications Notebook PC Applications Portable Equipment Applications

- Built-in a schottky barrier diode
 Low forward voltage: V_{DSF} = -0.6 V (max)
- · High-speed switching
- Small gate charge: Q_{SW} = 13.4 nC (typ.)
- Low drain-source ON-resistance: $R_{DS (ON)} = 2.3 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: |Y_{fs}| = 127 S (typ.)
- Low leakage current: I_{DSS} = 100 μ A (max) (V_{DS} = 30 V)
- Enhancement mode: V_{th} = 1.3 to 2.3 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

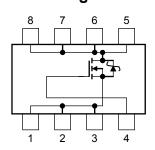
Characteristic		Symbol		Unit	
Drain-source voltage		V _{DSS} <	30	y	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	\\\\\	
Gate-source voltage		V _{GS} S	±20	V	
Drain current	DC (Note 1)	t D	44	A	
	Pulsed (Note 1)	((I _{DP}	132	\\^	
Drain power dissipation	on (Tc=25°C)		45	W)	
Drain power dissipation	on (t = 10 s) (Note 2a)	₽ D	2.8	w	
Drain power dissipation	on (t=10 s) (Note 2b)	PD	1.6	W	
Single-pulse avalanche energy (Note 3)		E _{AS}	252	mJ	
Avalanche current		IAR	44	Α	
Repetitive avalanche energy (Tc=25°C) (Note 4)		EAR	3.32	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	
				•	

Unit: mm

| 1.27 | 0.4 ± 0.1 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 | 0.05 |

Weight: 0.069 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

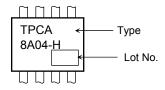
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient $(t=10 \; s) \eqno(Note \; 2a)$	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

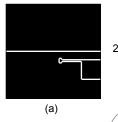
Marking (Note 5)



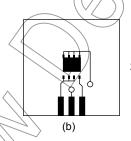
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



25.4 × 25.4 × 0.8 (Unit: mm)



FR-4 $25.4 \times 25.4 \times 0.8$ (Unit: mm)

Note 3: $V_{DD} = 24 \text{ V}$, $T_{Ch} = 25^{\circ}\text{C}$ (initial), L = 0.1 mH, $R_{G} \neq 25^{\circ}\Omega$, $I_{AR} = 44 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: * Weekly code: (Three digits)

Week of manufacture

(01) for first week of year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)

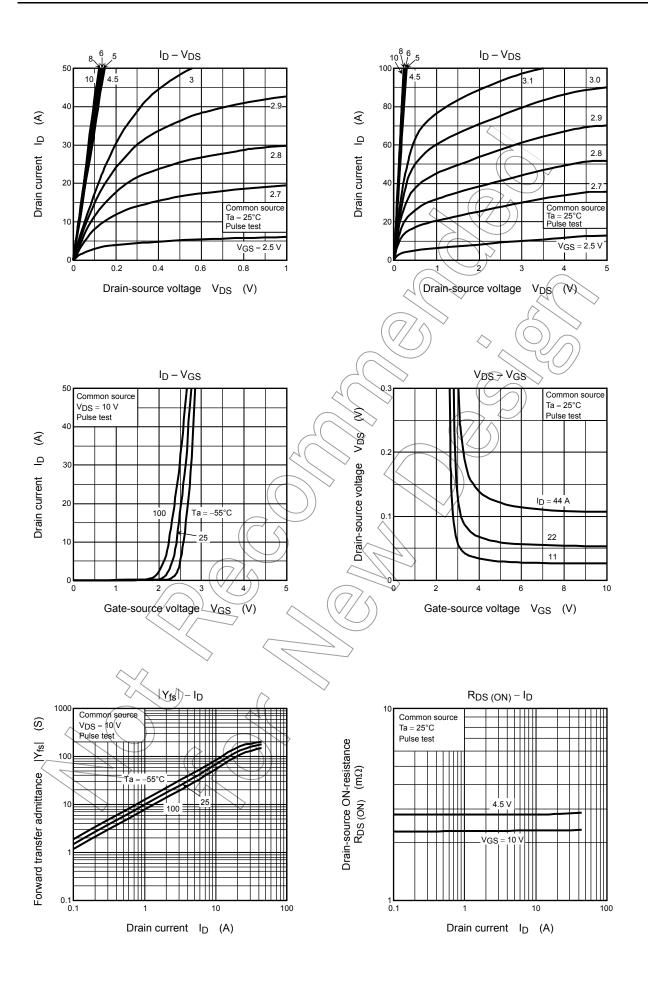
Electrical Characteristics (Ta = 25°C)

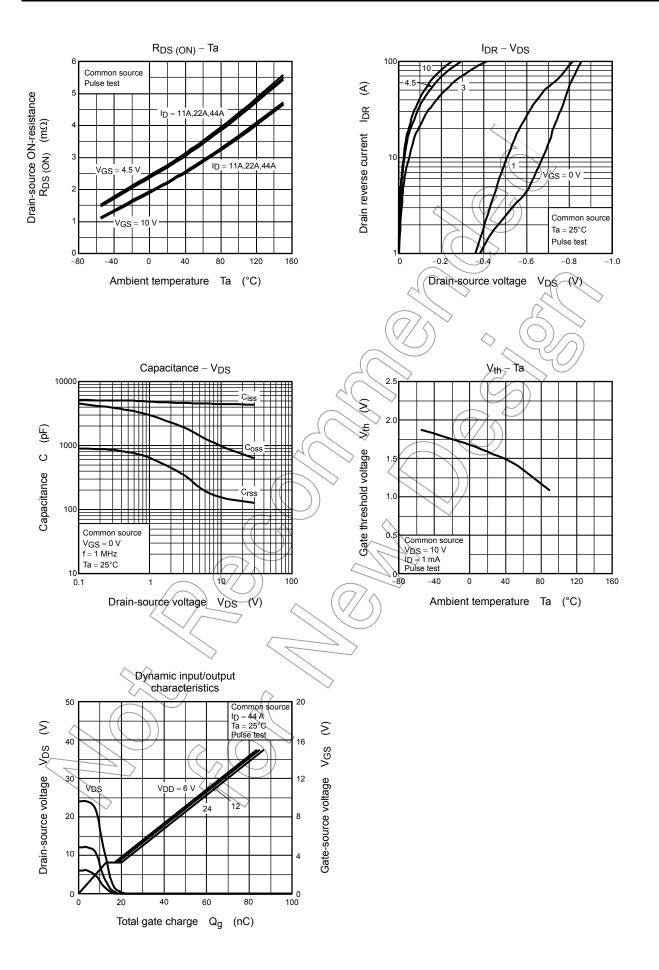
Cha	racteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage curr	ent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA	
Drain cutoff currer	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	100	μΑ	
Drain-source breakdown voltage		V _{(BR) DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V	
		V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_		
Gate threshold vol	tage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.3) / _	2.3	V	
Drain-source ON-resistance		R _{DS (ON)}	V _{GS} = 4.5 V, I _D = 22 A	> <u>~</u>	2.9	4.1	- mΩ	
			V _{GS} = 10 V, I _D = 22 A))	2.3	3.2		
Forward transfer a	dmittance	Y _{fs}	V _{DS} = 10 V, I _D = 22 A	64	127	_	S	
Input capacitance		C _{iss}		^ —	4400	5700		
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	180	270	pF	
Output capacitance		Coss			990	\rightarrow		
Gate resistance		rg	V _{DS} = 10 V, V _{GS} =0 V, f= 5 MHz	-6	1.0) 1.5	Ω	
Switching time	Rise time	t _r	10 V T ID = 22 A	7	4.9) _		
	Turn-on time	t _{on}	V _{GS} 10 V I _D = 22 A V _{OUT} G		15		- ns	
	Fall time	t _f	4.4 W W W O W O O O O O O O O O O O O O O		8.3			
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs	_	57			
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 44 \text{ A}$	_	59	_		
			$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, V_{D} \neq 44 \text{ A}$		30			
Gate-source charge 1		Q _{gs1}		_	13	_	nC	
Gate-drain ("Miller") charge		Q _{gd}	$V_{DD} \approx 24 V_{V} V_{GS} = 10 V, I_{D} = 44 A$		7.0			
Gate switch charge		Q _{SW}			13.4			

Source-Drain Ratings and Characteristics (Ta = 25°C)

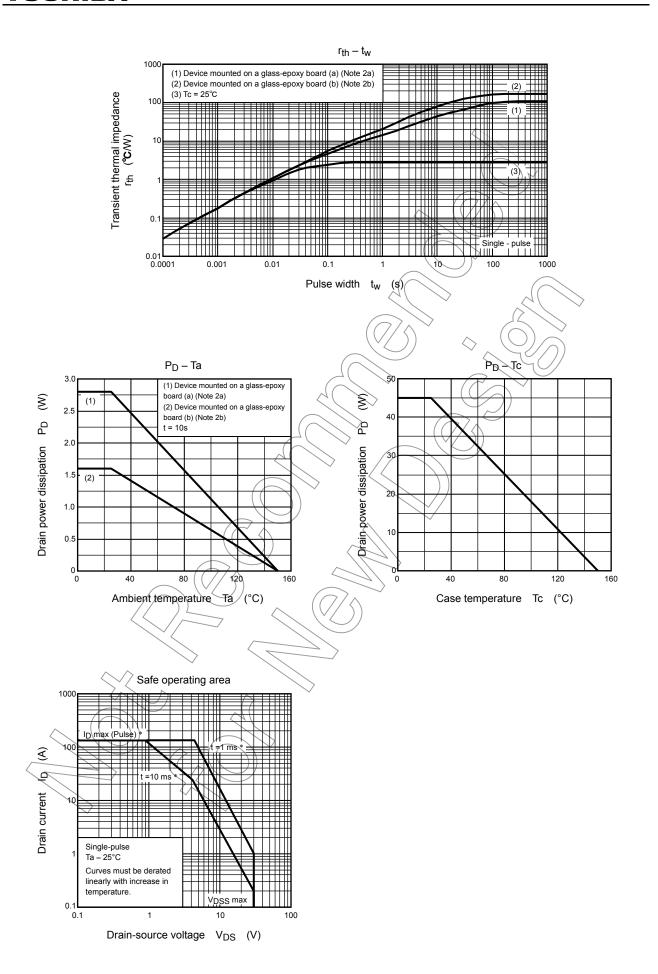
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP}	> -	_	_	132	Α
Equard voltage (Finds)		I _{DR} = 1 A, V _{GS} = 0 V	_	- 0.4	- 0.6	V
Forward voltage (diode)	v (diode)	$I_{DR} = 44 A$, $V_{GS} = 0 V$	_	_	- 1.2	V

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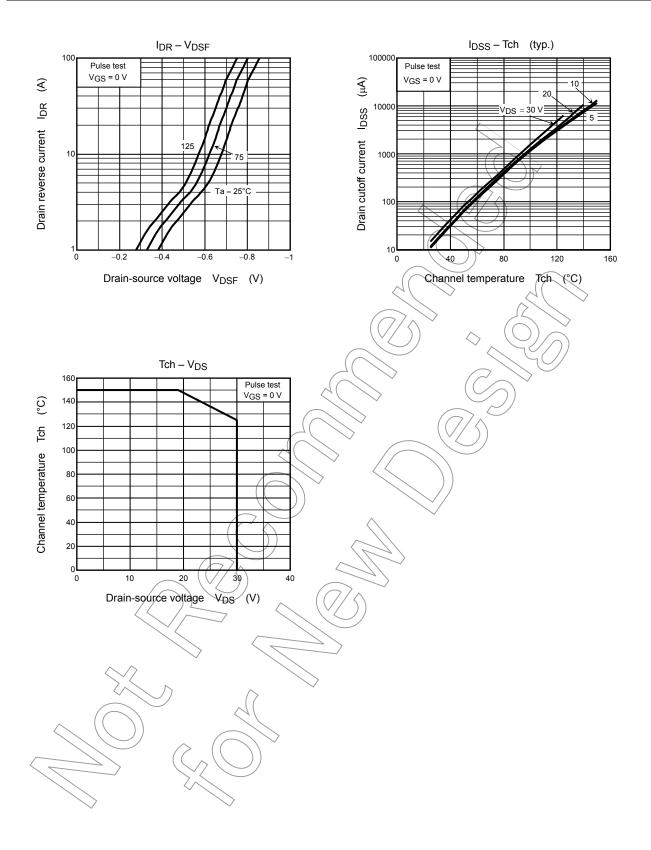




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