5-V, 55-ns Comparators with Push-Pull Output

Features

- Power Supply Voltage: 1.8 V to 5.5 V
- Low Supply Current: 350 µA per Channel
- Propagation Delay: 55 ns
- Offset Voltage: ±6 mV
- Input Common-Mode Range Extends 100 mV
- Push-Pull Output
- AEC-Q100 Qualified for Automotive Applications
  - Grade 1: −40°C to 125°C T_A

Applications

- On-Board Charger
- Motor Control
- Precision Signal Condition
- Battery Management System

Description

The devices are the newest comparators with 55-ns propagation delay. The devices are optimized for single +3-V or +5-V operation. The input common-mode range extends 100 mV beyond the rail. The outputs are push-pull and can sink or source 1 mA to within 200 mV of the power supply rail.

The devices are specified for the automotive temperature range from −40°C to +125°C.

Typical Application Circuit

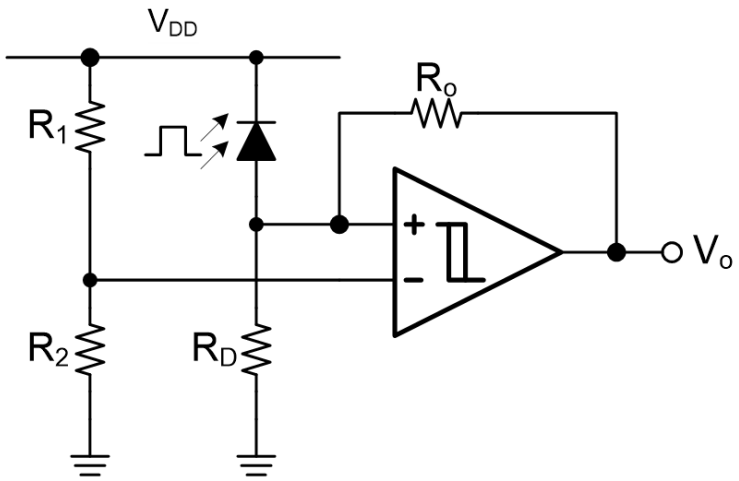


Table of Contents

Features 1
Applications 1
Description 1
Typical Application Circuit 1
Revision History 3
Pin Configuration and Functions 4
Specifications 6
Absolute Maximum Ratings (1) 6
ESD, Electrostatic Discharge Protection 6
Recommended Operating Conditions 6
Thermal Information 6
Electrical Characteristics 7
Electrical Characteristics (Continued) 9
Electrical Characteristics (Continued) 11
Typical Performance Characteristics 13
Detailed Description 15
Overview 15
Functional Block Diagram 15
Application and Implementation 16
Application Information 16
Typical Application 17
Tape and Reel Information 18
Package Outline Dimensions 19
SOT23-5 19
SOP8 20
MSOP8 21
Order Information 22
IMPORTANT NOTICE AND DISCLAIMER 23

Revision History

Date	Revision	Notes
2025-02-05	Rev.A.0	Initial version in releasing process.

Pin Configuration and Functions

TPCMP231Q
SOT23-5
Top View

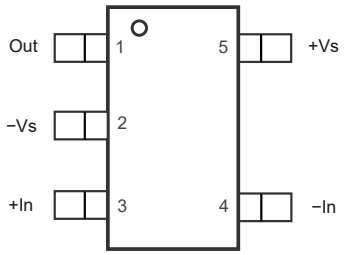
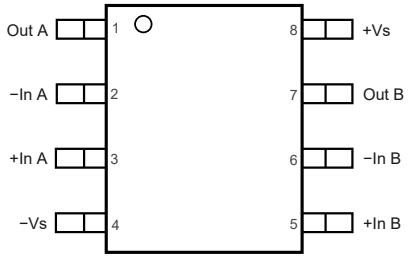


Table 1. Pin Functions: TPCMP231Q

Pin No.	Name	I/O	Description
1	Out	O	Output
2	$-V_S$	-	Negative power supply
3	+In	I	Non-inverting input
4	−In	I	Inverting input
5	$+V_S$	-	Positive power supply

TPCMP232Q
SOP8
Top View



TPCMP232Q
MSOP8
Top View

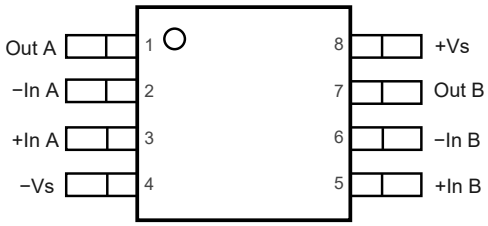


Table 2. Pin Functions: TPCMP232Q

Pin No.	Name	I/O	Description
1	Out A	O	Output
2	−In A	I	Inverting input
3	+In A	I	Non-inverting input
4	$-V_S$		Negative power supply
5	+In B	I	Non-inverting input
6	−In B	I	Inverting input
7	Out B	O	Output
8	$+V_S$		Positive power supply

Specifications

Absolute Maximum Ratings (1)

Parameter		Min	Max	Unit
	Supply Voltage, $(+V_S) – (-V_S)$		6.5	V
	Input Voltage	$(-V_S) – 0.5$	$(V_S) + 0.5$	V
	Input Current: +IN, −IN (2)	−10	10	mA
	Output Current: OUT	−10	10	mA
	Output Short-Circuit Duration (3)		Thermal Protection	
T_J	Maximum Junction Temperature		150	°C
T_A	Operating Temperature Range	−40	125	°C
T_{STG}	Storage Temperature Range	–65	150	°C
T_L	Lead Temperature (Soldering 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.
(2) The inputs are protected by ESD protection diodes to each power supply. If the input extends more than 500 mV beyond the power supply, the input current should be limited to less than 10 mA.
(3) A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many comparators are shorted. Thermal resistance varies with the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

ESD, Electrostatic Discharge Protection

Parameter		Condition	Level	Unit
HBM	Human Body Model ESD	AEC Q100-002	4	kV
CDM	Charged Device Model ESD	AEC Q100-011	1.5	kV

Recommended Operating Conditions

Parameter		Min	Typ	Max	Unit
V_S	Supply Voltage, $(+V_S) – (-V_S)$	1.8		5.5	V

Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
SOT23-5	250	81	°C/W
SOP8			
MSOP8			

Electrical Characteristics

All test conditions: V_S = 5 V, T_A = 25°C, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Power Supply						
I_Q	Quiescent Current per Comparator	V_{CM} = 0 V		350	500	µA
		V_{CM} = 0 V, T_A = −40°C to 125°C			600	µA
PSRR	Power Supply Rejection Ratio	V_S = 2.7 V to 5.5 V, V_{CM} = 0 V	60	75		dB
		V_S = 2.7 V to 5.5 V, V_{CM} = 0 V, T_A = −40°C to 125°C	55			dB
Input Characteristics						
V_{OS}	Input Offset Voltage (1)	V_{CM} = 2.5 V	−6		6	mV
		V_{CM} = 2.5 V, T_A = −40°C to 125°C	−7		7	mV
$V_{OS}TC$	Input Offset Voltage Drift (2)	T_A = −40°C to 125°C		4		µV/°C
I_B	Input Bias Current	V_{CM} = 2.5 V		1	20	nA
		V_{CM} = 2.5 V, T_A = −40°C to 125°C			25	nA
I_{OS}	Input Offset Current	V_{CM} = 2.5 V		0.5	8	nA
		V_{CM} = 2.5 V, T_A = −40°C to 125°C			10	nA
V_{DIFF}	Differential-Mode Input Voltage Range (4)	T_A = −40°C to 125°C	–5		5	V
V_{CMR}	Common-Mode Input Voltage Range	T_A = −40°C to 125°C	$(-V_S)$ – 0.1		$(+V_S)$ + 0.1	V
CMRR	Common-Mode Rejection Ratio	V_{CM} = 0 V to 5 V	70	95		dB
		V_{CM} = 0 V to 5 V, T_A = −40°C to 125°C	65			dB
Output Characteristics						
V_{OH}	Output Voltage High	I_{LOAD} = 4 mA	4.8	4.84		V
		I_{LOAD} = 4 mA, T_A = −40°C to 125°C	4.7			V
V_{OL}	Output Voltage Low	I_{LOAD} = 4 mA		130	180	mV
		I_{LOAD} = 4 mA, T_A = −40°C to 125°C			250	mV
I_{SC}	Short-Circuit Current			40		mA
Switching Characteristics (3)						
T_{PLH}	Propagation Delay (Low-to-High)	V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF		42	75	ns
		V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF, T_A = −40°C to 125°C			100	ns
T_{PLH}	Propagation Delay (Low-to-High)	V_{CM} = 0 V, Overdrive = 20 mV, C_{LOAD} = 50 pF		55	95	ns
T_{PHL}	Propagation Delay (High-to-Low)	V_{CM} = 0 V, Overdrive = 100 mV,		45	75	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
		C_{LOAD} = 50 pF				
		V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF, T_A = −40°C to 125°C			100	ns
T_{PHL}	Propagation Delay (High-to-Low)	V_{CM} = 0 V, Overdrive = 20 mV, C_{LOAD} = 50 pF		55	95	ns
T_{RISE}	Rising Time (2)(5)	f = 10 kHz, C_{LOAD} = 50 pF, R_{LOAD} = 10 kΩ, Overdrive = 100 mV		5.5		ns
T_{FALL}	Falling Time (2)(5)	f = 10 kHz, C_{LOAD} = 50 pF, R_{LOAD} = 10 kΩ, Overdrive = 100 mV		5.5		ns
T_{PDSKEW}	Propagation Delay Skew (2)	V_{CM} = 0 V, Overdrive = 100 mV, T_{PDSKEW} = | T_{PHL}− T_{PLH} |		5		ns

(1) The input offset voltage is the average of the input-referred trip points. The input hysteresis is the difference between the input-referred trip points.
(2) Provided by bench tests and design simulation.
(3) Delay time is measured from the mid-point of the input to the mid-point of the output.
(4) Provided by design simulation.
(5) Measured between 10% of V_S and 90% of V_S.

Electrical Characteristics (Continued)

All test conditions: V_S = 3.3 V, T_A = 25°C, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Power Supply						
I_Q	Quiescent Current per Amplifier	V_{CM} = 0 V		350	500	µA
		V_{CM} = 0 V, T_A = −40°C to 125°C			600	µA
Input Characteristics						
V_{OS}	Input Offset Voltage (1)	V_{CM} = 1.65 V	−6		6	mV
		V_{CM} = 1.65 V, T_A = −40°C to 125°C	−7		7	mV
$V_{OS}TC$	Input Offset Voltage Drift (2)	T_A = −40°C to 125°C		4		µV/°C
I_B	Input Bias Current	V_{CM} = 1.65 V		1	20	nA
		V_{CM} = 1.65 V, T_A = −40°C to 125°C			25	nA
I_{OS}	Input Offset Current	V_{CM} = 1.65 V		0.5	8	nA
		V_{CM} = 1.65 V, T_A = −40°C to 125°C			10	nA
V_{DIFF}	Differential-Mode Input Voltage Range (4)	T_A = −40°C to 125°C	–3.3		3.3	V
V_{CMR}	Common-Mode Input Voltage Range	T_A = −40°C to 125°C	($-V_S$) – 0.1		($+V_S$) + 0.1	V
CMRR	Common-Mode Rejection Ratio	V_{CM} = 0 V to 3.3 V	61	80		dB
		V_{CM} = 0 V to 3.3 V, T_A = −40°C to 125°C	60			dB
Output Characteristics						
V_{OH}	Output Voltage High	I_{LOAD} = 1 mA	3.1	3.2		V
		I_{LOAD} = 1 mA, T_A = −40°C to 125°C	3			V
V_{OL}	Output Voltage Low	I_{LOAD} = 1 mA		40	150	mV
		I_{LOAD} = 1 mA, T_A = −40°C to 125°C			200	mV
I_{SC}	Short-Circuit Current			25		mA
Switching Characteristics (3)						
T_{PLH}	Propagation Delay (Low-to-High)	V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF		40	65	ns
		V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF, T_A = −40°C to 125°C			85	ns
T_{PLH}	Propagation Delay (Low-to-High)	V_{CM} = 0 V, Overdrive = 20 mV, C_{LOAD} = 50 pF		50	80	ns
T_{PHL}	Propagation Delay (High-to-Low)	V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF		40	65	ns
		V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF, T_A = −40°C to 125°C			85	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{PHL}	Propagation Delay (High-to-Low)	V_{CM} = 0 V, Overdrive = 20 mV, C_{LOAD} = 50 pF		50	80	ns
T_{RISE}	Rising Time (2)(5)	f = 10 kHz, C_{LOAD} = 50 pF, R_{LOAD} = 10 kΩ, Overdrive = 100 mV		8		ns
T_{FALL}	Falling Time (2)(5)	f = 10 kHz, C_{LOAD} = 50 pF, R_{LOAD} = 10 kΩ, Overdrive = 100 mV		5		ns
T_{PDSKEW}	Propagation Delay Skew (2)	V_{CM} = 0 V, Overdrive = 100 mV, T_{PDSKEW} = | T_{PHL} − T_{PLH} |		5		ns

(1) The input offset voltage is the average of the input-referred trip points. The input hysteresis is the difference between the input-referred trip points.
(2) Provided by bench tests and design simulation.
(3) Delay time is measured from the mid-point of the input to the mid-point of the output.
(4) Provided by design simulation.
(5) Measured between 10% of V_S and 90% of V_S.

Electrical Characteristics (Continued)

All test conditions: V_S = 1.8 V, T_A = 25°C, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Power Supply						
I_Q	Quiescent Current per Amplifier	V_{CM} = 0 V		300	340	µA
		V_{CM} = 0 V, T_A = −40°C to 125°C			370	µA
Input Characteristics						
V_{OS}	Input Offset Voltage (1)	V_{CM} = 0.9 V	−6		6	mV
		V_{CM} = 0.9 V, T_A = −40°C to 125°C	−7		7	mV
$V_{OS}TC$	Input Offset Voltage Drift (2)	T_A = −40°C to 125°C		4		µV/°C
I_B	Input Bias Current	V_{CM} = 0.9 V		1	20	nA
		V_{CM} = 0.9 V, T_A = −40°C to 125°C			25	nA
I_{OS}	Input Offset Current	V_{CM} = 0.9 V,		0.5	8	nA
		V_{CM} = 0.9 V, T_A = −40°C to 125°C			10	nA
V_{DIFF}	Differential-Mode Input Voltage Range (4)	T_A = −40°C to 125°C	–1.8		1.8	V
V_{CMR}	Common-Mode Input Voltage Range	T_A = −40°C to 125°C	(−V_S) – 0.1		(+V_S) + 0.1	V
CMRR	Common-Mode Rejection Ratio	V_{CM} = 0 V to 1.8 V	61	80		dB
		V_{CM} = 0 V to 1.8 V, T_A = −40°C to 125°C	60			dB
Output Characteristics						
V_{OH}	Output Voltage High	I_{LOAD} = 1 mA	1.6	1.7		V
		I_{LOAD} = 1 mA, T_A = −40°C to 125°C	1.5			V
V_{OL}	Output Voltage Low	I_{LOAD} = 1 mA		100	150	mV
		I_{LOAD} = 1 mA, T_A = −40°C to 125°C			200	mV
I_{SC}	Short-Circuit Current			5		mA
Switching Characteristics (3)						
T_{PLH}	Propagation Delay (Low-to-High)	V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF		50	80	ns
		V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF, T_A = −40°C to 125°C			100	ns
T_{PLH}	Propagation Delay (Low-to-High)	V_{CM} = 0 V, Overdrive = 20 mV, C_{LOAD} = 50 pF		60	110	ns
T_{PHL}	Propagation Delay (High-to-Low)	V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF		50	80	ns
		V_{CM} = 0 V, Overdrive = 100 mV, C_{LOAD} = 50 pF, T_A = −40°C to 125°C			100	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{PHL}	Propagation Delay (High-to-Low)	V_{CM} = 0 V, Overdrive = 20 mV, C_{LOAD} = 50 pF		60	110	ns
T_{RISE}	Rising Time (2)(5)	f = 10 kHz, C_{LOAD} = 50 pF, R_{LOAD} = 10 kΩ, Overdrive = 100 mV		19		ns
T_{FALL}	Falling Time (2)(5)	f = 10 kHz, C_{LOAD} = 50 pF, R_{LOAD} = 10 kΩ, Overdrive = 100 mV		15		ns
T_{PDSKEW}	Propagation Delay Skew (2)	V_{CM} = 0 V, Overdrive = 100 mV, T_{PDSKEW} = | T_{PHL} − T_{PLH} |		10		ns

(1) The input offset voltage is the average of the input-referred trip points. The input hysteresis is the difference between the input-referred trip points.
(2) Provided by bench tests and design simulation.
(3) Delay time is measured from the mid-point of the input to the mid-point of the output.
(4) Provided by design simulation.
(5) Measured between 10% of V_S and 90% of V_S.

Typical Performance Characteristics

All test conditions: V_S = 5 V, V_{CM} = 0 V, $V_{overdrive}$ = 100 mV, R_L = open, unless otherwise noted.

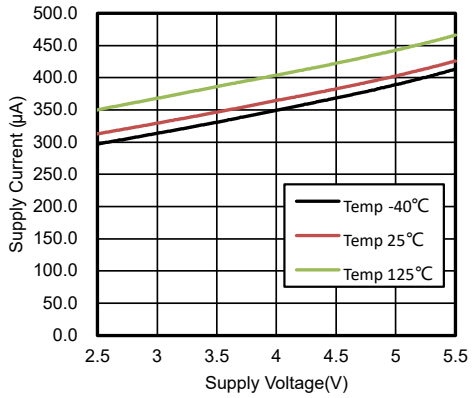


Figure 1. Supply Current vs. Supply Voltage, Output High

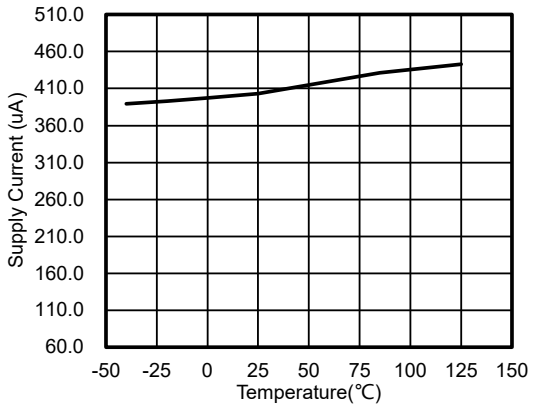


Figure 2. Supply Current vs. Temperature

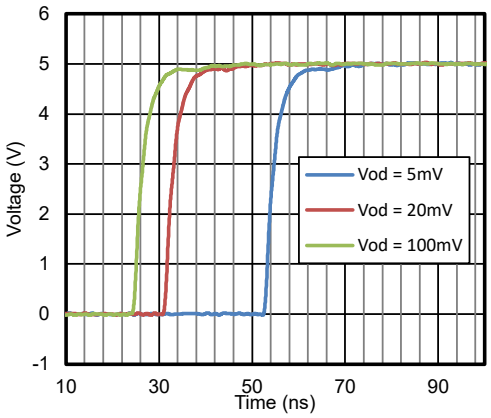


Figure 3. Propagation Delay, Low to High

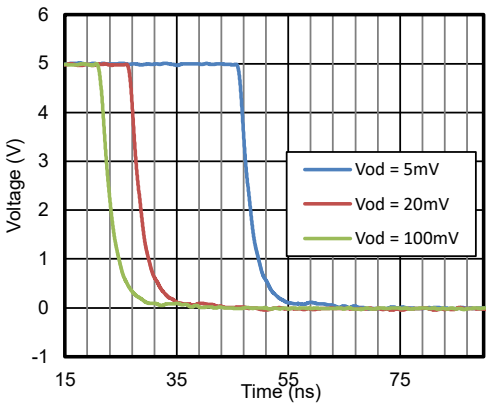


Figure 4. Propagation Delay, High to Low

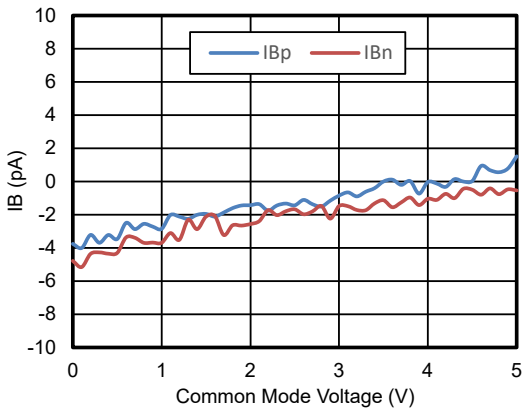


Figure 5. I_B vs Common-Mode Voltage

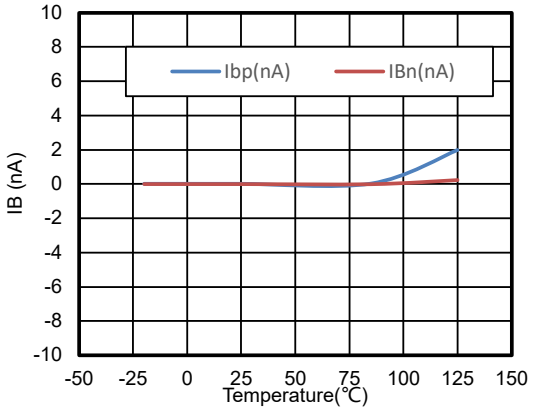


Figure 6. I_B vs Temperature

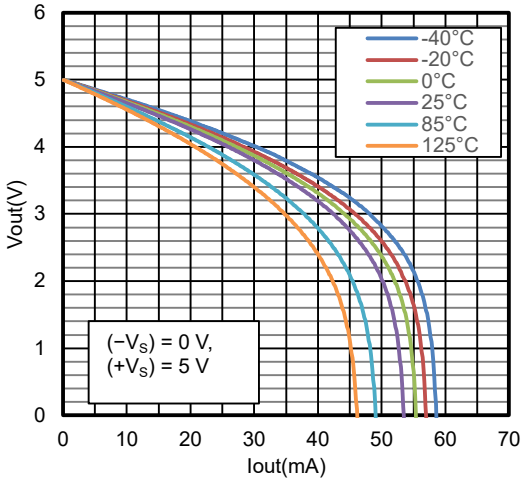


Figure 7. Output Voltage vs. Output Sourcing Current, 5 V

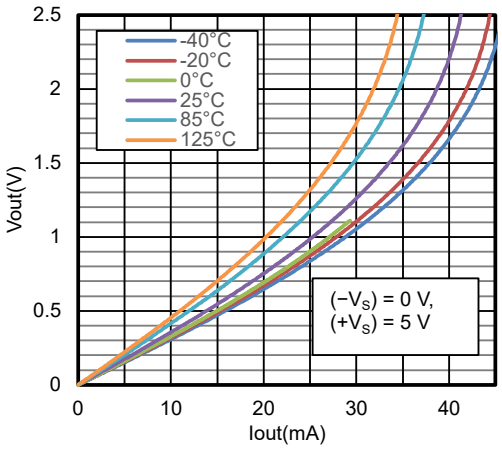


Figure 8. Output Voltage vs. Output Sinking Current, 5 V

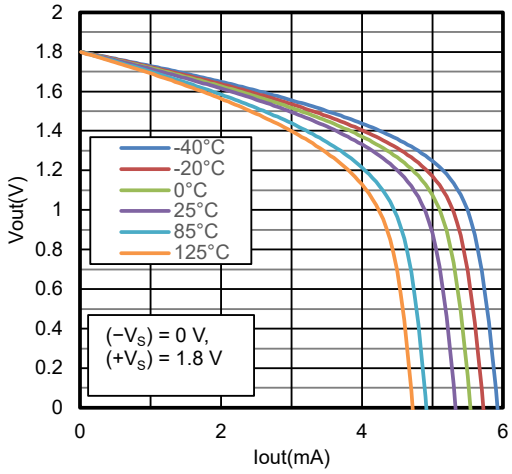


Figure 9. Output Voltage vs. Output Sourcing Current, 1.8 V

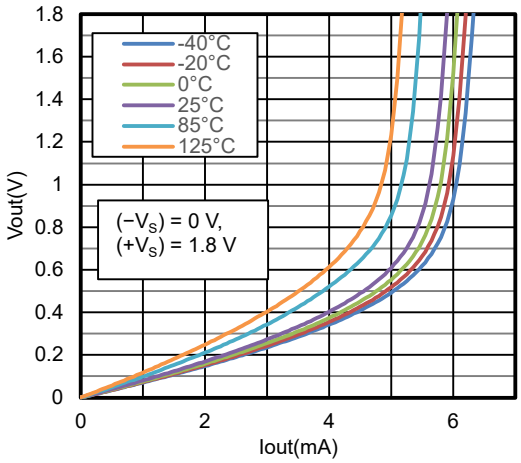


Figure 10. Output Voltage vs. Output Sinking Current, 1.8 V

Detailed Description

Overview

The devices are micro-power comparators with push-pull output and low input offset voltage. The devices operate down to 1.8 V while only consuming 350 μA per channel. The design of the comparator includes an internal charge pump that powers the input stage with an internal supply rail which is above the external supply (+V_S). This internal supply rail allows the single differential input pair to operate and remain linear over a very wide input common-mode range.

Functional Block Diagram

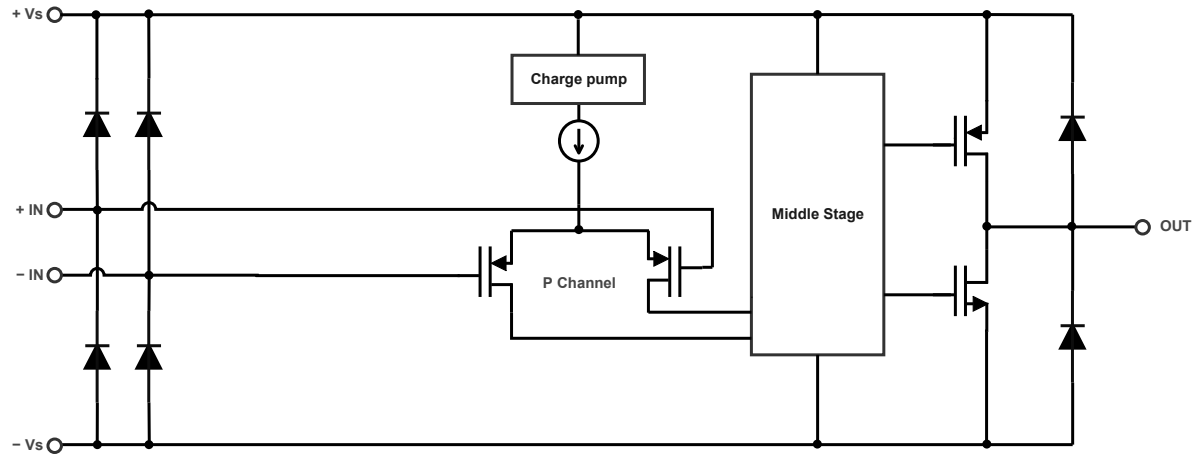


Figure 11. Functional Block Diagram

Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

Power Supply Layout and Bypass

The power supply pin of the devices should have a local bypass capacitor (i.e., 0.01 μF to 0.1 μF) within 2 mm for high-frequency performance. It can also use a bulk capacitor (i.e., 1 μF or larger) within 100 mm to provide large and slow currents. This bulk capacitor can be shared with other analog parts.

A good ground layout improves performance by decreasing the amount of stray capacitance and noise at the inputs and outputs of the comparator. To decrease stray capacitance, minimize PCB lengths and resistor leads, and place external components to the comparator pins as close as possible.

Operation Outside of the Common Input Voltage Range

A list of input voltage situations and their outcomes are as follows:

1. When both −IN and +IN are within the common-mode range:
   a. If the voltage at the −IN pin is higher than the voltage at the +IN pin and the offset voltage, the output is low, and the output MOSFET is sinking current.
   b. If the voltage at the −IN pin is lower than the voltage at the +IN pin and the offset voltage, the output is high, and the output MOSFET is sourcing current.
2. When the voltage at the −IN pin is higher than the common-mode voltage range and the voltage at the +IN pin is within the common-mode voltage range, the output is low, and the output MOSFET is sinking current.
3. When the voltage at the +IN pin is higher than the common-mode voltage range and the voltage at the −IN pin is within the common-mode voltage range, the output is high impedance.
4. When the voltage at the −IN and +IN pins are both higher than the common-mode voltage range, the output is in an uncertain state.

Typical Application

IR Receiver

The device is an ideal candidate to be used as an infrared receiver shown in Figure 12. The infrared photo diode creates a current relative to the amount of infrared light present. The current creates a voltage across R_D. When this voltage level crosses the voltage applied by the voltage divider to the inverting input, the output transitions. Optional R_O provides additional hysteresis for noise immunity.

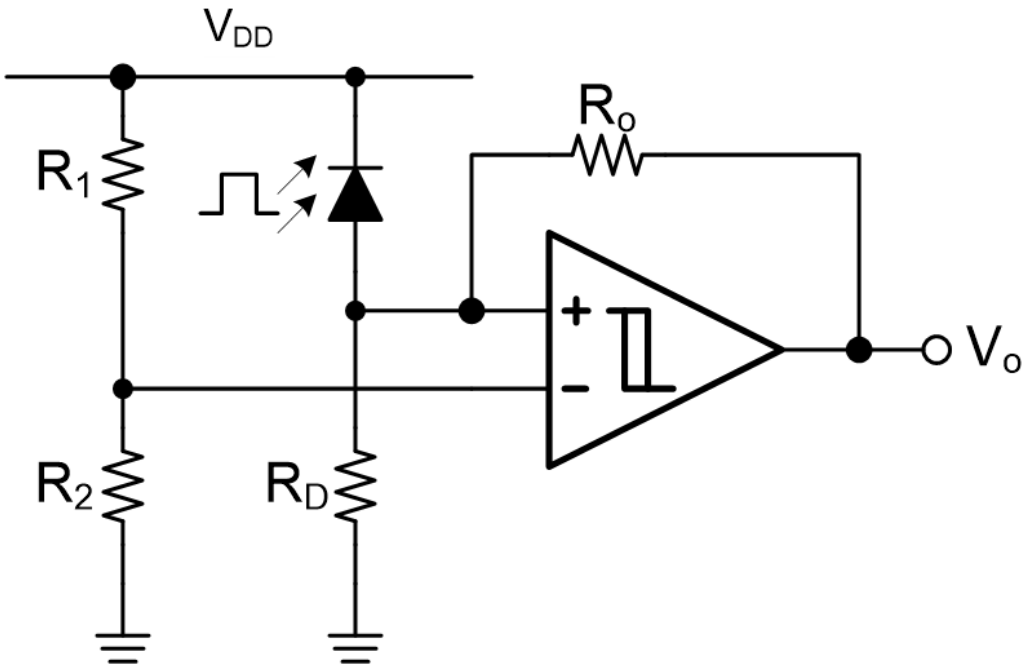
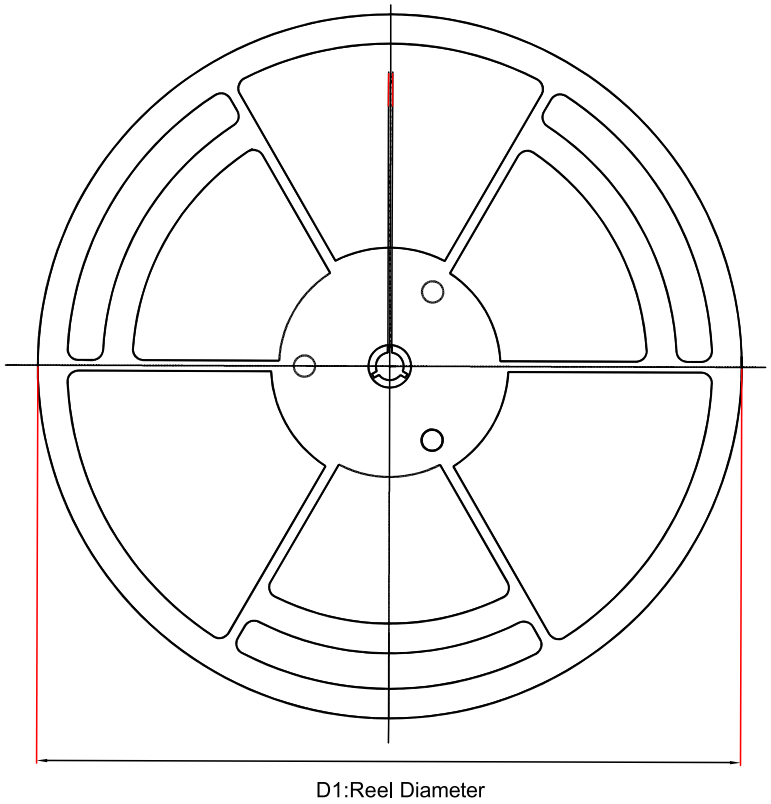
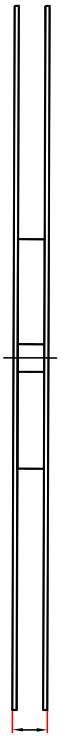
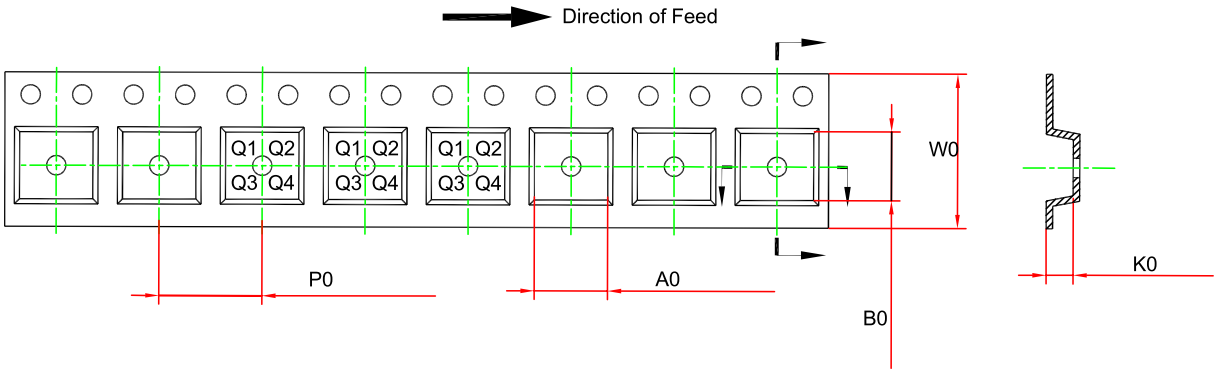


Figure 12. Typical Application Circuit

Tape and Reel Information







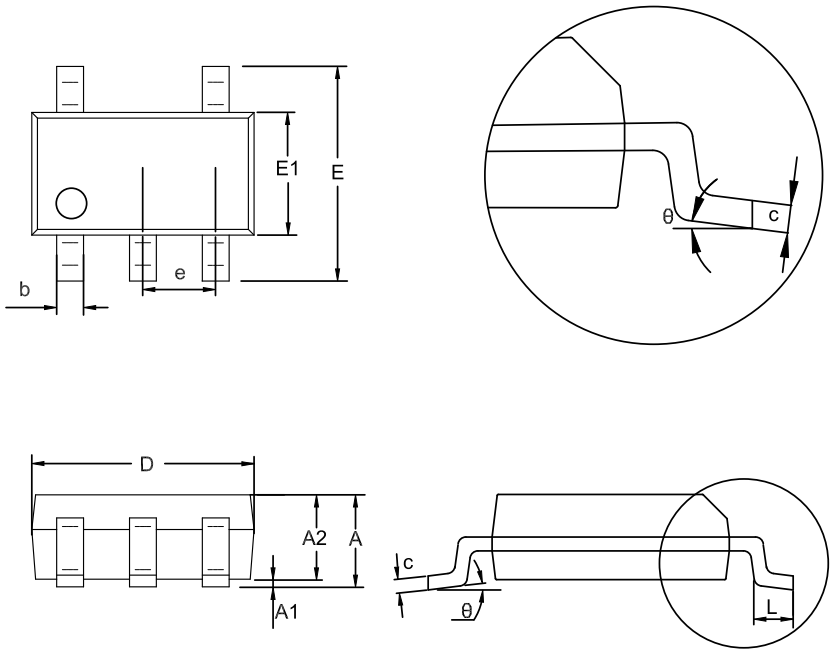
Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm) (1)	B0 (mm) (1)	K0 (mm) (1)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPCMP231Q-S5TR-S	SOT23-5	179	12	3.3	3.25	1.4	4	8	Q3
TPCMP232Q-SO1R-S	SOP8								Q1
TPCMP232Q-VS1R-S	MSOP8								Q1

(1) The value is for reference only. Contact the 3PEAK factory for more information.

Package Outline Dimensions

SOT23-5

Package Outline Dimensions S5T(SOT23-5-A)



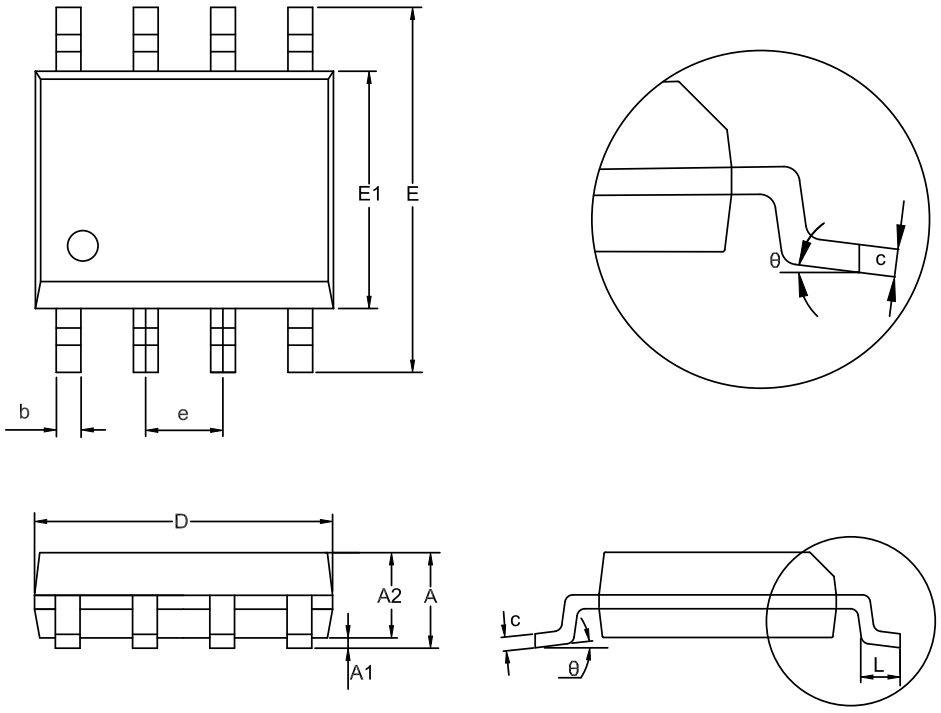
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.150	0.000	0.006
A2	1.000	1.200	0.039	0.047
b	0.280	0.500	0.011	0.020
c	0.100	0.230	0.004	0.009
D	2.820	3.020	0.111	0.119
E	2.600	3.000	0.102	0.118
E1	1.500	1.720	0.059	0.068
e	0.950 BSC		0.037 BSC	
L	0.300	0.600	0.012	0.024
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

SOP8

Package Outline Dimensions SO1(SOP-8-A)



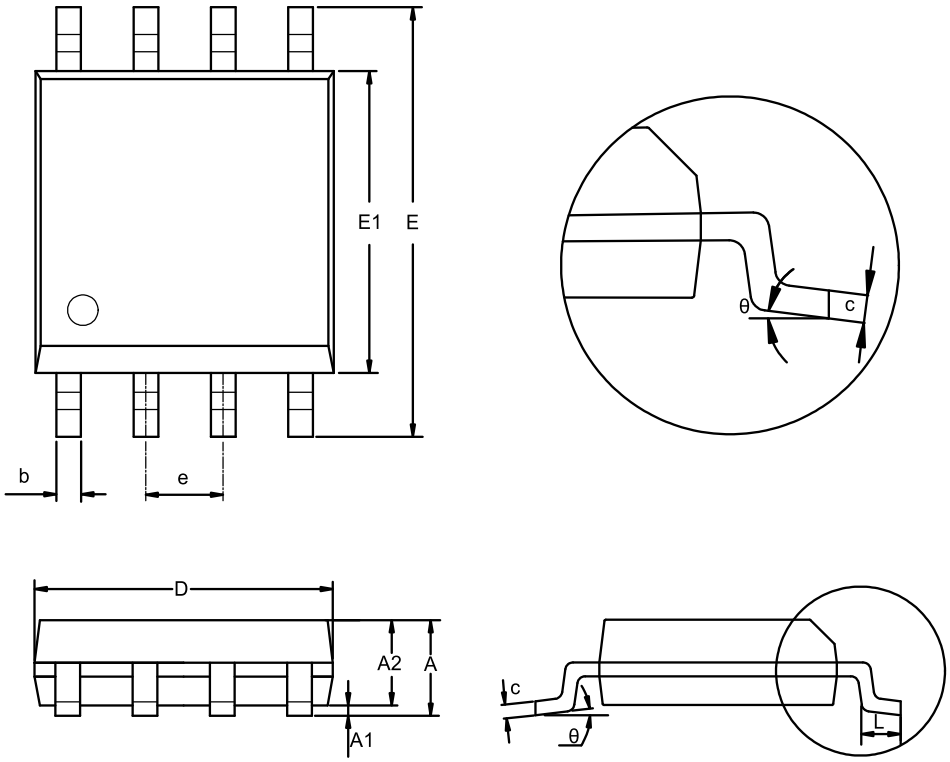
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.550	0.049	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.400	1.000	0.016	0.039
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

MSOP8

Package Outline Dimensions VS1(MSOP-8-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.800	1.100	0.031	0.043
A1	0.020	0.150	0.001	0.006
A2	0.750	0.950	0.030	0.037
b	0.250	0.380	0.010	0.015
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
E	4.700	5.100	0.185	0.201
E1	2.900	3.100	0.114	0.122
e	0.650 BSC		0.026 BSC	
L	0.400	0.800	0.016	0.031
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPCMP231Q-S5TR-S	−40 to 125°C	SOT23-5	31Q	3	Tape and Reel, 3000	Green
TPCMP232Q-SO1R-S (1)	−40 to 125°C	SOP8	CM232Q		Tape and Reel, 4000	Green
TPCMP232Q-VS1R-S (1)	−40 to 125°C	MSOP8	C232Q		Tape and Reel, 3000	Green

(1) For future products, contact the 3PEAK factory for more information and sample.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

IMPORTANT NOTICE AND DISCLAIMER

Trademarks. Any of the 思瑞浦 or 3PEAK trade names, trademarks, graphic marks, and domain names contained in this document /material are the property of 3PEAK. You may NOT reproduce, modify, publish, transmit or distribute any Trademark without the prior written consent of 3PEAK.

Performance Information. Performance tests or performance range contained in this document/material are either results of design simulation or actual tests conducted under designated testing environment. Any variation in testing environment or simulation environment, including but not limited to testing method, testing process or testing temperature, may affect actual performance of the product.

Disclaimer. 3PEAK provides technical and reliability data (including data sheets), design resources (including reference designs), application or other design recommendations, networking tools, security information and other resources "As Is". 3PEAK makes no warranty as to the absence of defects, and makes no warranties of any kind, express or implied, including without limitation, implied warranties as to merchantability, fitness for a particular purpose or non-infringement of any third-party's intellectual property rights. Unless otherwise specified in writing, products supplied by 3PEAK are not designed to be used in any life-threatening scenarios, including critical medical applications, automotive safety-critical systems, aviation, aerospace, or any situations where failure could result in bodily harm, loss of life, or significant property damage. 3PEAK disclaims all liability for any such unauthorized use.

This page intentionally left blank