

isc Triacs

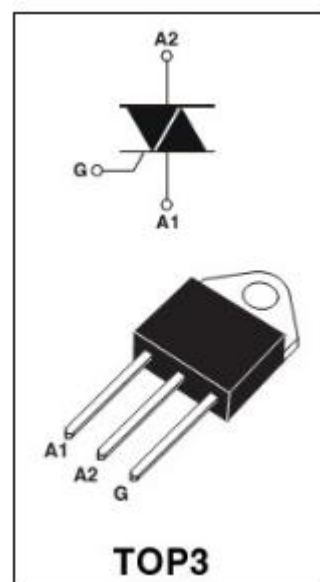
TPDV840

FEATURES

- With TOP3 insulated package
- Be suitable for general purpose where high surge current capability is required.
Application such as phase control and tatic switching on inductive or resistive load.
- Minimum Lot-to-Lot variations for robust device
performance and reliable operation

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	Value	UNIT
V _{DRM}	Repetitive peak off-state voltage	800	V
V _{RRM}	Repetitive peak reverse voltage	800	V
I _{T(RMS)}	RMS on-state current (full sine wave)T _c =75°C	40	A
I _{TSM}	Non-repetitive peak on-state current	t _p =2.5ms	590
		t _p =8.3ms	370
		t _p =10ms	350
T _j	Operating junction temperature	-40~125	°C
T _{stg}	Storage temperature	-40~150	°C
P _{G(AV)}	Average gate power dissipation(T _j =125°C)	1	W
R _{th(j-c)}	Thermal resistance, junction to case	1.2	°C/W
R _{th(j-a)}	Thermal resistance, junction to ambient	50	°C/W

ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _R =V _{RRM} , T _j =25°C T _j =125°C	0.02 8	mA
I _{DRM}	Repetitive peak off-state current	V _R =V _{RRM} , T _j =25°C T _j =125°C	0.02 8	mA
I _{GT}	Gate trigger current Quadrant (I-II-III)	V _D =12V; R _L = 33 Ω	200	mA
V _{GT}	Gate trigger voltage all quadrant Quadrant(I-II-III)	V _D =12V; R _L = 33 Ω	1.5	V
V _{TM}	On-state voltage	I _{TM} = 60A; t _p = 380 μ s	1.8	V

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