

Features

- Supply Voltage: 3.3 V to 5.25 V
- High Gain Bandwidth Product: 4 GHz
- High Slew Rate: 1000 V/ μ s
- Low input voltage Noise: 1.25 nV / $\sqrt{\text{Hz}}$
- Offset Voltage: ± 1.8 mV Maximum at 25 °C
- Stable when Gain > 7 V/V
- Quiescent Current: 9.8 mA
- Overload Recovery: 4.8 ns
- Package: DFN2X2-8
- -40°C to 125°C Operation Temperature Range
- AEC-Q100 Qualified for Automotive Applications, Grade 1: -40°C to $+125^{\circ}\text{C}$ Ta

Applications

- Automotive Lidar
- Lab Equipment
- Automated Test Equipment
- OTDR
- Laser Distance Meter

Description

TPH2865Q is a high-speed, low noise operational amplifier with high-speed BJT inputs, suitable for high-speed applications.

The TPH2865Q has a gain-bandwidth product of 4 GHz, a slew rate of 1000 V/ μ s and only 4.8 ns is need for overload recovery, making it suitable for high-speed pulse applications.

The feedback pin (FB) of TPH2865Q decreases the distance of the feedback network connection between the input and output on the PCB, which benefits the achievement of high closed-loop bandwidth.

The DFN2X2-8 package with a wettable flank is provided for TPH2865Q, making the solder yield easy to check during the SMT.

Typical Application Circuit

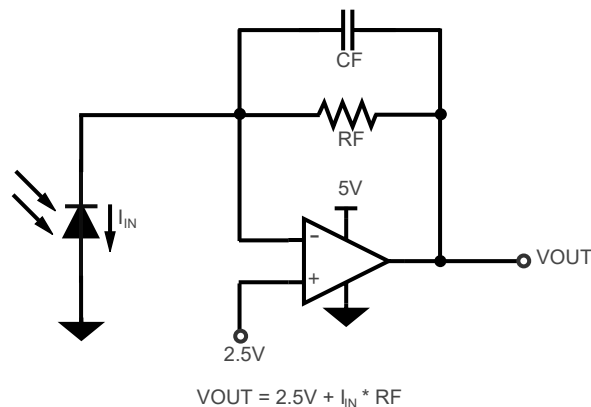


Table of Contents

Features	1
Applications	1
Description	1
Typical Application Circuit	1
Revision History	3
Pin Configuration and Functions	4
Specifications	5
Absolute Maximum Ratings ⁽¹⁾	5
ESD, Electrostatic Discharge Protection.....	5
Recommended Operating Conditions.....	5
Thermal Information.....	5
Electrical Characteristics.....	6
Electrical Characteristics (continued).....	7
Typical Performance Characteristics.....	9
Detailed Description	10
Overview.....	10
Functional Block Diagram.....	10
Feature Description.....	10
Application and Implementation	11
Application Information	11
Tape and Reel Information	12
Package Outline Dimensions	13
DFN2X2-8-WET-H.....	13
Order Information	14
IMPORTANT NOTICE AND DISCLAIMER	15

Revision History

Date	Revision	Notes
2024-05-22	Rev.A.0	Initial version.
2024-07-05	Rev.A.1	<p>The following updates are all about the typo, the actual product remains unchanged.</p> <p>In Absolute Maximum Ratings:</p> <p>Changed the value of Differential Input Voltage from min: $(-V_S) - (+V_S)$, max: $(+V_S) - (-V_S)$ to min: , max: 1.</p> <p>Changed the value of Input Current: $+I_N$, $-I_N$ from min: -1 , max: +1 to min: -10 , max: +10.</p> <p>Changed the value of Output Short-Circuit Duration from min: , max: Infinite to min: -100 , max: +100.</p>

Pin Configuration and Functions

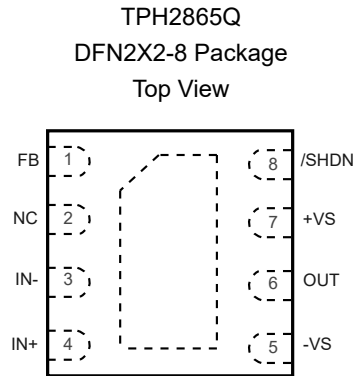


Table 1. Pin Functions: TPH2865Q

Pin No.	Name	I/O	Description
1	FB	Input	Feedback connection to the output of amplifier
2	NC		No connection
3	IN-	Input	Inverting input
4	IN+	Input	Noninverting input
5	-VS		Negative power supply
6	OUT	Output	Output
7	+VS		Positive power supply
8	/SHDN	Input	Shut down input, the device is shut down when the low-level input voltage is on the input; the device is active when the high-level input voltage is on the input. The device is active by default with an internal pull-up resistor.
Thermal pad			Connect the thermal pad to -VS

Specifications

Absolute Maximum Ratings ⁽¹⁾

Parameter		Min	Max	Unit
	Supply Voltage, (+V _S) – (–V _S)		5.5	V
	Input Voltage	(–V _S) – 0.3	(+V _S) + 0.3	V
	Differential Input Voltage		1	V
	Input Current: +IN, –IN ⁽²⁾	–10	+10	mA
	Output Short-Circuit Duration ⁽³⁾	–100	+100	mA
T _J	Maximum Junction Temperature		150	°C
T _A	Operating Temperature Range	–40	125	°C
T _{STG}	Storage Temperature Range	–65	150	°C
T _L	Lead Temperature (Soldering 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

(2) The inputs are protected by ESD protection diodes to the power supply. If the input extends more than 300 mV beyond the power supply, the input current should be limited to less than 10 mA.

(3) A heat sink may be required to keep the junction temperature below the absolute maximum rating. This depends on the power dissipation of the application. Thermal resistance varies with the amount of PC board metal connected to the package.

ESD, Electrostatic Discharge Protection

Parameter		Condition	Level	Unit
HBM	Human Body Model ESD	AEC-Q100-002	2	kV
CDM	Charged Device Model ESD	AEC-Q100-011	1.5	kV

Recommended Operating Conditions

Parameter		Min	Typ	Max	Unit
V _S	Supply Voltage, (+V _S) – (–V _S)	3.3 (±1.65)		5.25 (±2.625)	V
T _A	Operating Temperature Range	–40		125	°C

Thermal Information

Package Type	θ _{JA}	θ _{JC(TOP)}	Unit
DFN2X2-8	72	114	°C/W

Electrical Characteristics

All test conditions: $V_S = 5\text{ V}$, $V_{CM} = 2.5\text{ V}$, $T_A = 25^\circ\text{C}$, $G = 7\text{ V/V}$, input common-mode biased at mid-supply, unless otherwise noted.

Parameter		Conditions	Min	Typ	Max	Unit
Power Supply						
V _S	Supply Voltage Range		3.3		5.25	V
I _Q	Quiescent Current per Amplifier			9.8	11	mA
		T _A = −40°C to 125°C			13	mA
PSRR+	Positive Power-supply Rejection Ratio		80	87		dB
		T _A = −40°C to 125°C	72			dB
PSRR-	Negative Power-supply Rejection Ratio		64	69		dB
		T _A = −40°C to 125°C	60			dB
Input Characteristics						
V _{OS}	Input Offset Voltage		-1.8	0.2	1.8	mV
		T _A = −40°C to 125°C	-3		3	mV
V _{OS} TC	Input Offset Voltage Drift	T _A = −40°C to 125°C		2		μV/°C
I _B	Input Bias Current		-18	-11	-2	μA
I _{OS}	Input Offset Current		-4	1	4	μA
C _{IN}	Input Capacitance	Differential Mode		0.5		pF
		Common Mode		0.6		pF
R _{IN}	Input Resistance	Differential Mode		4		kΩ
		Common Mode		0.3		MΩ
A _v	Open-Loop Voltage Gain		64	68		dB
V _{IH}	Common-mode input range (high)		4.4	4.6		V
		T _A = −40°C to 125°C		4.3		V
V _{IL}	Common-mode input range (Low)			1.1	1.3	V
		T _A = −40°C to 125°C		1.3		V
CMRR	Common Mode Rejection Ratio	V _{CM} = ±0.5 V referred to midsupply	75	110		dB

5-V, G=7 Stable, 4-GHz, High-speed Op Amp
Electrical Characteristics (continued)

All test conditions: $V_S = 5\text{ V}$, $T_A = 25^\circ\text{C}$, $R_L = 10\text{ k}\Omega$, unless otherwise noted.

Parameter		Conditions	Min	Typ	Max	Unit
Output Characteristics						
	Output Voltage Swing from Positive Rail	I _{OUT} = 10 mA		0.9	1.1	V
		I _{OUT} = 10 mA, T _A = −40°C to 125°C			1.2	V
		V _S = 3.3 V, I _{OUT} = 10 mA		0.9	1.1	V
		V _S = 3.3 V, I _{OUT} = 10 mA, T _A = −40°C to 125°C			1.2	V
	Output Voltage Swing from Negative Rail	I _{OUT} = 10 mA		1.05	1.15	V
		I _{OUT} = 10 mA, T _A = −40°C to 125°C			1.2	V
		V _S = 3.3 V, I _{OUT} = 10 mA		1.05	1.15	V
		V _S = 3.3 V, I _{OUT} = 10 mA, T _A = −40°C to 125°C			1.2	V
I _{SC}	Output Short-Circuit Current	V _S = 5 V, Source	27	50		mA
		V _S = 5 V, Sink	40	94		mA
AC Specifications						
SSBW	Small-signal Bandwidth	V _{OUT} = 100 mV _{PP}		1.15		GHz
LSBW	Large-signal Bandwidth	V _{OUT} = 2 V _{PP}		433		MHz
GBW	Gain–Bandwidth Product			4		GHz
SR	Slew Rate	V _{OUT} = 3 V step		1000		V/μs
t _{OR}	Overload Recovery	2x output overdrive		4.8		ns
t _S	Settling Time, 0.1%			4.6		ns
	Settling Time, 0.001%			2600		ns
Noise Performance						
e _N	Input Voltage Noise Density	f = 1 MHz, V _{CM} = 1 V		1.28		nV/√Hz
i _N	Input Current Noise	f = 1 MHz		2.5		pA/√Hz
HD2	Second-order Harmonic Distortion	f = 10 MHz, V _{OUT} = 2 V _{PP}		83		dBc
		f = 100 MHz, V _{OUT} = 2 V _{PP}		55		dBc
HD3	Third-order Harmonic Distortion	f = 10 MHz, V _{OUT} = 2 V _{PP}		86		dBc
		f = 100 MHz, V _{OUT} = 2 V _{PP}		72		dBc
PD Performance						
	Disable Voltage Threshold	Amplifier OFF below this voltage	0.8	0.9		V
		Amplifier OFF below this voltage, T _A = −40°C to 125°C	0.7			V
	Enable Voltage Threshold	Amplifier ON above this voltage		1.1	1.2	V

5-V, G=7 Stable, 4-GHz, High-speed Op Amp

Parameter		Conditions	Min	Typ	Max	Unit
		Amplifier ON above this voltage, $T_A = -40^{\circ}\text{C}$ to 125°C			1.3	V
	Power-down Quiescent Current			224	255	μA
	Input PD bias Current			67	77	μA
		$T_A = -40^{\circ}\text{C}$ to 125°C			82	μA
	Turn-on Time Delay	Time to $V_{\text{OUT}} = 90\%$ of final value		17		ns
	Turn-off Time Delay			86		ns

Typical Performance Characteristics

All test conditions: $T_A = 25^\circ\text{C}$, $V_{S+} = 2.5\text{ V}$, $V_{S-} = -2.5\text{ V}$, $V_{IN+} = 0\text{ V}$, $R_F = 453\ \Omega$, Gain = 7 V/V, $R_L = 200\ \Omega$, and output load referenced to midsupply, unless otherwise noted.

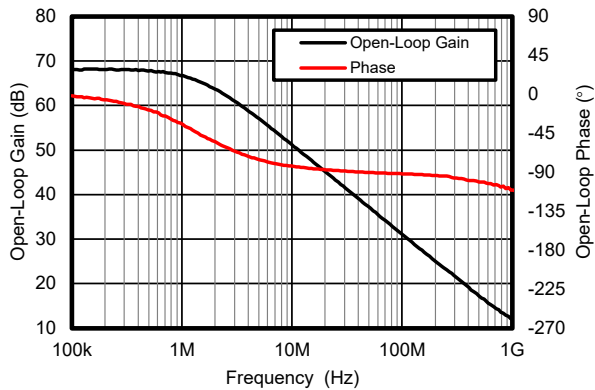


Figure 1. Open Loop Gain and Phase vs Frequency

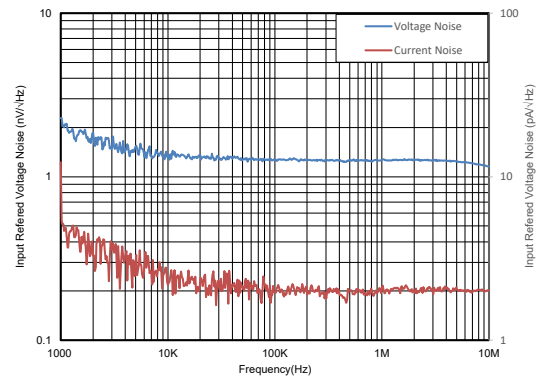


Figure 2. Voltage and Current Noise Density vs Frequency

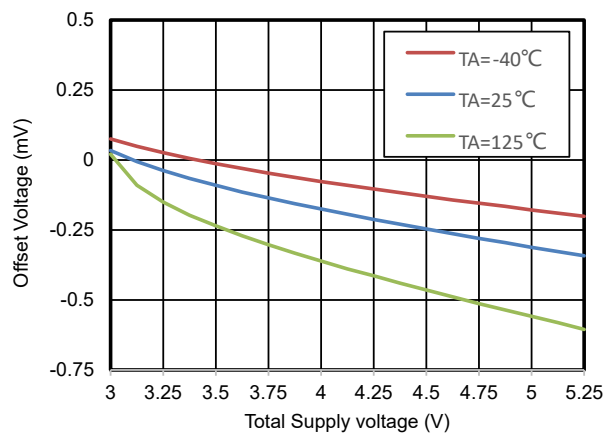


Figure 3. Offset Voltage vs Input Common-Mode Voltage

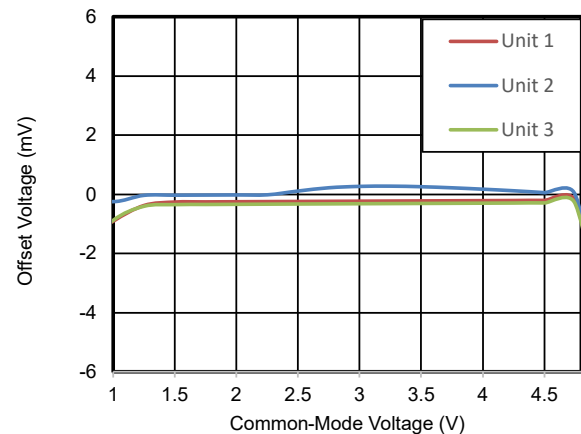


Figure 4. Offset Voltage vs Input Common-Mode Voltage

Detailed Description

Overview

The TPH2865Q is a BJT, high-speed, voltage-feedback operational amplifier designed for high-speed pulse, high-speed data acquisition systems and other applications. It is available as a single op amp. The amplifier features a 4-GHz gain bandwidth, 1000-V/ μ s slew rate and broad voltage noise of 1.28 nV / $\sqrt{\text{Hz}}$, but it is not unity-gain stable and can be stable when the gain is larger than 7 V/V. The TPH2865Q power-supply range of +3.3 V to +5.25 V (± 1.65 V to ± 2.625 V).

Functional Block Diagram

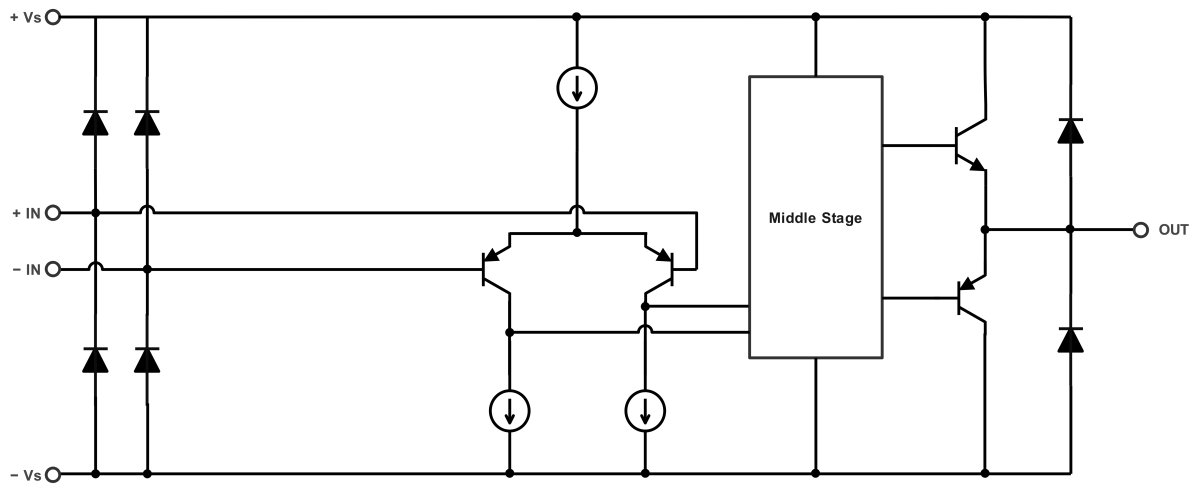


Figure 5. Functional Block Diagram

Feature Description

Operating Voltage

The TPH2865Q is designed for single supply operation from 3.3 V to 5.25 V or dual supply operation from ± 1.65 V to ± 2.625 V.

Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

Transimpedance Amplifier Application

Figure 6 shows the device is configured in a transimpedance amplifier application. In the circuit shown in the following figure, the current source (shown as a photodiode) is connected between ground and the inverting input of the op-amp, the other input of the op-amp is connected to 2.5 V. The current of the photodiode is equal to the feedback current through R_F due to the high gain of the op-amp. The DC gain of a transimpedance amplifier is determined by the equation shown in Figure 6. The C_F is used to maintain the stability of the whole circuit.

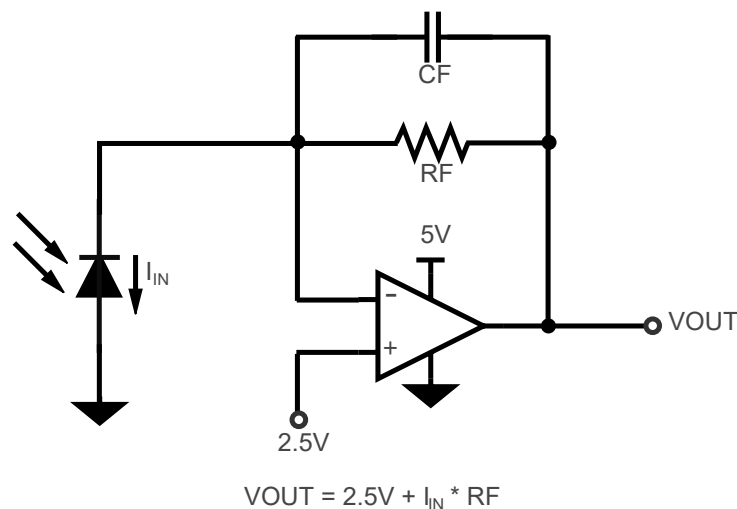
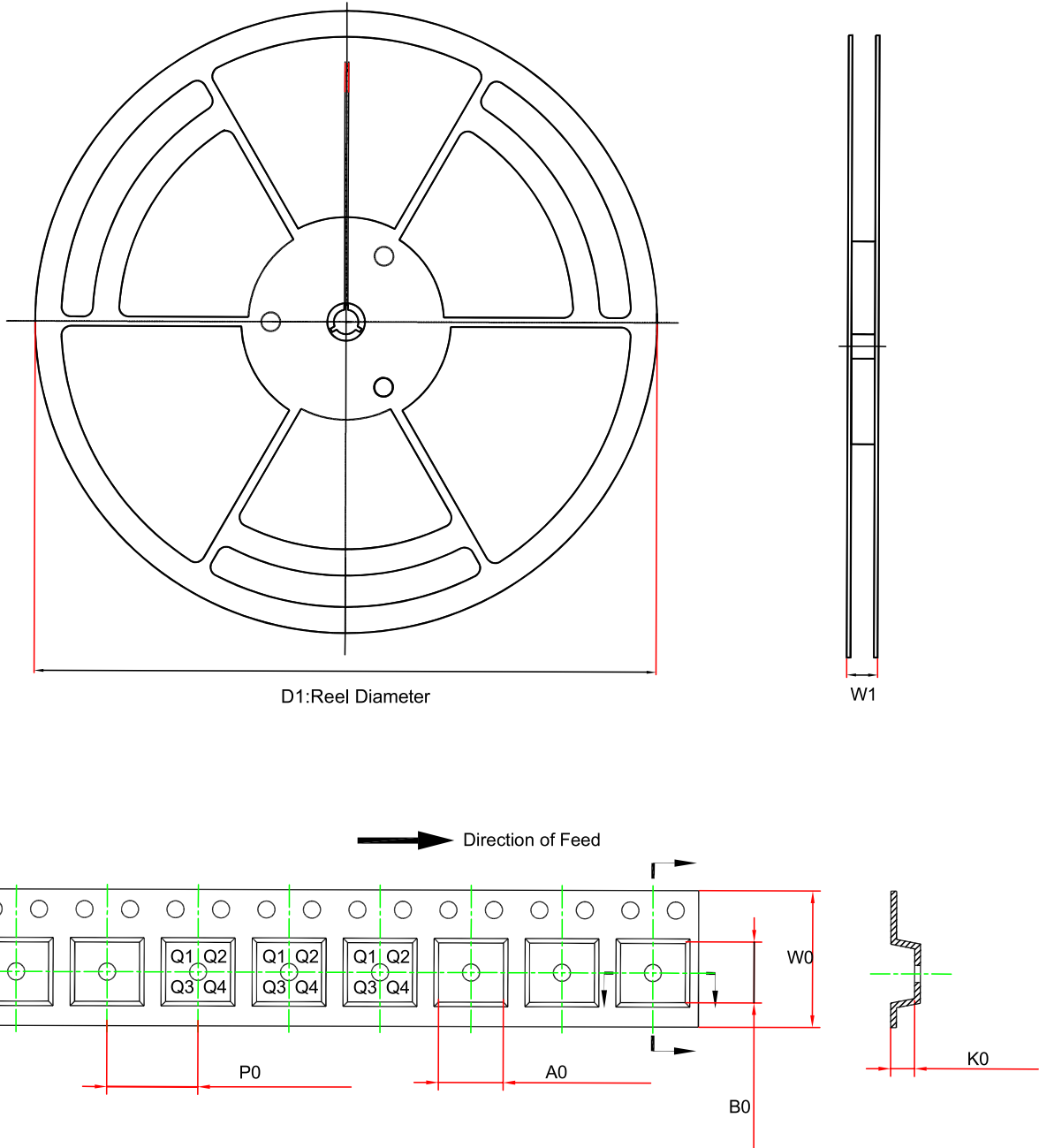


Figure 6. TIA(transimpedance amplifier) Application

Power Supply Recommendations

Place 0.1- μ F bypass capacitors close to the power supply pins to reduce coupling errors from the noisy or high-impedance power supplies.

Tape and Reel Information



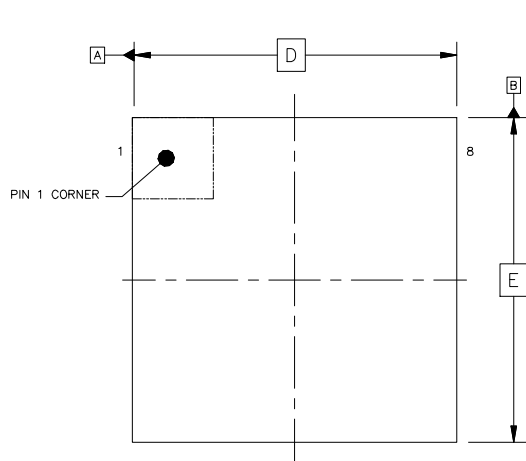
Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPH2865Q-DFVR-S	DFN2X2-8	180	12.5	2.3	2.3	1.1	4	8	Q2

Package Outline Dimensions

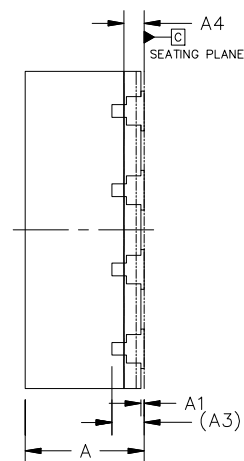
DFN2X2-8-WET-H

Package Outline Dimensions

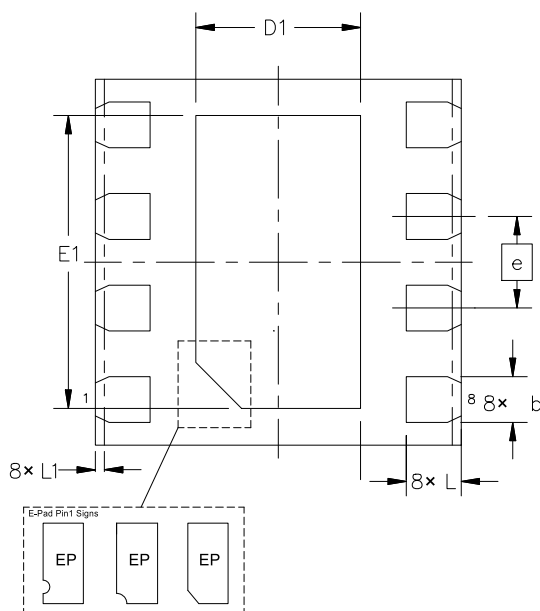
DFV(DFN2X2-8-WET-H)



Top View



Side View



Bottom View

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.
3. The many types of E-pad Pin1 signs may appear in the product.

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203 REF		0.008 REF	
A4	0.080	0.180	0.003	0.007
b	0.200	0.300	0.008	0.012
D	2.000 BSC		0.079 BSC	
D1	0.800	1.000	0.031	0.039
E	2.000 BSC		0.079 BSC	
E1	1.500	1.700	0.059	0.067
e	0.500 BSC		0.020 BSC	
L	0.250	0.350	0.010	0.014
L1	0.010	0.090	0.000	0.004

Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPH2865Q-DFVR-S	-40 to 125°C	DFN2X2-8	A30	MSL2	Tape and Reel,3000	Green

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

IMPORTANT NOTICE AND DISCLAIMER

Copyright© 3PEAK 2012-2024. All rights reserved.

Trademarks. Any of the 思瑞浦 or 3PEAK trade names, trademarks, graphic marks, and domain names contained in this document /material are the property of 3PEAK. You may NOT reproduce, modify, publish, transmit or distribute any Trademark without the prior written consent of 3PEAK.

Performance Information. Performance tests or performance range contained in this document/material are either results of design simulation or actual tests conducted under designated testing environment. Any variation in testing environment or simulation environment, including but not limited to testing method, testing process or testing temperature, may affect actual performance of the product.

Disclaimer. 3PEAK provides technical and reliability data (including data sheets), design resources (including reference designs), application or other design recommendations, networking tools, security information and other resources "As Is". 3PEAK makes no warranty as to the absence of defects, and makes no warranties of any kind, express or implied, including without limitation, implied warranties as to merchantability, fitness for a particular purpose or non-infringement of any third-party's intellectual property rights. Unless otherwise specified in writing, products supplied by 3PEAK are not designed to be used in any life-threatening scenarios, including critical medical applications, automotive safety-critical systems, aviation, aerospace, or any situations where failure could result in bodily harm, loss of life, or significant property damage. 3PEAK disclaims all liability for any such unauthorized use.

This page intentionally left blank