



700V Super-junction Power MOSFET

Description

700V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The Multi-EPI SJ MOSFET provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

Features

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant

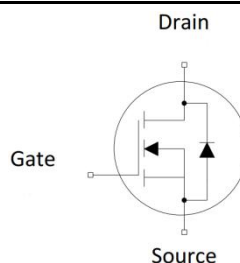
Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger

TO-220F



TO-220FP-NL



Device Marking and Package Information

Device	Package	Marking
TPA70R260M	TO-220F	70R260M
TPR70R260M	TO-220FP-NL	70R260M

Key Performance Parameters

Parameter	Value	Unit
$V_{DS} @ T_{j,max}$	750	V
$R_{DS(on),max}$	0.26	Ω
$Q_{g,typ}$	27	nC
I_D	15	A
$I_{D,pulse}$	45	A
$E_{OSS} @ 400V$	3.47	μJ
Body Diode di_F/dt	500	A/ μs

**Absolute Maximum Ratings** $T_C = 25^{\circ}\text{C}$, unless otherwise noted

Parameter		Symbol	Value	Unit
Continuous Drain Current	$T_C = 25^{\circ}\text{C}$	I_D	15	A
	$T_C = 100^{\circ}\text{C}$		9	
Pulsed Drain Current	(note1)	$I_{D,pulse}$	45	A
Gate-Source Voltage		V_{GSS}	± 30	V
Single Pulse Avalanche Energy	(note2)	E_{AS}	290	mJ
Repetitive Avalanche Energy	(note2)	E_{AR}	0.44	mJ
Avalanche Current		I_{AR}	2.4	A
MOSFET dv/dt Ruggedness, $V_{DS} = 0 \dots 480\text{V}$		dv/dt	50	V/ns
Power Dissipation For TO-220F,TO-220FP-NL		P_D	36	W
Continuous Diode Forward Current		I_S	12.8	A
Diode Pulsed Current	(note1)	$I_{S,pulse}$	45	
Reverse Diode dv/dt	(note3)	dv/dt	15	V/ns
Maximum Diode Commutation Speed	(note3)	di/dt	500	A/ μs
Operating Junction and Storage Temperature Range		T_J, T_{stg}	$-55 \sim +150$	$^{\circ}\text{C}$

Thermal Resistance For TO-220F,TO-220FP-NL

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	3.5	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	80	



Electrical Characteristics T _J = 25°C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	700	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 700V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
		V _{DS} = 700V, V _{GS} = 0V, T _J = 150°C	--	--	100	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.5	--	4.5	V
Drain-Source On-State-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 7.5A	--	0.24	0.26	Ω
Gate Resistance	R _G	f = 1.0MHz open drain	--	12.5	--	Ω
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 100V, f = 1.0MHz	--	1122	--	pF
Output Capacitance	C _{oss}		--	38	--	
Reverse Transfer Capacitance	C _{rss}		--	2.5	--	
Total Gate Charge	Q _g	V _{DD} = 520V, I _D = 15A, V _{GS} = 10V	--	27	--	nC
Gate-Source Charge	Q _{gs}		--	5.5	--	
Gate-Drain Charge	Q _{gd}		--	10.5	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 400V, I _D = 15A, R _G = 25Ω	--	25	--	ns
Turn-on Rise Time	t _r		--	63	--	
Turn-off Delay Time	t _{d(off)}		--	100	--	
Turn-off Fall Time	t _f		--	50	--	
Drain-Source Body Diode Characteristics						
Body Diode Forward Voltage	V _{SD}	T _J = 25°C, I _{SD} = 15A, V _{GS} = 0V	--	0.9	1.2	V
Reverse Recovery Time	t _{rr}	V _R = 400V, I _F = I _S , di _F /dt = 100A/μs	--	410	--	ns
Reverse Recovery Charge	Q _{rr}		--	4.1	--	μC
Peak Reverse Recovery Current	I _{rrm}		--	20	--	A

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = 2.4A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$
3. Identical low side and high side switch with identical R_G



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

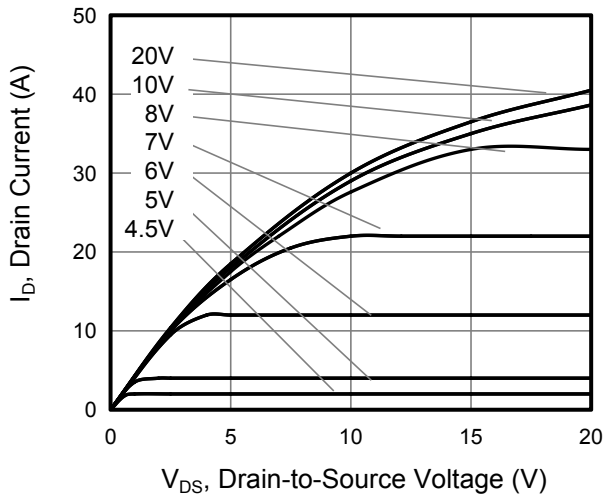


Figure 2. Transfer Characteristics

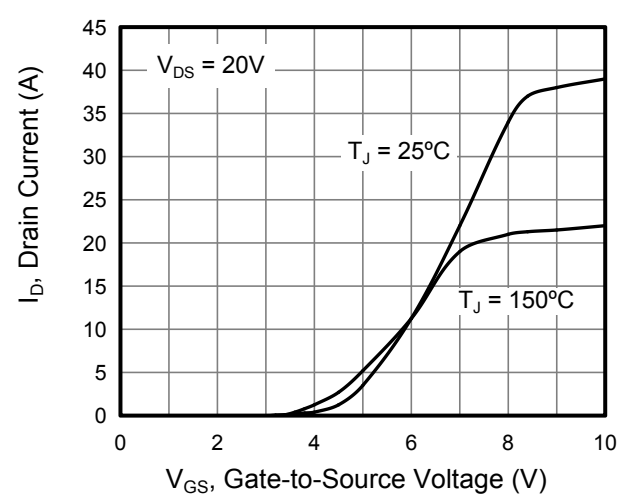


Figure 3. On-Resistance vs. Drain Current

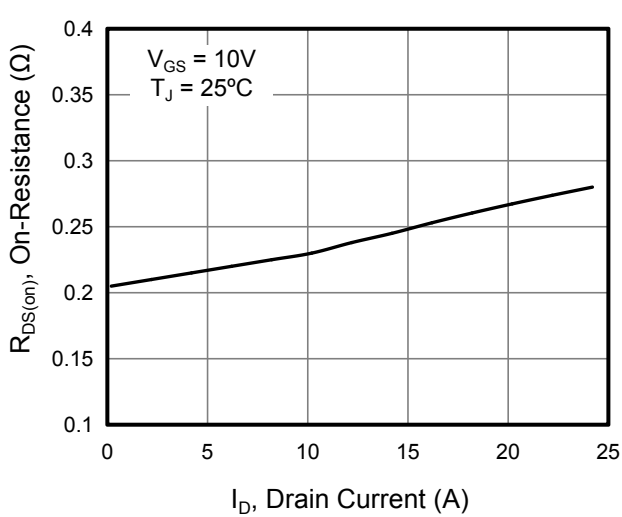


Figure 4. Capacitance

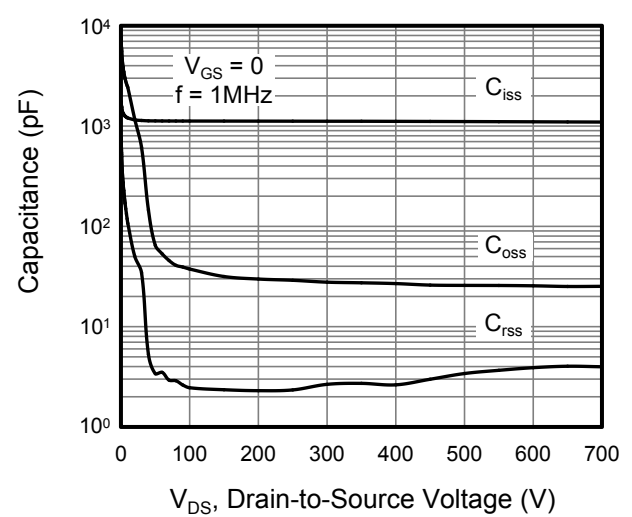


Figure 5. Gate Charge

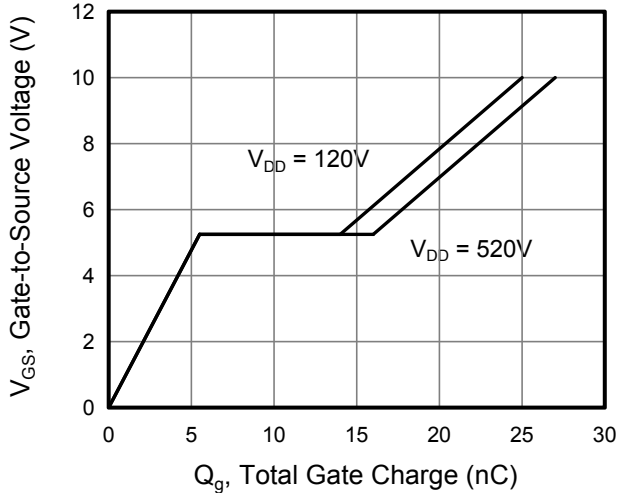
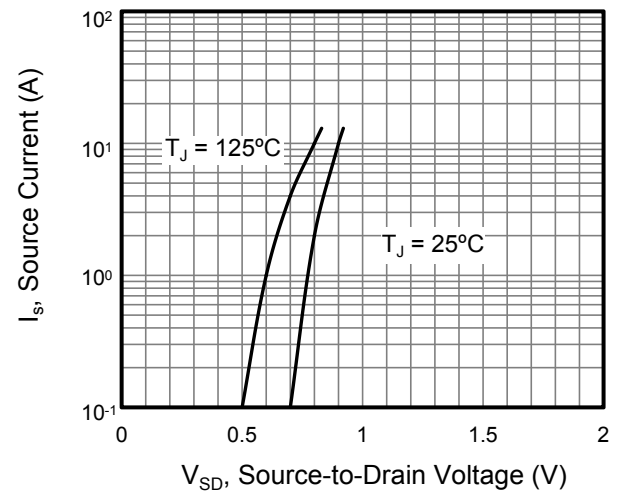


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. On-Resistance vs. Temperature

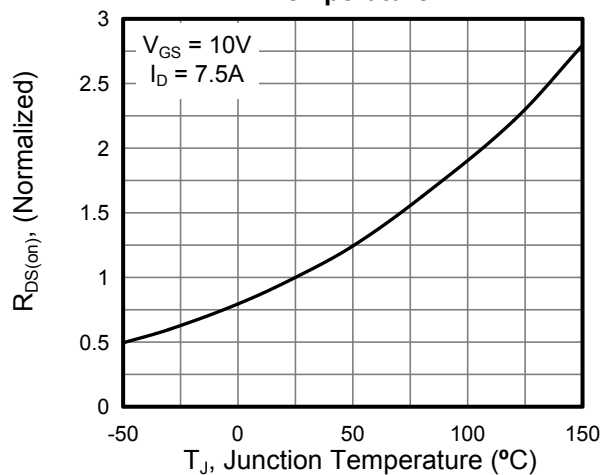


Figure 8. Breakdown voltage vs. Junction Temperature

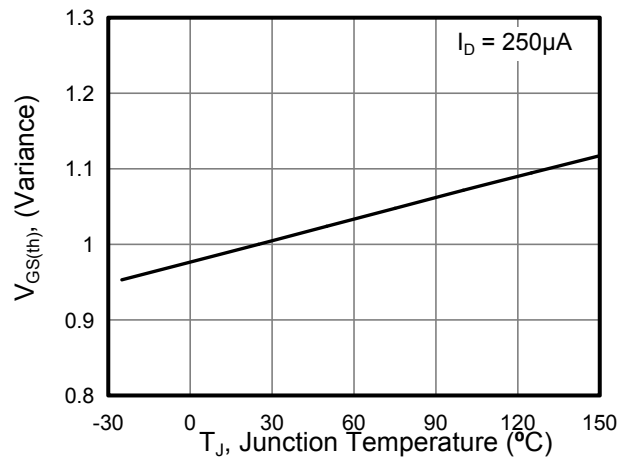


Figure 9. Transient Thermal Impedance For TO-220F/TO-220FP-NL

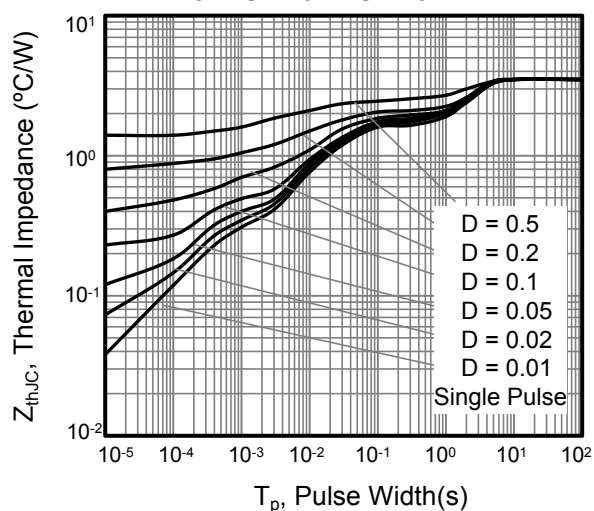


Figure 10. Safe Operation Area For TO-220F/TO-220FP-NL

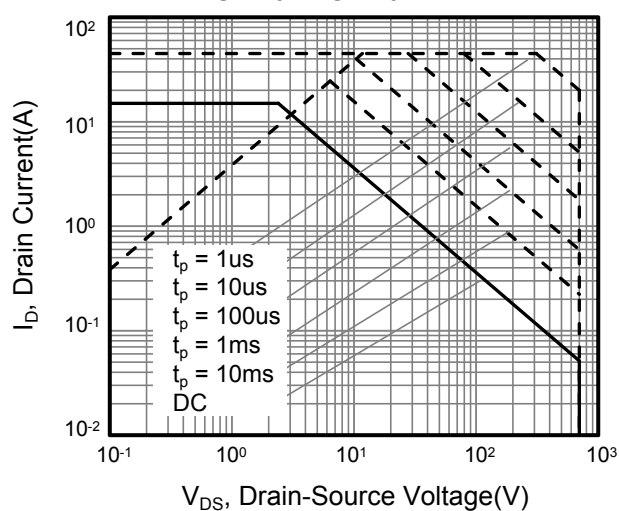


Figure 11. Typ. Coss Stored Energy

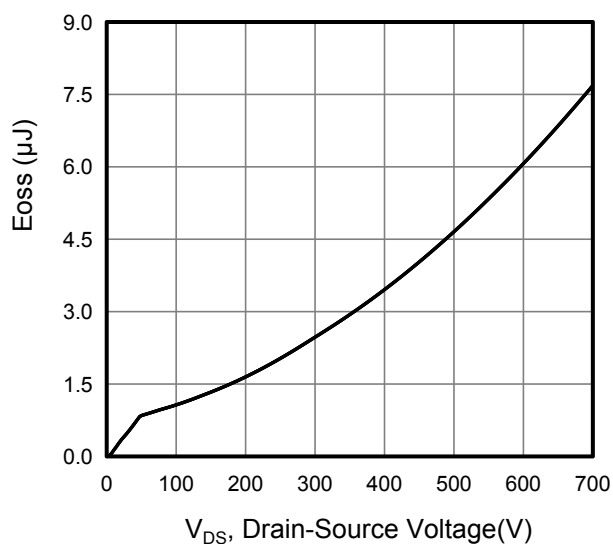




Figure A: Gate Charge Test Circuit and Waveform

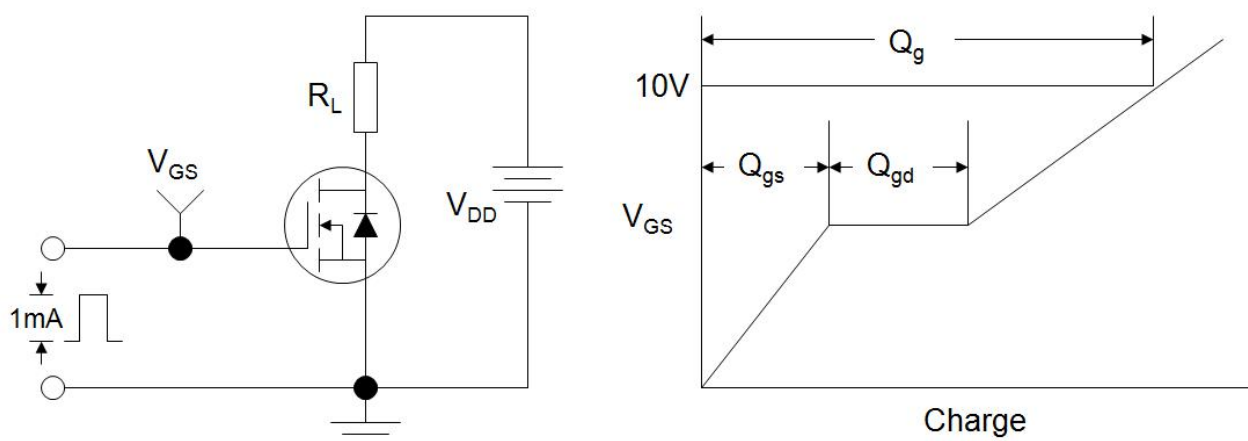


Figure B: Resistive Switching Test Circuit and Waveform

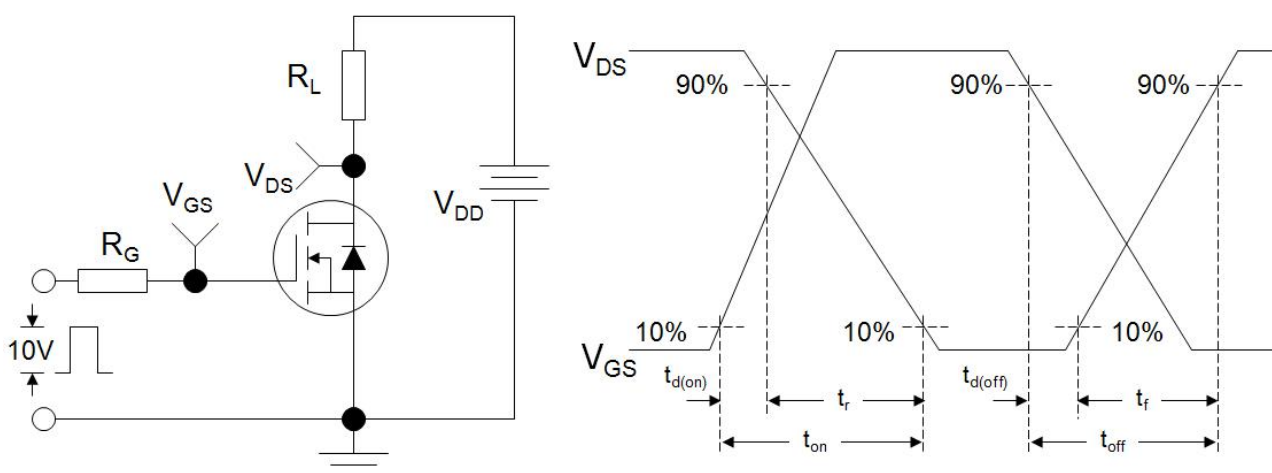
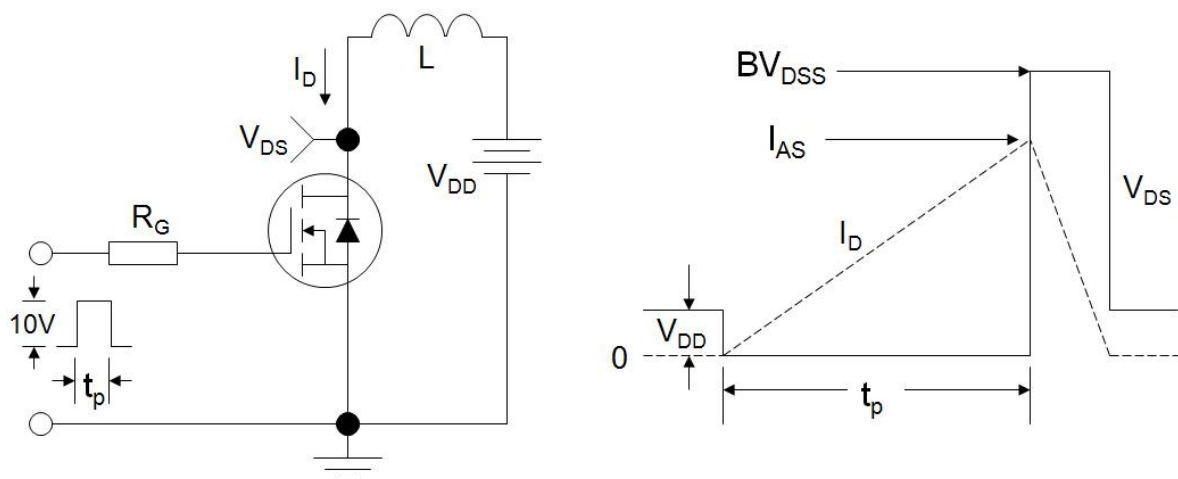
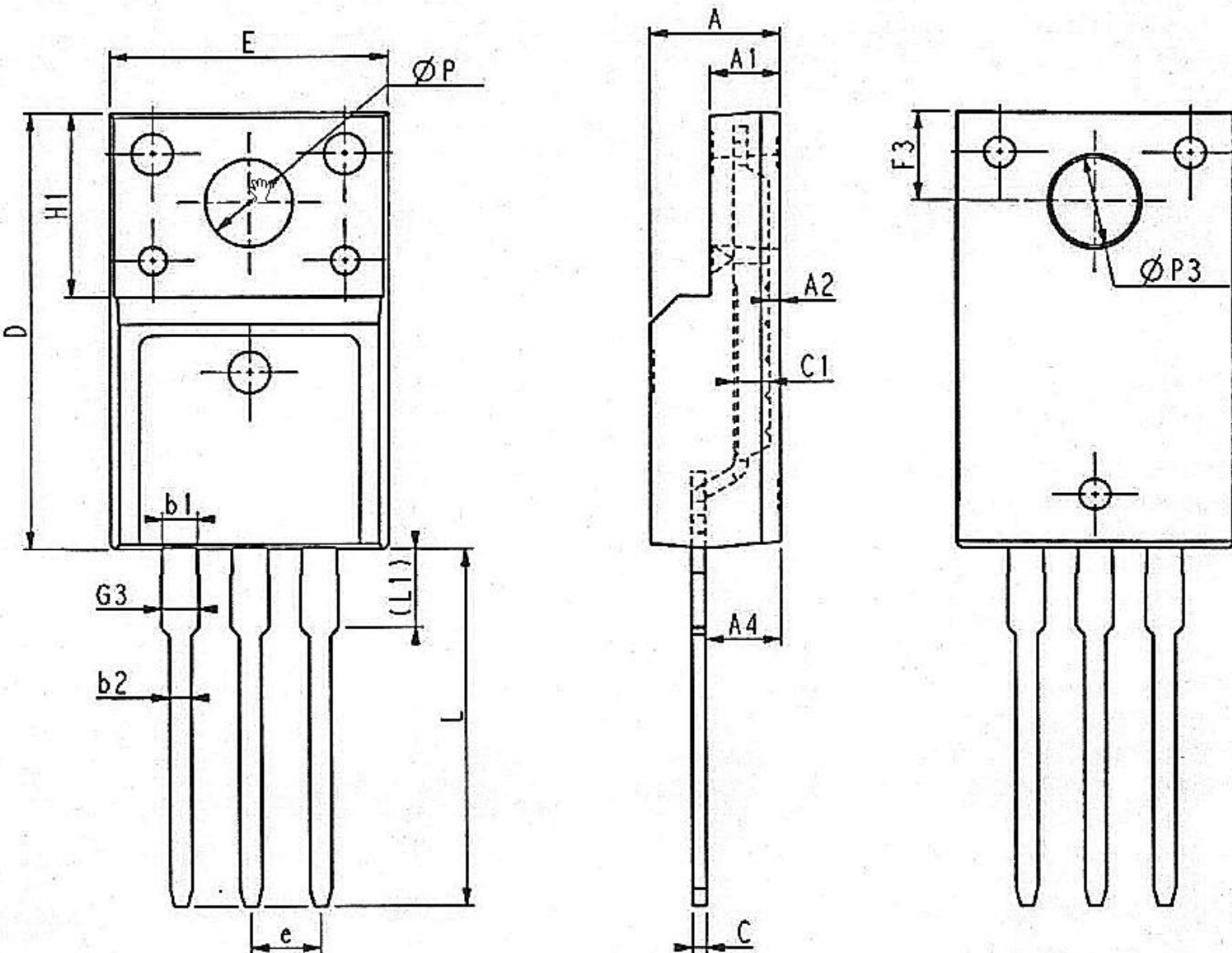


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F (华羿)

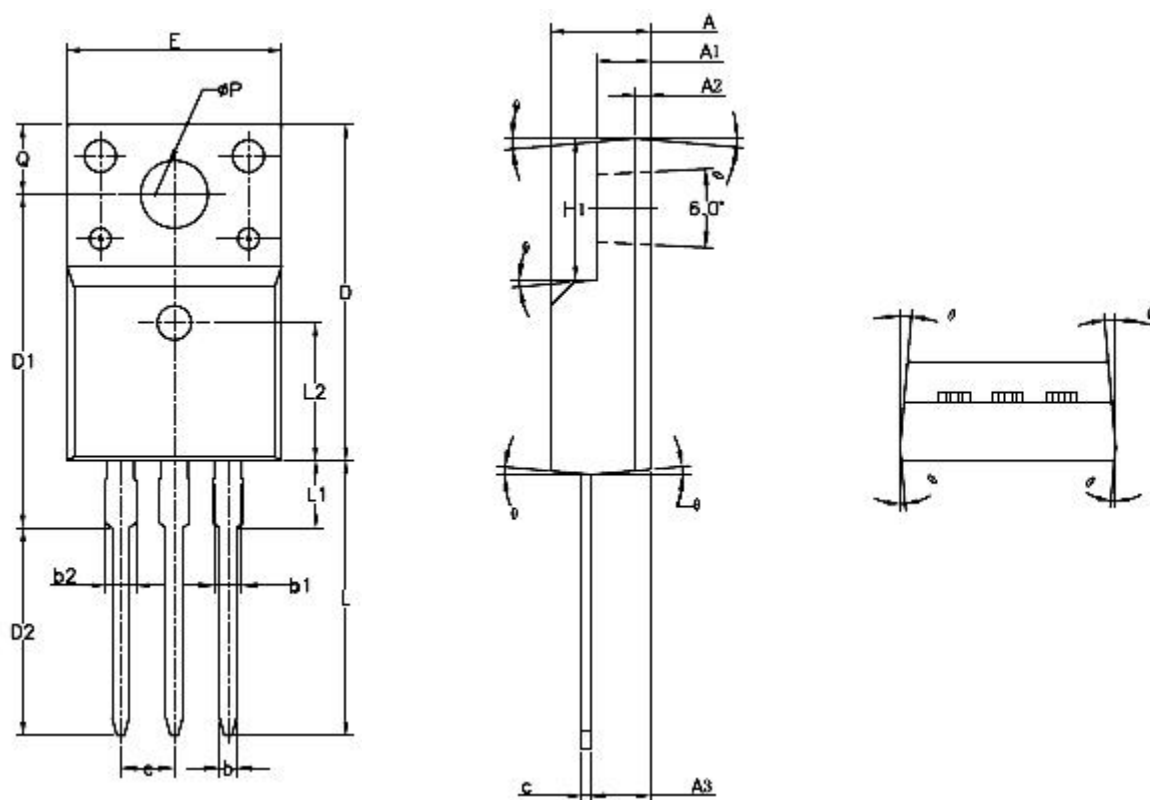


Unit:mm			
Symbol	Min.	Nom	Max.
E	9.96	10.16	10.36
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A2	0.30	0.45	0.60
A4	2.56	2.76	2.96
c	0.40	0.50	0.65
c1	1.20	1.30	1.35
D	15.57	15.87	16.17
H1	6.70REF		

Unit:mm			
Symbol	Min.	Nom	Max.
e	2.54BSC		
L	12.68	12.98	13.28
L1	2.88	3.03	3.18
ΦP	3.03	3.18	3.38
ΦP3	3.15	3.45	3.65
F3	3.15	3.30	3.45
G3	1.25	1.35	1.55
b1	1.18	1.28	1.43
b2	0.70	0.80	0.95



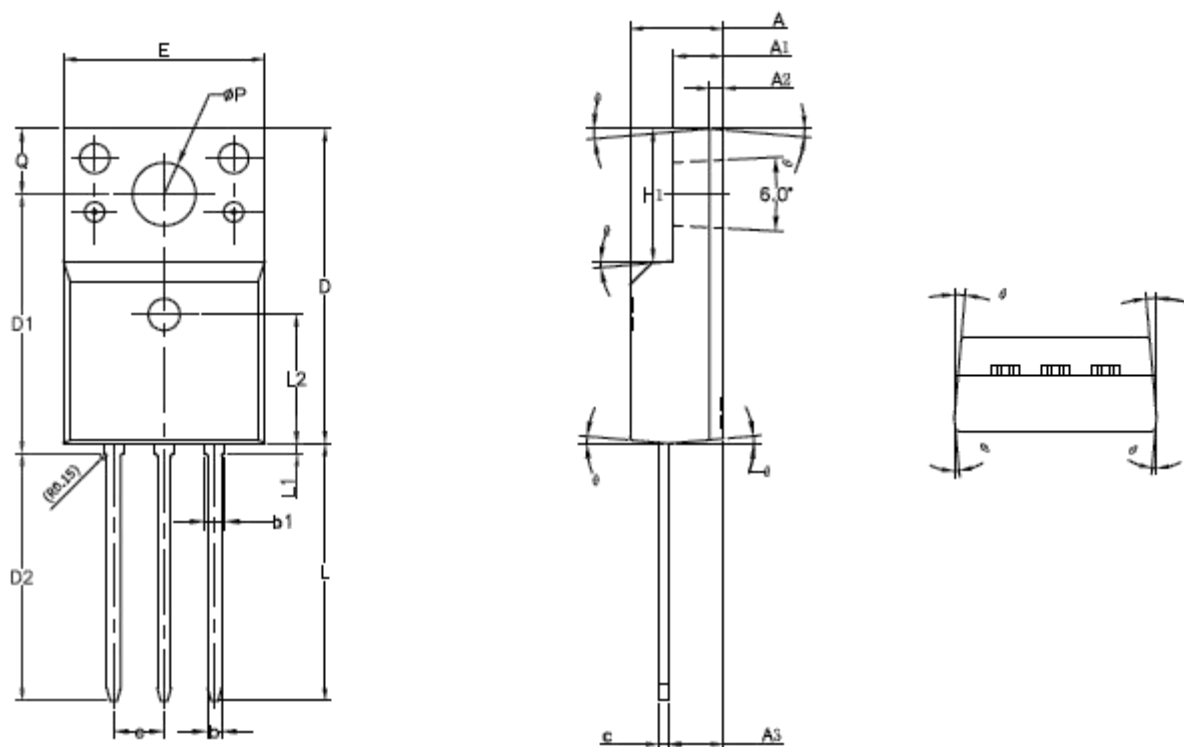
TO-220F (集佳)



SYMBOL	MIN	NOM	MAX
A	4.50	4.70	4.83
A1	2.34	2.54	2.74
A2	0.70 REF		
A3	2.56	2.76	2.93
b	0.70	—	0.90
b1	1.18	—	1.38
b2	—	—	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	9.60	9.80	10.0
E	9.96	10.16	10.36
e	2.54BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	—	—	3.50
L2	6.50REF		
øP	3.08	3.18	3.28
Q	3.20	—	3.40
θ1	1°	3°	5°



TO-220FP-NL (集佳)



SYMBOL	MIN	NOM	MAX
A	4.50	4.70	4.83
A1	2.34	2.54	2.74
A2	0.70 REF		
A3	2.56	2.76	2.93
b	0.60	—	0.80
b1	0.90	—	1.10
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	12.87	13.07	13.27
D2	12.28	12.48	12.68
E	9.96	10.16	10.36
e	2.54BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	—	—	0.85
L2	6.50REF		
ØP	3.08	3.18	3.28
Q	3.20	—	3.40
θ1	1°	3°	5°



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