

TR136D

High voltage fast-switching NPN power transistor

Preliminary Data

Features

- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- Integrated antiparallel collector-emitter diode

Applications

- Electronic ballast for fluorescent lighting
- Electronic transformer for halogen lamps

Description

The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining a satisfactory RBSOA.

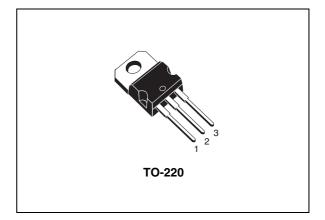


Figure 1. Internal schematic diagrams

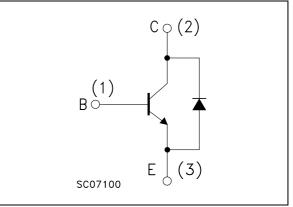


Table 1. Device summary

Order code	Marking	Package	Packing
TR136D	TR136D	TO-220	Tube

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1 Electrical ratings

Та	ble	2.	
	Nº I C	_	

2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage ($V_{BE} = 0$)	700	V
V _{CEO}	Collector-emitter voltage $(I_B = 0)$	400	V
V _{EBO}	Emitter-base voltage ($I_{\rm C} = 0$)	9	V
Ι _C	Collector current	3	А
I _{CM}	Collector peak current (t _P < 5ms)	6	А
Ι _Β	Base current	1.5	Α
I _{BM}	Base peak current (t _P < 5ms)	3	Α
P _{tot}	Total dissipation at $T_c \simeq 25^{\circ}C$	60	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C



ww.DataSheet4U **Electrical characteristics**

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 3.	Electrical characteristics
Table 5.	

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CEV}	Collector cut-off current (V _{BE} =-1.5V)	V _{CE} =700 V V _{CE} =700 V T _C = 100 °	°C		1 5	mA mA
I _{EBO}	Emitter cut-off current (I _C =0)	V _{EB} =9 V			1	mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage $(I_B = 0)$	I _C =10mA	400			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage		۱A		0.5 0.7 1	V V V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	$I_{\rm C} = 1 \text{ A}$ $I_{\rm B} = 0.2$ $I_{\rm C} = 2 \text{ A}$ $I_{\rm B} = 0.5$			1.2 1.6	V V
h _{FE}	DC current gain	$I_{C} = 10 \text{ mA}$ $V_{CE} = 5$ $I_{C} = 2 \text{ A}$ $V_{CE} = 5$			20	
t _s t _f	Inductive load Storage time Fall time		Ω	0.8 0.16		μs μs
V _F	Diode forward voltage	I _F = 1 A			2.5	V

1. Pulsed duration = 300 ms, duty cycle ⊴.5%

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2.1 Test circuits

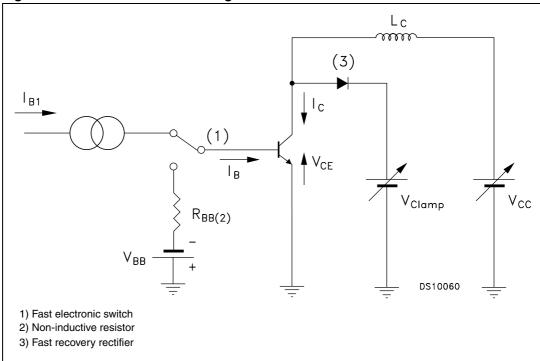


Figure 2. Inductive load switching test circuit



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Package mechanical data

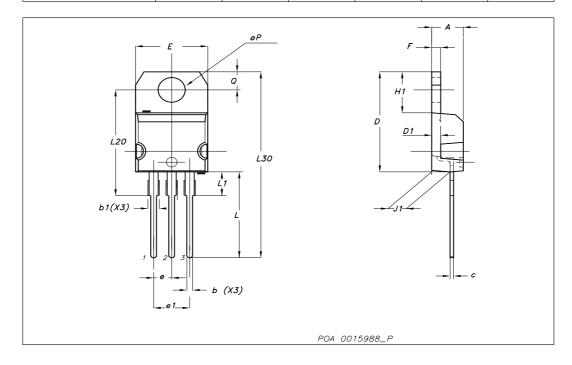
In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



Package mechanical data

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Dim		mm			inch	
Dilli	Min	Тур	Max	Min	Тур	Max
Α	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
С	0.49		0.70	0.019		0.027
D	15.25		15.75	0.6		0.62
D1		1.27			0.050	
E	10		10.40	0.393		0.409
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.051
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
ØP	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116

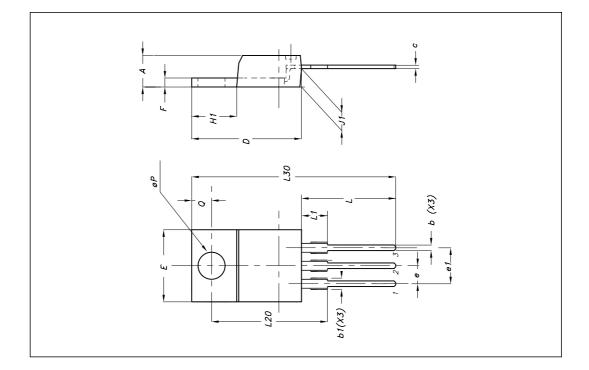




TR136D

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TO-220 Mechanical Data "Option 1"				
DIM.	mm.			
	MIN.	ТҮР	MAX.	
A	4.40		4.60	
b	0.61		0.88	
b1	1.15		1.70	
С	0.37		0.43	
D	15.25		15.75	
E	10		10.40	
е	2.40		2.70	
e1	4.95		5.15	
F	1.23		1.32	
H1	6.20		6.60	
J1	2.40		2.72	
L		12.87		
L1		3.25		
L20		16.40		
L30		28.90		
øP	3.75		3.85	
Q	2.65		2.95	





Revision history

Table 4.Document revision history

Date	Revision	Changes
08-Oct-2007	1	Initial release.



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